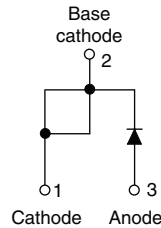


Fast Soft Recovery Rectifier Diode, 20 A



TO-220AC



FEATURES

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level



APPLICATIONS

- Output rectification and freewheeling in inverters, choppers and converters
- Input rectifications where severe restrictions on conducted EMI should be met

DESCRIPTION

The VS-20ETF..PbF fast soft recovery rectifier series has been optimized for combined short reverse recovery time and low forward voltage drop.

The glass passivation ensures stable reliable operation in the most severe temperature and power cycling conditions.

PRODUCT SUMMARY	
V_F at 20 A	< 1.31 V
I_{FSM}	355 A
V_{RRM}	800 to 1200 V

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
V_{RRM}		800 to 1200	V
$I_{F(AV)}$	Sinusoidal waveform	20	A
I_{FSM}		355	
t_{rr}	1 A, 100 A/ μ s	95	ns
V_F	20 A, $T_J = 25\text{ }^\circ\text{C}$	1.31	V
T_J	Range	- 40 to 150	$^\circ\text{C}$

VOLTAGE RATINGS			
PART NUMBER	V_{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V_{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I_{RRM} AT 150 $^\circ\text{C}$ mA
VS-20ETF08PbF	800	900	6
VS-20ETF10PbF	1000	1100	
VS-20ETF12PbF	1200	1300	

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	$I_{F(AV)}$	$T_C = 97\text{ }^\circ\text{C}$, 180° conduction half sine wave	20	A
Maximum peak one cycle non-repetitive surge current	I_{FSM}	10 ms sine pulse, rated V_{RRM} applied	300	
		10 ms sine pulse, no voltage reapplied	355	
Maximum I^2t for fusing	I^2t	10 ms sine pulse, rated V_{RRM} applied	450	A^2s
		10 ms sine pulse, no voltage reapplied	635	
Maximum $I^2\sqrt{t}$ for fusing	$I^2\sqrt{t}$	$t = 0.1\text{ ms to } 10\text{ ms}$, no voltage reapplied	6350	$\text{A}^2\sqrt{\text{s}}$

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V_{FM}	20 A, $T_J = 25\text{ }^\circ\text{C}$		1.31	V
Forward slope resistance	r_t	$T_J = 150\text{ }^\circ\text{C}$		11.88	$\text{m}\Omega$
Threshold voltage	$V_{F(TO)}$			0.93	V
Maximum reverse leakage current	I_{RM}	$T_J = 25\text{ }^\circ\text{C}$	$V_R = \text{Rated } V_{RRM}$	0.1	mA
		$T_J = 150\text{ }^\circ\text{C}$		6	

RECOVERY CHARACTERISTICS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Reverse recovery time	t_{rr}	I_F at 20 Apk	400	ns	
Reverse recovery current	I_{rr}	25 A/ μs	6.1	A	
Reverse recovery charge	Q_{rr}	25 $^\circ\text{C}$	1.7	μC	
Snap factor	S	Typical	0.6		

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		- 40 to 150	$^\circ\text{C}$
Maximum thermal resistance, junction to case	R_{thJC}	DC operation	0.9	$^\circ\text{C/W}$
Maximum thermal resistance, junction to ambient	R_{thJA}		62	
Typical thermal resistance, case to heatsink	R_{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum		6 (5)	kgf · cm (lbf · in)
	maximum		12 (10)	
Marking device		Case style TO-220AC	20ETF08	



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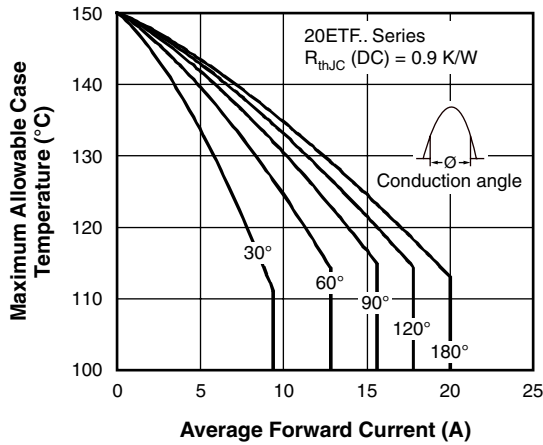


Fig. 1 - Current Rating Characteristics

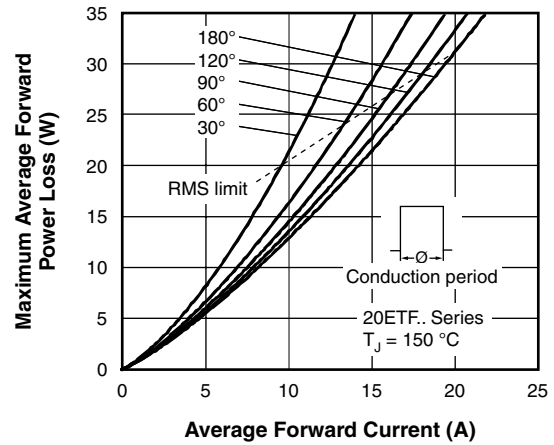


Fig. 4 - Forward Power Loss Characteristics

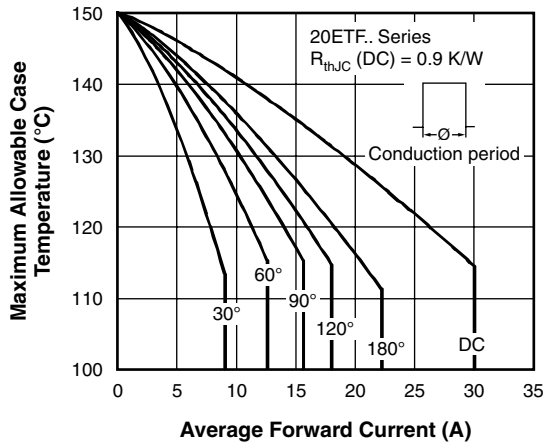


Fig. 2 - Current Rating Characteristics

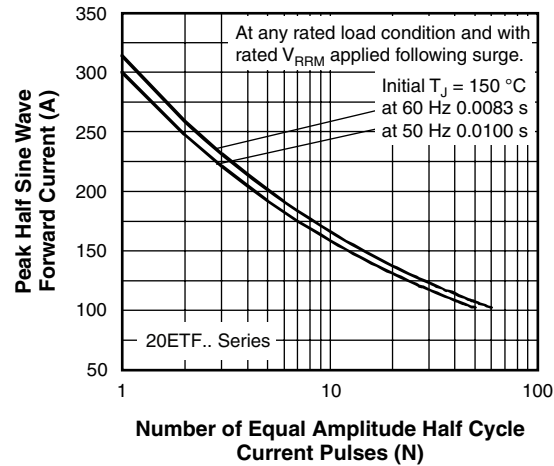


Fig. 5 - Maximum Non-Repetitive Surge Current

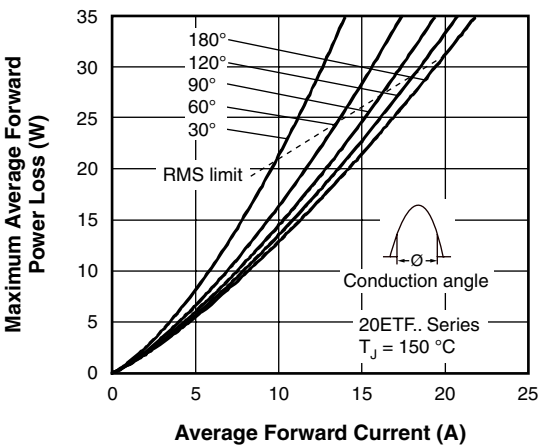


Fig. 3 - Forward Power Loss Characteristics

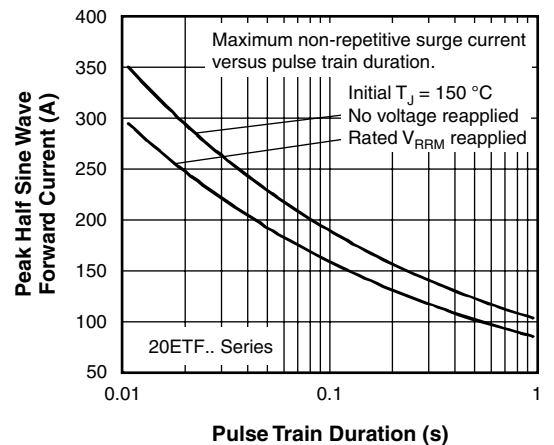


Fig. 6 - Maximum Non-Repetitive Surge Current

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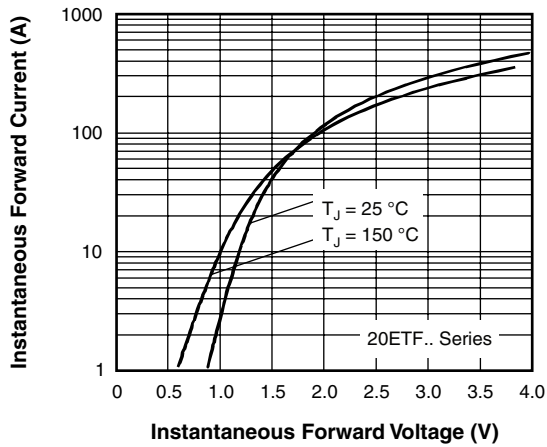


Fig. 7 - Forward Voltage Drop Characteristics

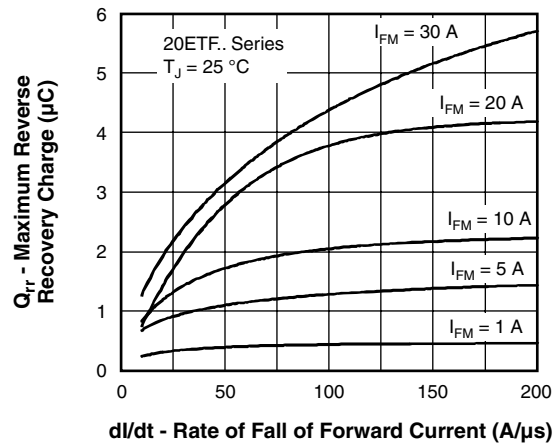


Fig. 10 - Recovery Charge Characteristics, $T_J = 25\text{ °C}$

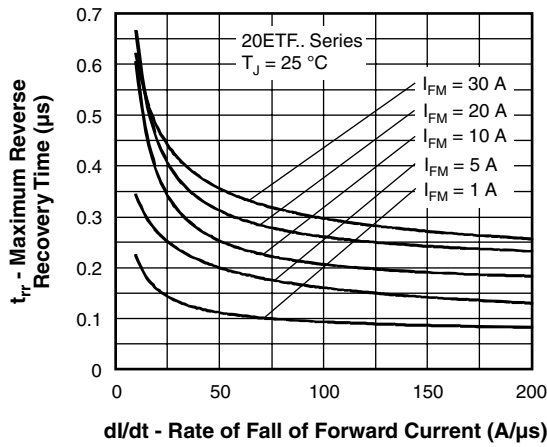


Fig. 8 - Recovery Time Characteristics, $T_J = 25\text{ °C}$

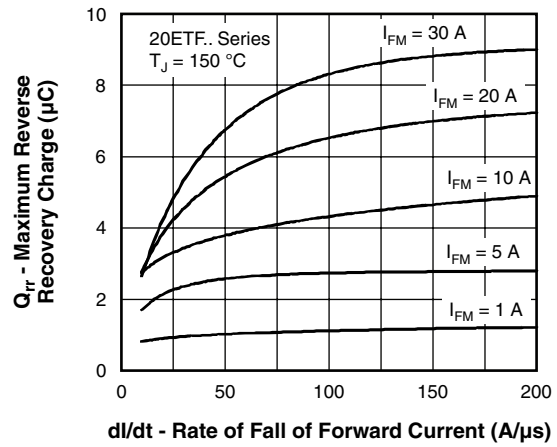


Fig. 11 - Recovery Charge Characteristics, $T_J = 150\text{ °C}$

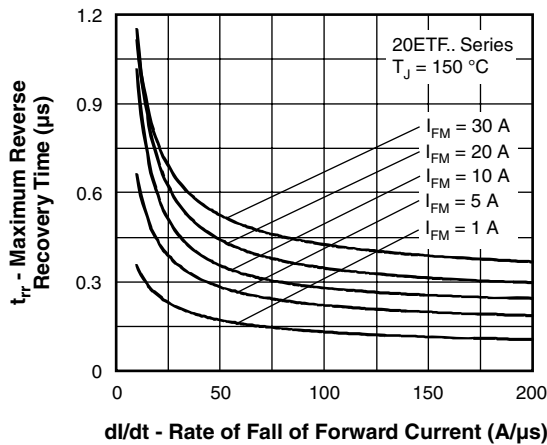


Fig. 9 - Recovery Time Characteristics, $T_J = 150\text{ °C}$

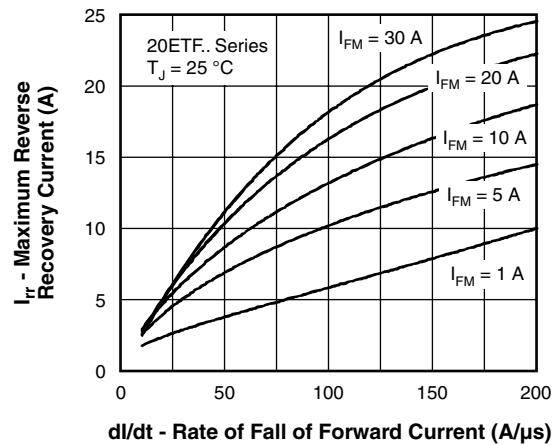


Fig. 12 - Recovery Current Characteristics, $T_J = 25\text{ °C}$



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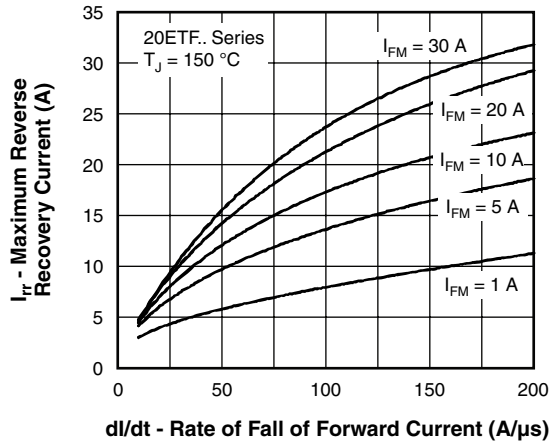


Fig. 13 - Recovery Current Characteristics, $T_J = 150^\circ\text{C}$

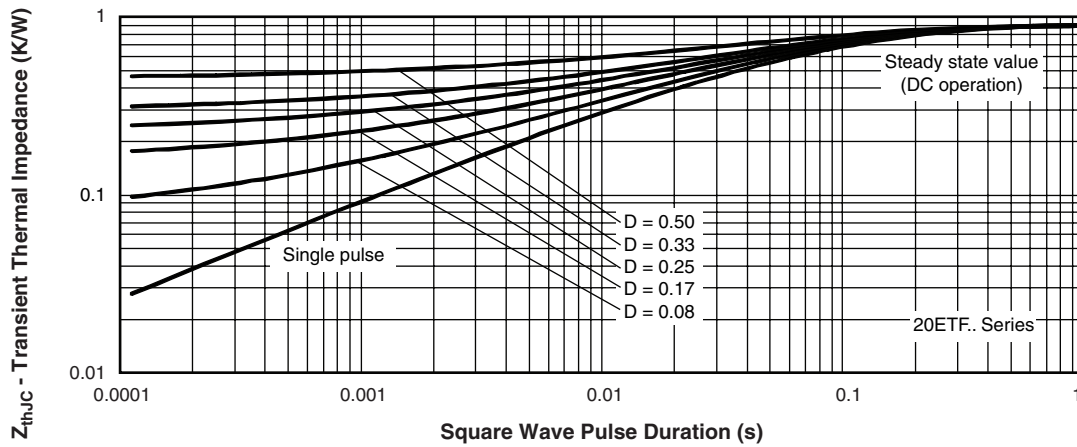


Fig. 14 - Thermal Impedance Z_{thJC} Characteristics

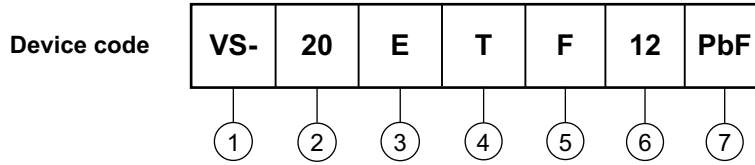
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ORDERING INFORMATION TABLE



- 1** - HPP product suffix
 - 2** - Current rating (20 = 20 A)
 - 3** - Circuit configuration:
E = Single diode
 - 4** - Package:
T = TO-220AC
 - 5** - Type of silicon:
F = Fast soft recovery rectifier
 - 6** - Voltage ratings
 - 7** - PbF = Lead (Pb)-free
- | |
|-------------|
| 08 = 800 V |
| 10 = 1000 V |
| 12 = 1200 V |

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95221
Part marking information	www.vishay.com/doc?95224

TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.			MIN.	MAX.	MIN.	MAX.	
A	4.25	4.65	0.167	0.183		E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055		E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115		e	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040		e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4	H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068		L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4	L1	3.32	3.82	0.131	0.150	2
c	0.36	0.61	0.014	0.024		L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4	L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3	Ø P	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355		Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6	θ	90° to 93°		90° to 93°		
E	10.11	10.51	0.398	0.414	3, 6						

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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