



STB55NF06 - STB55NF06-1 STP55NF06 - STP55NF06FP

N-CHANNEL 60V -0.015Ω - 50A TO-220/FP/D²/I²PAK
STripFET™II MOSFET

Table 1: General Features

| TYPE | V _{DSS} | R _{DS(on)} | I _D |
|-------------|------------------|---------------------|----------------|
| STB55NF06 | 60V | <0.018Ω | 50A |
| STB55NF06-1 | 60V | <0.018Ω | 50A |
| STP55NF06FP | 60V | <0.018Ω | 50A(*) |
| STP55NF06 | 60V | <0.018Ω | 50A |

- TYPICAL R_{DS(on)} = 0.015Ω
- EXCEPTIONAL dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- SURFACE-MOUNTING D²PAK POWER PACKAGE IN TAPE & REEL
- THROUGH-HOLE I²PAK POWER PACKAGE IN TUBE

DESCRIPTION

This MOSFET is the latest development of STMicroelectronics unique “Single Feature Size™” strip-based process. The resulting transistor shows extremely high packing density for low on-resistance, rugged avalanche characteristics and less critical alignment steps therefore a remarkable manufacturing reproducibility.

APPLICATIONS

- HIGH CURRENT, HIGH SWITCHING SPEED
- MOTOR CONTROL, AUDIO AMPLIFIERS
- DC-DC & DC-AC CONVERTERS
- AUTOMOTIVE

Figure 1: Package

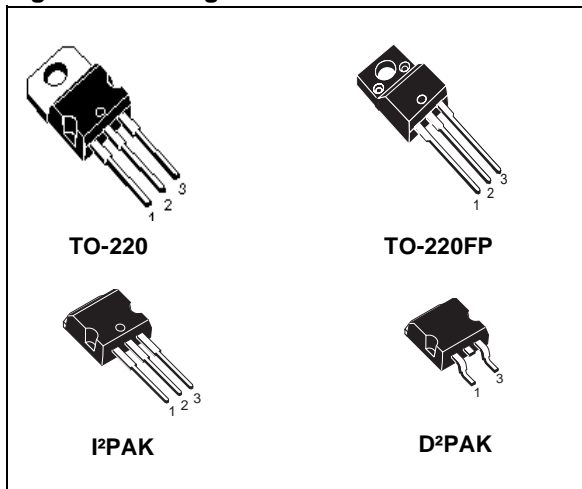


Figure 2: Internal Schematic Diagram

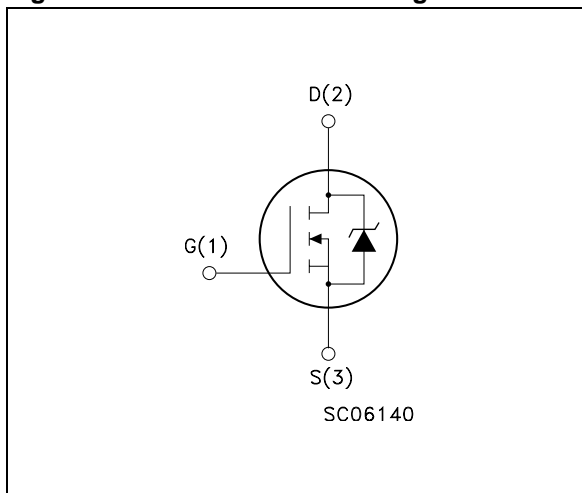


Table 2: Order Codes

| SALES TYPE | MARKING | PACKAGE | PACKAGING |
|-------------|-----------|--------------------|-------------|
| STB55NF06 | B55NF06 | D ² PAK | TAPE & REEL |
| STB55NF06-1 | B55NF06 | I ² PAK | TUBE |
| STP55NF06FP | P55NF06FP | TO-220FP | TUBE |
| STP55NF06 | P55NF06 | TO-220 | TUBE |

Table 3: Absolute Maximum ratings

| Symbol | Parameter | Value | | Unit |
|------------------------------------|---|---|----------|------|
| | | TO-220/D ² /I ² PAK | TO-220FP | |
| V _{DS} | Drain-source Voltage (V _{GS} = 0) | 60 | | V |
| V _{GS} | Gate- source Voltage | ± 20 | | V |
| I _D | Drain Current (continuous) at T _C = 25°C | 50 | 50 (*) | A |
| I _D | Drain Current (continuous) at T _C = 100°C | 35 | 35 (*) | A |
| I _{DM} (●) | Drain Current (pulsed) | 200 | 200 (*) | A |
| P _{TOT} | Total Dissipation at T _C = 25°C | 110 | 30 | W |
| | Derating Factor | 0.73 | 0.20 | W/°C |
| E _{AS} (2) | Single Pulse Avalanche Energy | 340 | | mJ |
| dv/dt (1) | Peak Diode Recovery voltage slope | 7 | | V/ns |
| V _{ISO} | Insulation Withstand Voltage (DC) | - | 2500 | V |
| T _j T _{stg} | Operating Junction Temperature Storage Temperature | -55 to 175 | | °C |

(●) Pulse width limited by safe operating area

(*) Refer to soa for the max allowable current value on FP-type due to R_{th} value

(1) I_{SD} ≤ 50 A, di/dt ≤ 400A/μs, V_{DD} ≤ V_{(BR)DSS}, T_j ≤ T_{jmax}

(2) Starting T_j = 25°C, V_{DD} = 30V, I_D = 25A

Table 4: Thermal Data

| | | TO-220/D ² /I ² PAK | TO-220FP | |
|-----------------------|---|---|----------|------|
| R _{thj-case} | Thermal Resistance Junction-case Max | 1.36 | 5 | °C/W |
| R _{thj-amb} | Thermal Resistance Junction-ambient Max | 62.5 | | °C/W |
| T _l | Maximum Lead Temperature For Soldering Purpose (1.6mm from case, for 10sec) | 300 | | °C |

ELECTRICAL CHARACTERISTICS (T_{CASE} = 25°C UNLESS OTHERWISE SPECIFIED)
Table 5: On/Off

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|----------------------|---|--|------|-------|---------|----------|
| V _{(BR)DSS} | Drain-source Breakdown Voltage | I _D = 250μA, V _{GS} = 0 | 60 | | | V |
| I _{DSS} | Zero Gate Voltage Drain Current (V _{GS} = 0) | V _{DS} = Max Rating V _{DS} = Max Rating, T _C = 125°C | | | 1 10 | μA μA |
| I _{GSS} | Gate-body Leakage Current (V _{DS} = 0) | V _{GS} = ± 20V | | | ±100 | nA |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} , I _D = 250μA | 2 | 3 | 4 | V |
| R _{DS(on)} | Static Drain-source On Resistance | V _{GS} = 10V, I _D = 27.5A | | 0.015 | 0.018 | Ω |

ELECTRICAL CHARACTERISTICS (CONTINUED)

Table 6: DYNAMIC

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|---|---|---|------|----------------------|------|----------------------|
| g_{fs} (4) | Forward Transconductance | $V_{DS} = 15V, I_D = 27.5A$ | | 18 | | S |
| C_{iss} C_{oss} C_{rss} | Input Capacitance Output Capacitance Reverse Transfer Capacitance | $V_{DS} = 25V, f=1MHz, V_{GS}=0$ | | 1300 300 105 | | pF pF pF |
| $t_{d(on)}$ t_r $t_{d(off)}$ t_f | Turn-on Delay Time Rise Time Turn-off Delay Time Fall Time | $V_{DD} = 30V, I_D = 27.5A$ $R_G = 4.7\Omega, V_{GS} = 10V$ (see Figure 17) | | 20 50 36 15 | | ns ns ns ns |
| Q_g Q_{gs} Q_{gd} | Total Gate Charge Gate-Source Charge Gate-Drain Charge | $V_{DD} = 48V, I_D = 55A,$ $V_{GS} = 10V$ (see Figure 20) | | 44.5 10.5 17.5 | 60 | nC nC nC |

Table 7: Source Drain Diode

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|-----------------------------------|--|--|------|------------------|-----------|---------------|
| I_{SD} $I_{SDM} (\bullet)$ | Source-drain Current Source-drain Current (pulsed) | | | | 50 200 | A A |
| V_{SD} (4) | Forward On Voltage | $I_{SD} = 50A, V_{GS} = 0$ | | | 1.5 | V |
| t_{rr} Q_{rr} I_{RRM} | Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current | $I_{SD} = 50A, di/dt = 100A/\mu s$ $V_{DD} = 30V, T_j = 150^\circ C$ (see Figure 18) | | 75 170 4.5 | | ns nC A |

(4) Pulsed: pulse duration = 300 μs , duty cycle 1.5%

Figure 3: Safe Operating Area for TO-220/D²/I²PAK

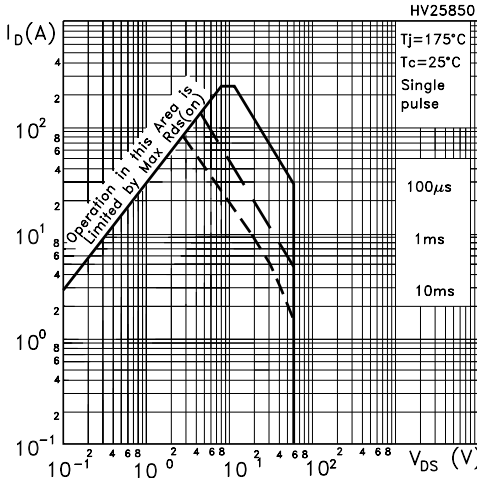


Figure 4: Safe Operating Area for TO-220FP

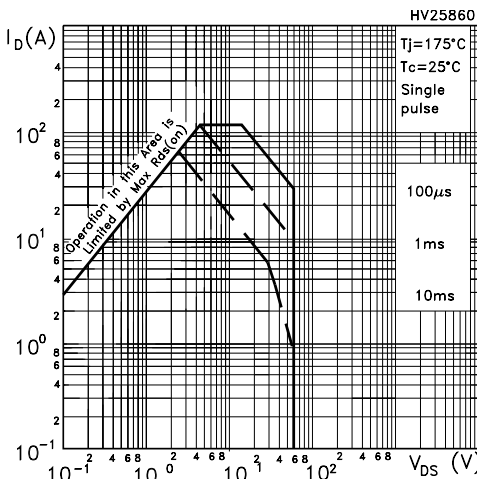


Figure 5: Output Characteristics

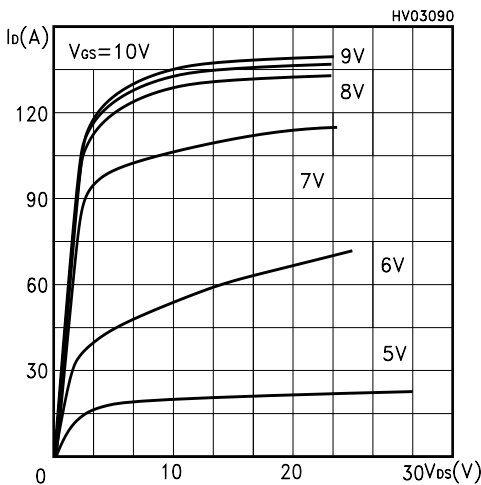


Figure 6: Thermal Impedance for TO-220/D²/I²PAK

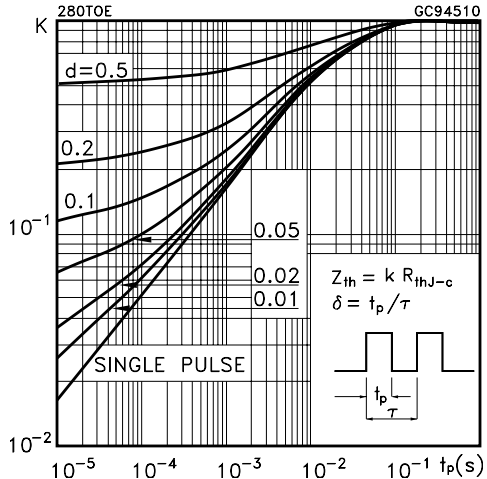


Figure 7: Thermal Impedance for TO-220FP

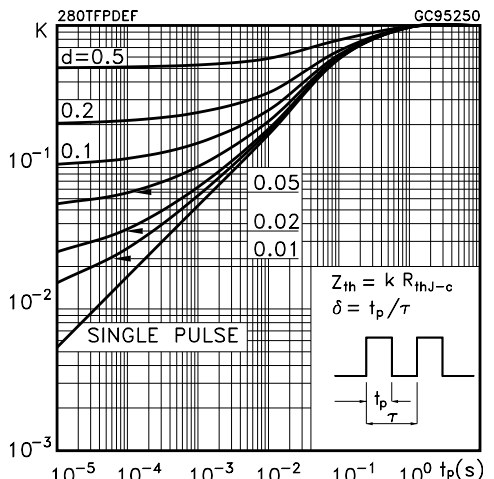


Figure 8: Transfer Characteristics

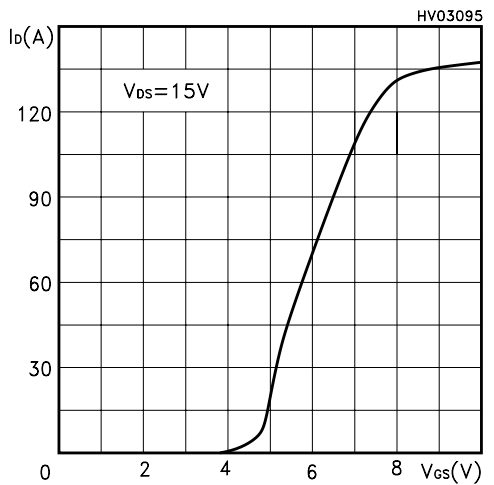


Figure 9: Transconductance

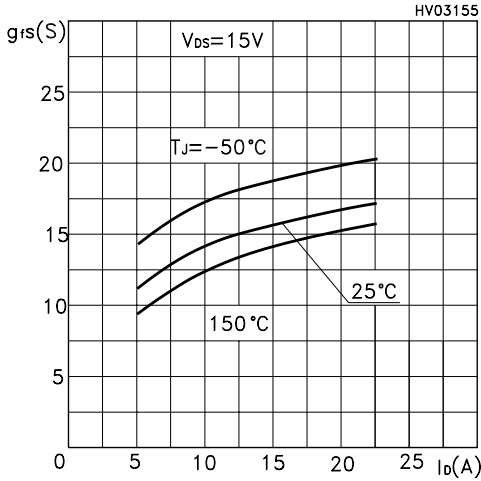


Figure 10: Gate Charge vs Gate-source Voltage

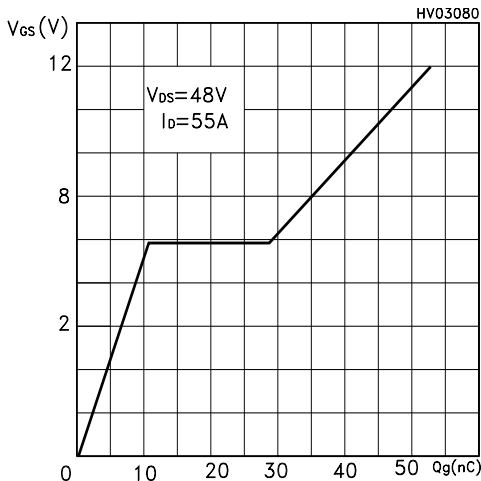


Figure 11: Normalized Gate Threshold Voltage vs Temperature

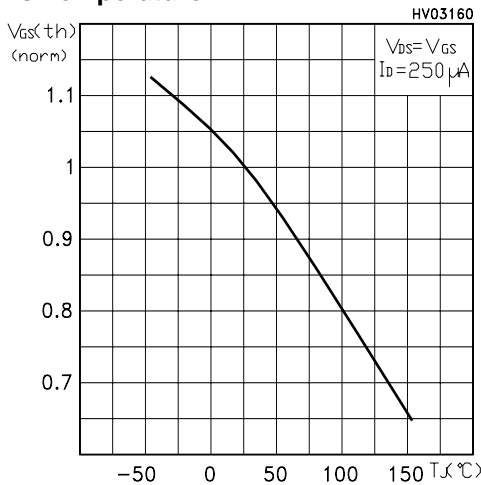


Figure 12: Static Drain-source On Resistance

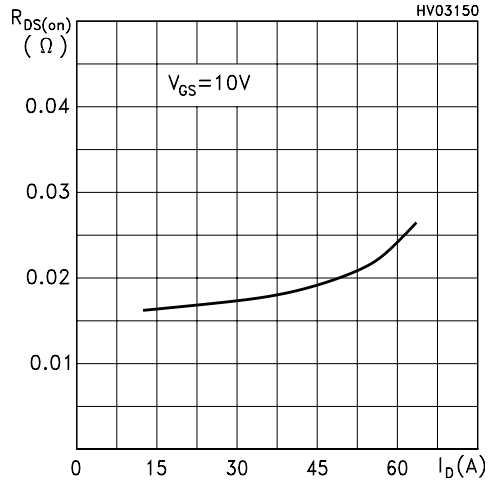


Figure 13: Capacitance Variations

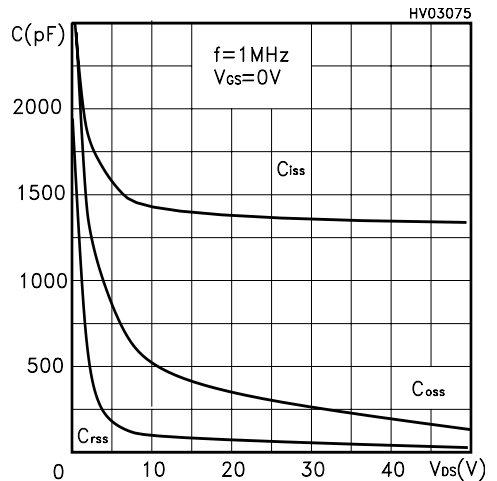


Figure 14: Normalized On Resistance vs Temperature

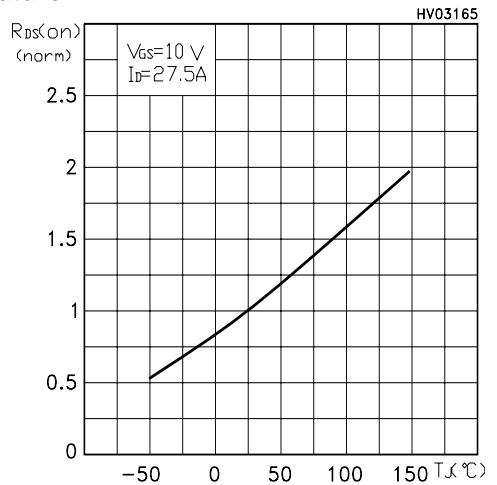


Figure 15: Source-Drain Diode Forward Characteristics

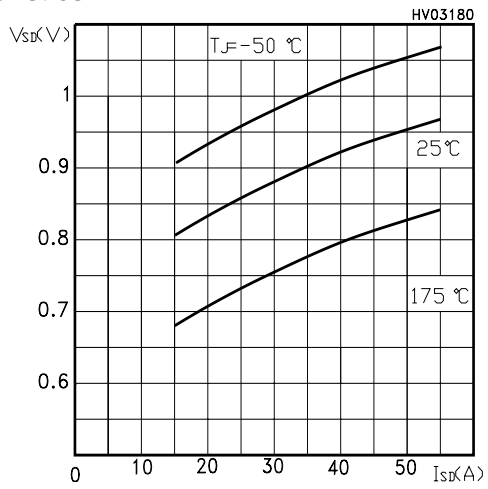


Figure 16: Unclamped Inductive Load Test Circuit

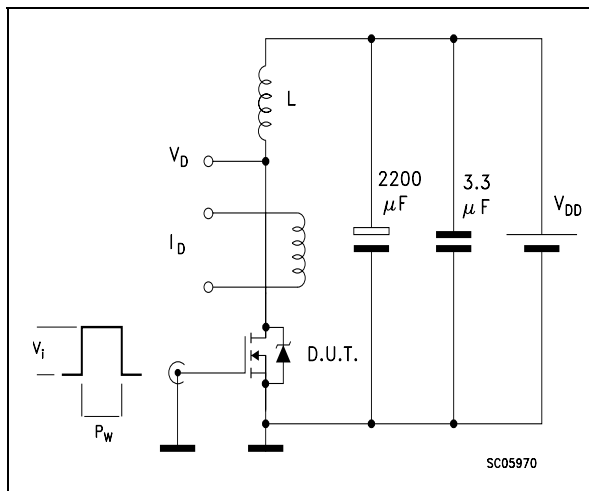


Figure 17: Switching Times Test Circuit For Resistive Load

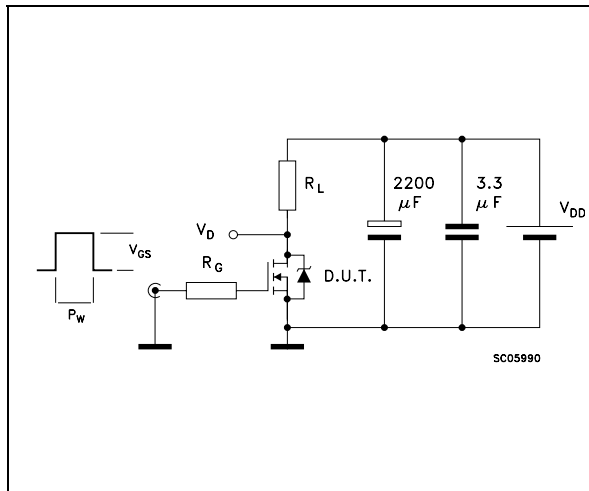


Figure 18: Test Circuit For Inductive Load Switching and Diode Recovery Times

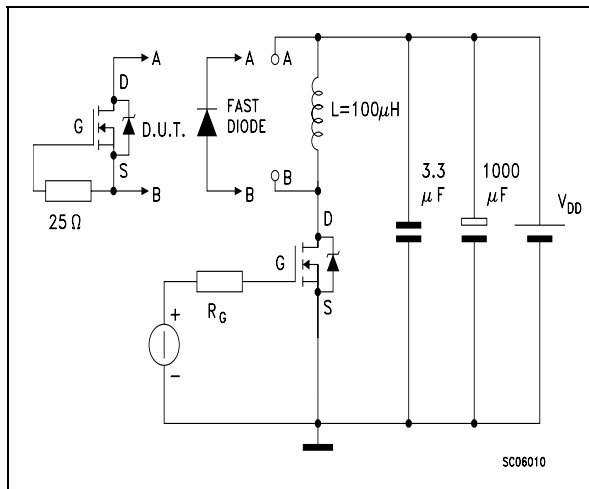


Figure 19: Unclamped Inductive Waferform

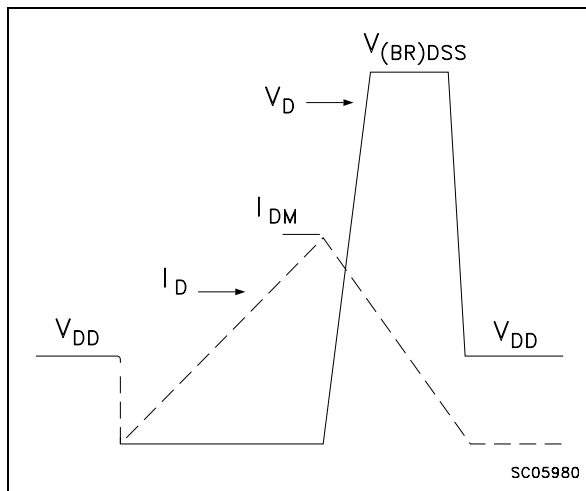
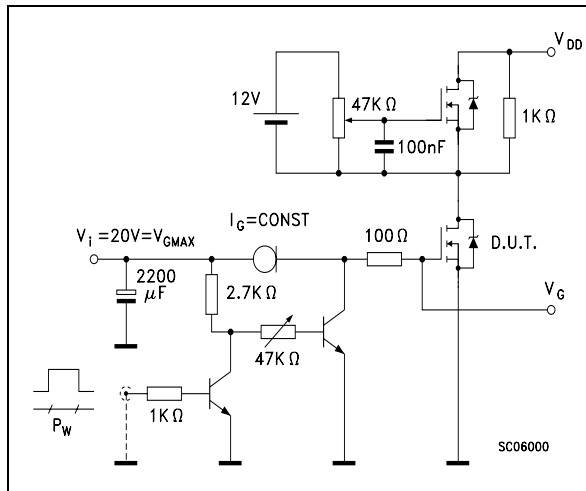


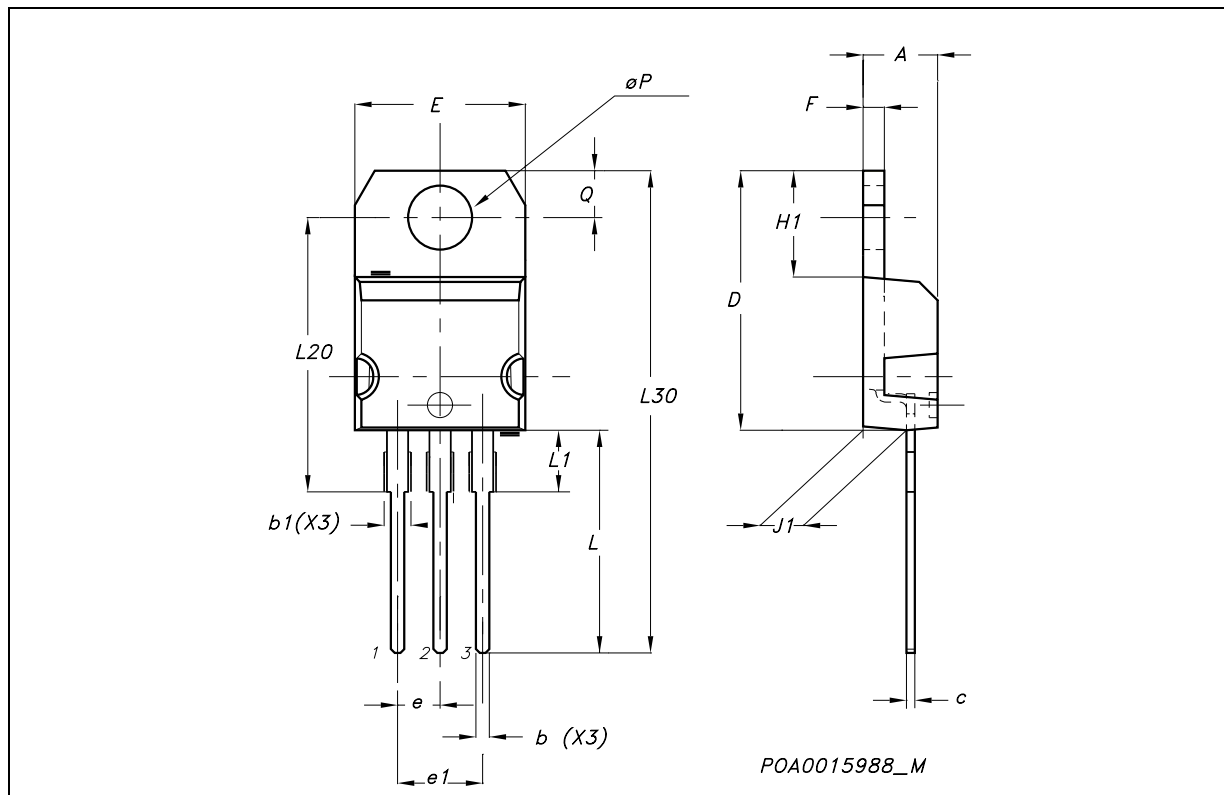
Figure 20: Gate Charge Test Circuit



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

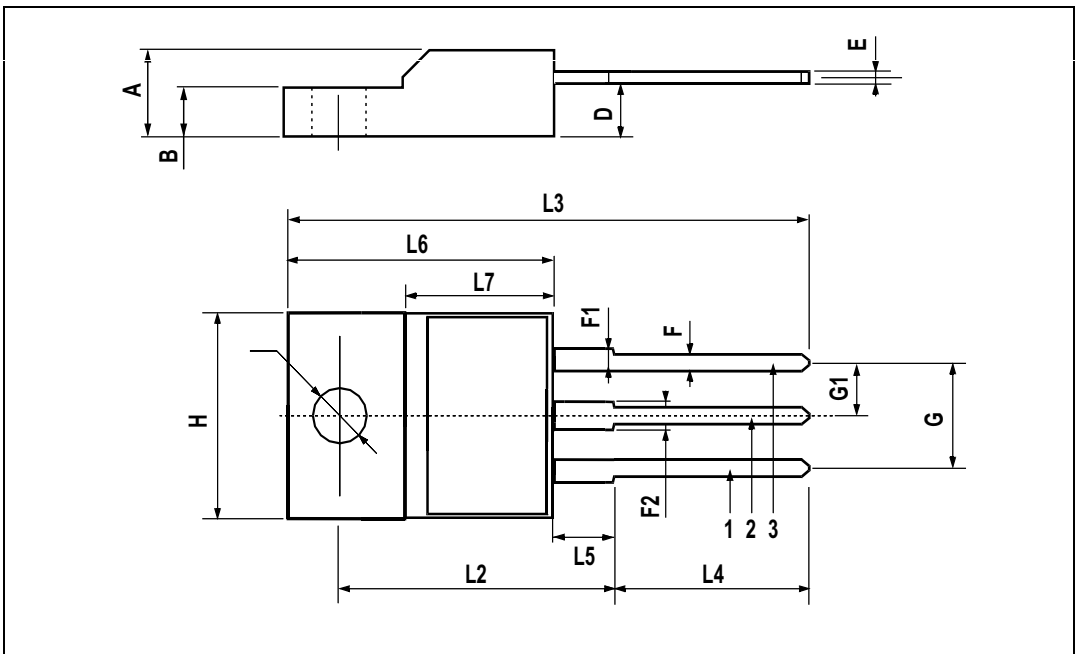
TO-220 MECHANICAL DATA

| DIM. | mm. | | | inch | | |
|------|-------|-------|-------|-------|-------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | MAX. |
| A | 4.40 | | 4.60 | 0.173 | | 0.181 |
| b | 0.61 | | 0.88 | 0.024 | | 0.034 |
| b1 | 1.15 | | 1.70 | 0.045 | | 0.066 |
| c | 0.49 | | 0.70 | 0.019 | | 0.027 |
| D | 15.25 | | 15.75 | 0.60 | | 0.620 |
| E | 10 | | 10.40 | 0.393 | | 0.409 |
| e | 2.40 | | 2.70 | 0.094 | | 0.106 |
| e1 | 4.95 | | 5.15 | 0.194 | | 0.202 |
| F | 1.23 | | 1.32 | 0.048 | | 0.052 |
| H1 | 6.20 | | 6.60 | 0.244 | | 0.256 |
| J1 | 2.40 | | 2.72 | 0.094 | | 0.107 |
| L | 13 | | 14 | 0.511 | | 0.551 |
| L1 | 3.50 | | 3.93 | 0.137 | | 0.154 |
| L20 | | 16.40 | | | 0.645 | |
| L30 | | 28.90 | | | 1.137 | |
| øP | 3.75 | | 3.85 | 0.147 | | 0.151 |
| Q | 2.65 | | 2.95 | 0.104 | | 0.116 |



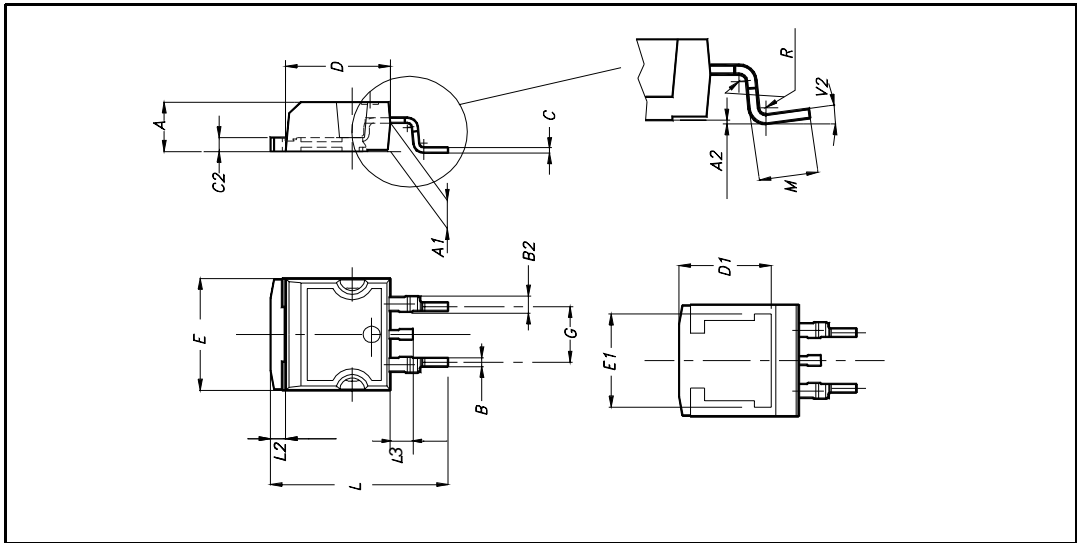
TO-220FP MECHANICAL DATA

| DIM. | mm. | | | inch | | |
|------|------|-----|------|-------|-------|-------|
| | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A | 4.4 | | 4.6 | 0.173 | | 0.181 |
| B | 2.5 | | 2.7 | 0.098 | | 0.106 |
| D | 2.5 | | 2.75 | 0.098 | | 0.108 |
| E | 0.45 | | 0.7 | 0.017 | | 0.027 |
| F | 0.75 | | 1 | 0.030 | | 0.039 |
| F1 | 1.15 | | 1.7 | 0.045 | | 0.067 |
| F2 | 1.15 | | 1.7 | 0.045 | | 0.067 |
| G | 4.95 | | 5.2 | 0.195 | | 0.204 |
| G1 | 2.4 | | 2.7 | 0.094 | | 0.106 |
| H | 10 | | 10.4 | 0.393 | | 0.409 |
| L2 | | 16 | | | 0.630 | |
| L3 | 28.6 | | 30.6 | 1.126 | | 1.204 |
| L4 | 9.8 | | 10.6 | .0385 | | 0.417 |
| L5 | 2.9 | | 3.6 | 0.114 | | 0.141 |
| L6 | 15.9 | | 16.4 | 0.626 | | 0.645 |
| L7 | 9 | | 9.3 | 0.354 | | 0.366 |
| ∅ | 3 | | 3.2 | 0.118 | | 0.126 |

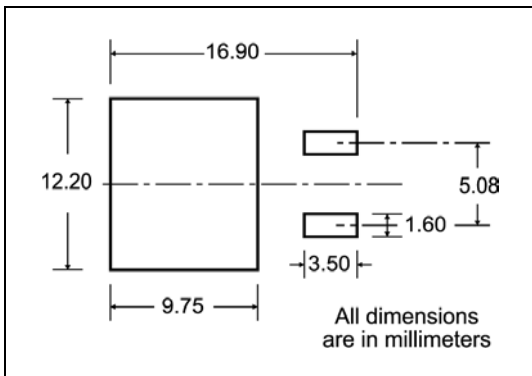


D²PAK MECHANICAL DATA

| DIM. | mm. | | | inch | | |
|------|------|------|-------|-------|-------|-------|
| | MIN. | TYP. | MAX. | MIN. | TYP. | MAX. |
| A | 4.4 | | 4.6 | 0.173 | | 0.181 |
| A1 | 2.49 | | 2.69 | 0.098 | | 0.106 |
| A2 | 0.03 | | 0.23 | 0.001 | | 0.009 |
| B | 0.7 | | 0.93 | 0.027 | | 0.036 |
| B2 | 1.14 | | 1.7 | 0.044 | | 0.067 |
| C | 0.45 | | 0.6 | 0.017 | | 0.023 |
| C2 | 1.23 | | 1.36 | 0.048 | | 0.053 |
| D | 8.95 | | 9.35 | 0.352 | | 0.368 |
| D1 | | 8 | | | 0.315 | |
| E | 10 | | 10.4 | 0.393 | | |
| E1 | | 8.5 | | | 0.334 | |
| G | 4.88 | | 5.28 | 0.192 | | 0.208 |
| L | 15 | | 15.85 | 0.590 | | 0.625 |
| L2 | 1.27 | | 1.4 | 0.050 | | 0.055 |
| L3 | 1.4 | | 1.75 | 0.055 | | 0.068 |
| M | 2.4 | | 3.2 | 0.094 | | 0.126 |
| R | | 0.4 | | | 0.015 | |
| V2 | 0° | | 4° | | | |



D²PAK FOOTPRINT



TAPE AND REEL SHIPMENT

40 mm min. Access hole at slot location

Full radius

Tape slot in core for tape start

2.5mm min. width

REEL MECHANICAL DATA

| DIM. | mm | | inch | |
|------|------|------|-------|--------|
| | MIN. | MAX. | MIN. | MAX. |
| A | | 330 | | 12.992 |
| B | 1.5 | | 0.059 | |
| C | 12.8 | 13.2 | 0.504 | 0.520 |
| D | 20.2 | | 0.795 | |
| G | 24.4 | 26.4 | 0.960 | 1.039 |
| N | 100 | | 3.937 | |
| T | | 30.4 | | 1.197 |

| BASE QTY | BULK QTY |
|----------|----------|
| 1000 | 1000 |

TAPE MECHANICAL DATA

| DIM. | mm | | inch | |
|------|------|------|--------|--------|
| | MIN. | MAX. | MIN. | MAX. |
| A0 | 10.5 | 10.7 | 0.413 | 0.421 |
| B0 | 15.7 | 15.9 | 0.618 | 0.626 |
| D | 1.5 | 1.6 | 0.059 | 0.063 |
| D1 | 1.59 | 1.61 | 0.062 | 0.063 |
| E | 1.65 | 1.85 | 0.065 | 0.073 |
| F | 11.4 | 11.6 | 0.449 | 0.456 |
| K0 | 4.8 | 5.0 | 0.189 | 0.197 |
| P0 | 3.9 | 4.1 | 0.153 | 0.161 |
| P1 | 11.9 | 12.1 | 0.468 | 0.476 |
| P2 | 1.9 | 2.1 | 0.075 | 0.082 |
| R | 50 | | 1.574 | |
| T | 0.25 | 0.35 | 0.0098 | 0.0137 |
| W | 23.7 | 24.3 | 0.933 | 0.956 |

10 pitches cumulative tolerance on tape +/- 0.2 mm

Center line of cavity

User Direction of Feed

TRL

FEED DIRECTION

Bending radius

R min.

* on sales type

Table 8: Revision History

| Date | Revision | Description of Changes |
|-------------|-----------------|--|
| 26-May-2005 | 1 | First Release. New datasheet according to PCN MLD-PMT/05/1115 |
| 28-Nov-2005 | 2 | Inserted ecopack indication |

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