



STAC2942B

RF power transistors HF/VHF/UHF N-channel MOSFETs

Preliminary Data

Features

- Gold metallization
- Excellent thermal stability
- Common source push-pull configuration
- $P_{OUT} = 350 \text{ W min.}$
with 21 dB gain @ 175 MHz
- In compliance with the 2002/95/EC
European directive

Description

The STAC2942B is a gold metallized N-channel MOS field-effect RF power transistor. It is intended for use in 50 V DC large signal applications up to 250 MHz.



Figure 1. Pin connection

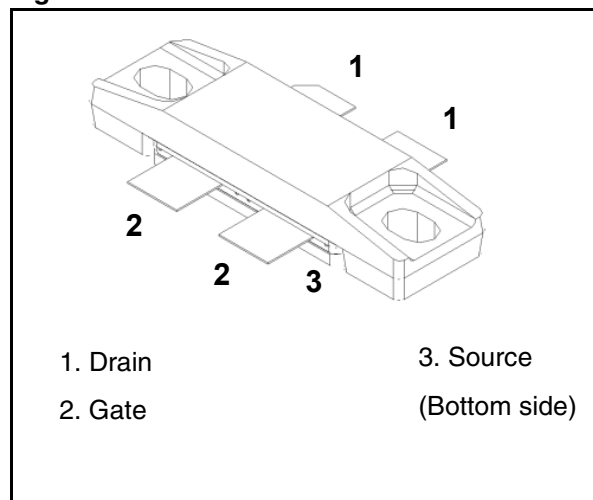


Table 1. Device summary

Order code	Marking	Package	Packaging
STAC2942B	STAC2942	STAC244B	Plastic tray

Contents

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1 Electrical data

1.1 Maximum ratings

Table 2. Absolute maximum ratings ($T_{CASE} = 25\text{ °C}$)

Symbol	Parameter	Value	Unit
$V_{(BR)DSS}^{(1)}$	Drain source voltage	130	V
$V_{DGR}^{(1)}$	Drain-gate voltage ($R_{GS} = 1\text{ M}\Omega$)	130	V
V_{GS}	Gate-source voltage	± 20	V
I_D	Drain current	40	A
P_{DISS}	Power dissipation	625	W
T_J	Max. operating junction temperature	200	$^{\circ}\text{C}$
T_{STG}	Storage temperature	-65 to +150	$^{\circ}\text{C}$

1. $T_J = 150\text{ °C}$

1.2 Thermal data

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R_{thJC}	Junction - case thermal resistance	0.28	$^{\circ}\text{C/W}$

2 Electrical characteristics

$$T_{\text{CASE}} = +25\text{ }^{\circ}\text{C}$$

2.1 Static

Table 4. Static (per side)

Symbol	Test conditions			Min.	Typ.	Max.	Unit
$V_{(\text{BR})\text{DSS}}^{(1)}$	$V_{\text{GS}} = 0\text{ V}$	$I_{\text{DS}} = 100\text{ mA}$		130			V
I_{DSS}	$V_{\text{GS}} = 0\text{ V}$	$V_{\text{DS}} = 50\text{ V}$				100	μA
I_{GSS}	$V_{\text{GS}} = 20\text{ V}$	$V_{\text{DS}} = 0\text{ V}$				250	nA
$V_{\text{GS(Q)}}$	$V_{\text{DS}} = 10\text{ V}$	$I_{\text{D}} = 250\text{ mA}$		1.5	2.5	4.0	V
$V_{\text{DS(ON)}}$	$V_{\text{GS}} = 10\text{ V}$	$I_{\text{D}} = 10\text{ A}$				3.0	V
G_{FS}	$V_{\text{DS}} = 10\text{ V}$	$I_{\text{D}} = 5\text{ A}$		5			S
C_{ISS}	$V_{\text{GS}} = 0\text{ V}$ $V_{\text{DS}} = 50\text{ V}$ $f = 1\text{ MHz}$				425		pF
C_{OSS}					202		pF
C_{RSS}					12		pF

1. $T_{\text{J}} = 150\text{ }^{\circ}\text{C}$

2.2 Dynamic

Table 5. Dynamic

Symbol	Test conditions	Min.	Typ.	Max.	Unit
P_{OUT}	$V_{\text{DD}} = 50\text{ V}$, $I_{\text{DQ}} = 2 \times 250\text{ mA}$, $P_{\text{IN}} = 4\text{ W}$, $f = 175\text{ MHz}$	350	450		W
h_{D}	$V_{\text{DD}} = 50\text{ V}$, $I_{\text{DQ}} = 2 \times 250\text{ mA}$, $P_{\text{IN}} = 4\text{ W}$, $f = 175\text{ MHz}$	60	75		%

3 Impedance

Figure 2. Current conventions

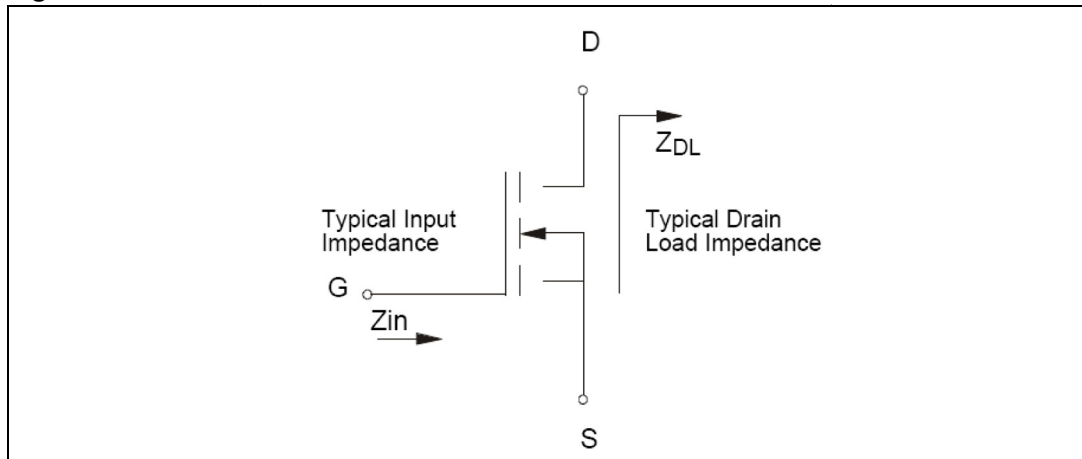


Table 6. Impedance data

Freq. (MHz)	Z_{IN} (Ω)	Z_{DL} (Ω)
175 MHz	$2.0 - j2.0$	$3.5 + j5.2$

Note: Measured gate to gate and drain to drain, respectively.

4 Typical performance

Figure 3. Capacitances vs drain supply voltage

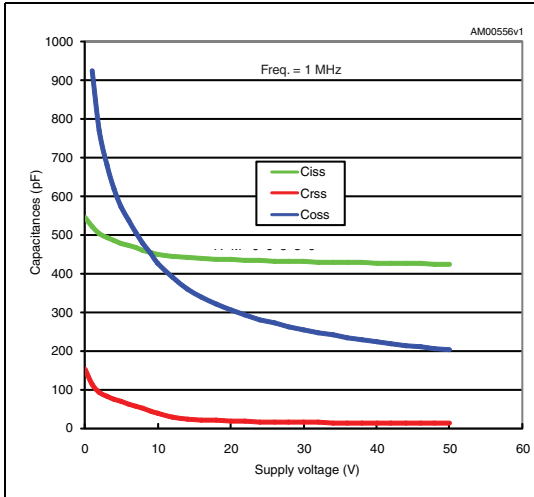


Figure 4. Output power vs drain supply voltage

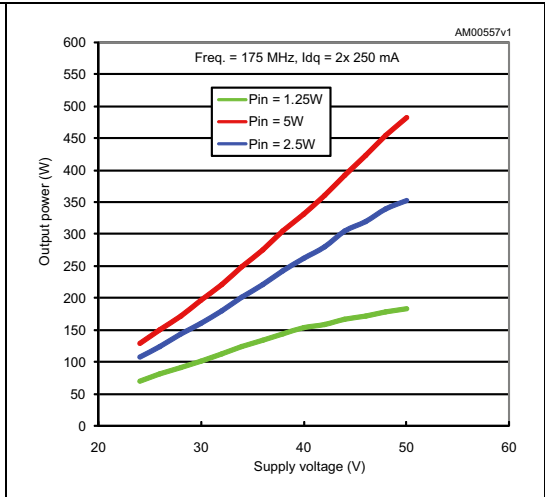


Figure 5. Output power vs gate voltage

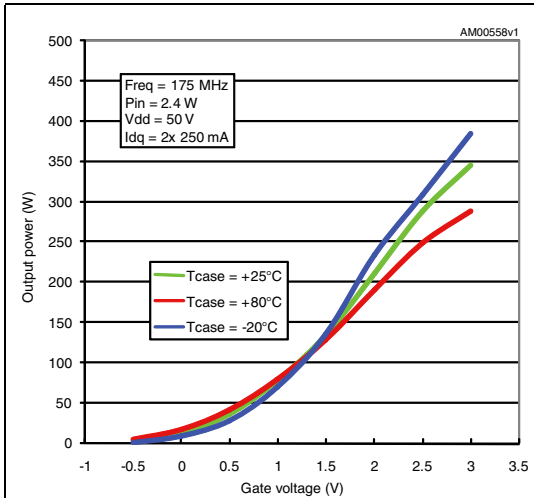


Figure 6. Output power vs input power

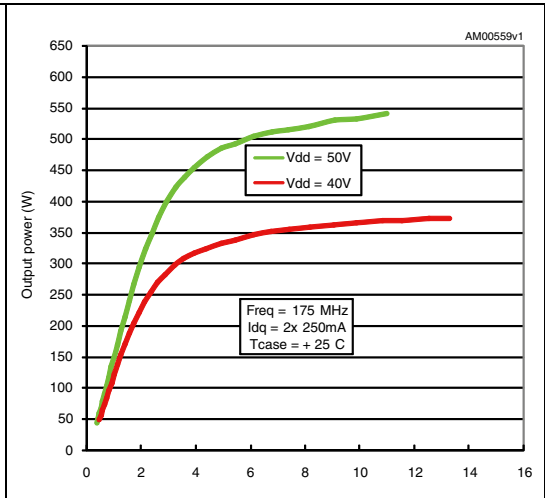


Figure 7. Output power vs input power and case temperature

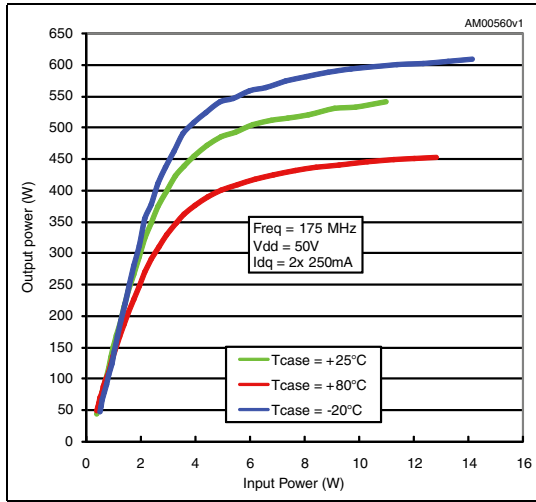


Figure 8. Efficiency vs output power and case temperature

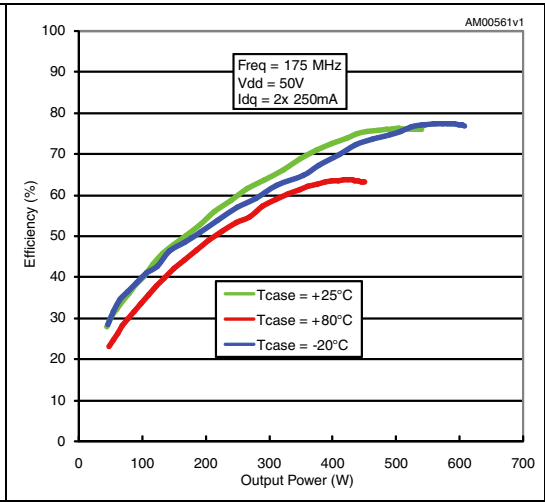
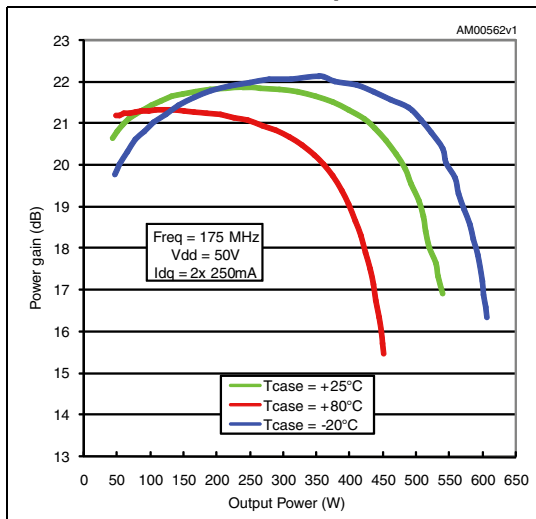


Figure 9. Power gain vs output power and case temperature



5 Test circuit

Figure 10. 175 MHz test circuit schematic (production test circuit)

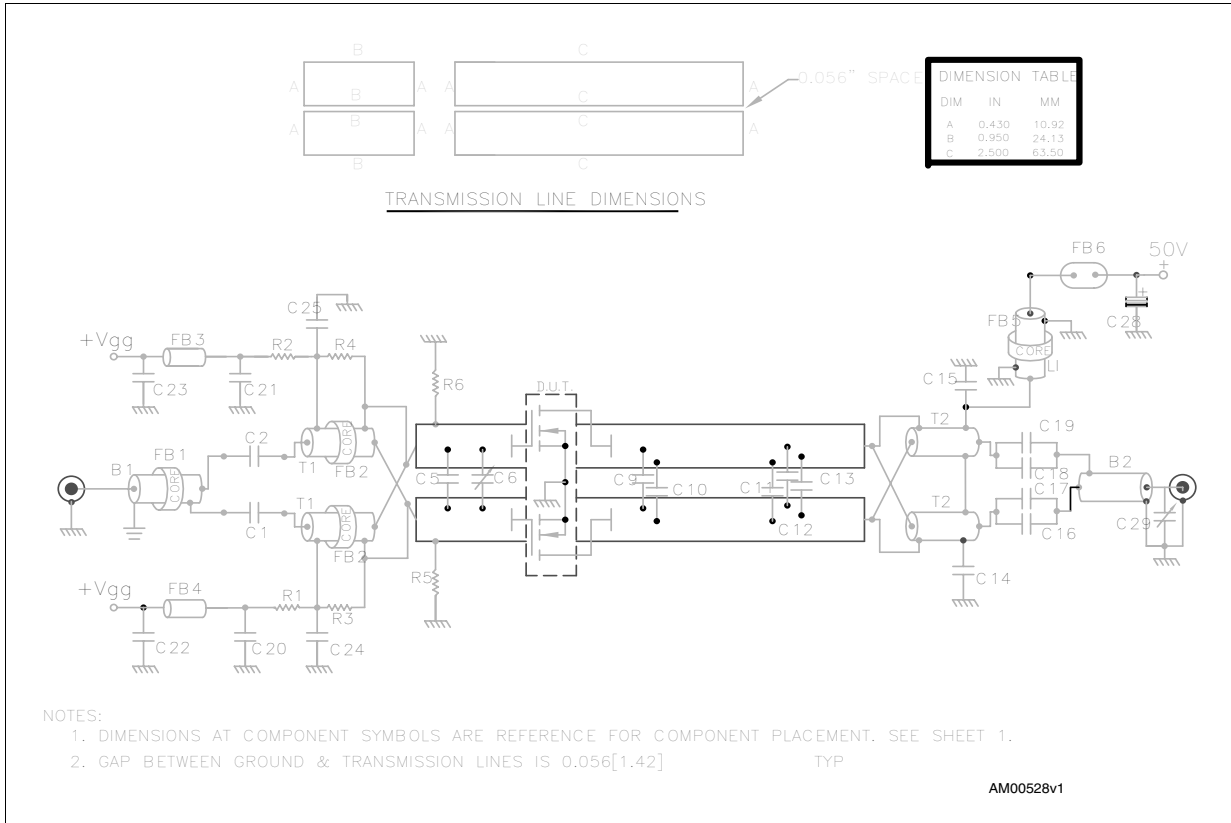


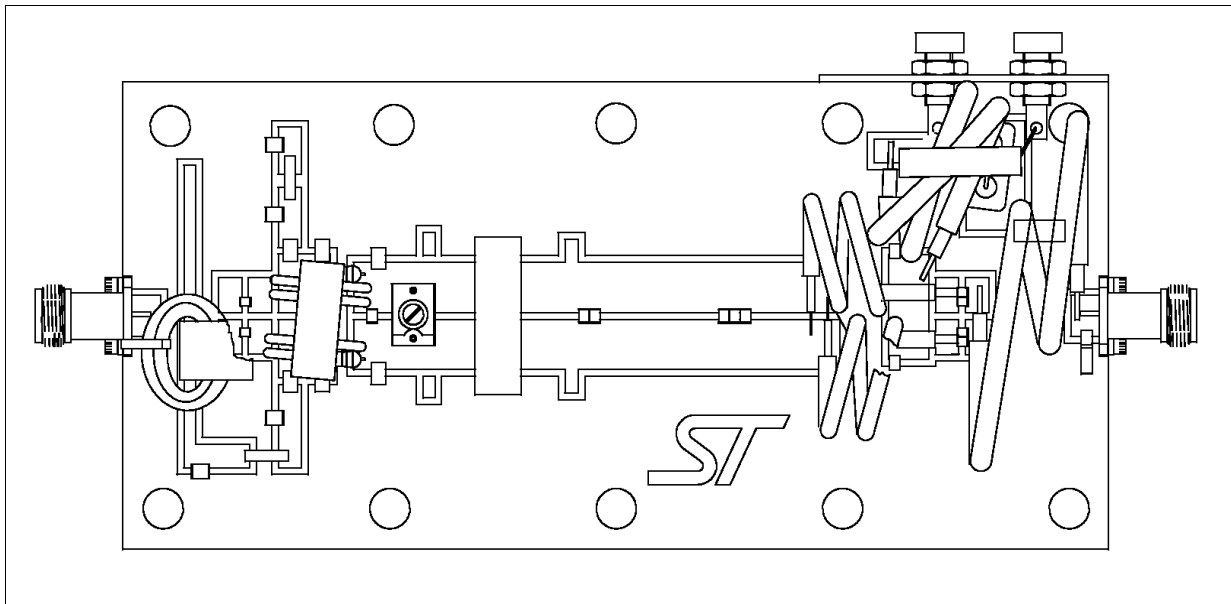
Table 7. 175 MHz test circuit component part list

Component	Description
C1, C2, C14, C15, C24, C25	1200 pF ATC 700B chip capacitor
C5	75 pF ATC 100B chip capacitor
C6	ST406 variable capacitor
C9, C10	47 pF ATC 100B chip capacitor
C11, C12, C13	43 pF ATC 100B chip capacitor
C16, C18	470 pF ATC 100B chip capacitor
C17, C19, C20, C21	10,000 pF ATC 200B chip capacitor
C22, C23	.1 μ F 200 V chip capacitor
C28	10 μ F 100 V electrolytic capacitor
C29	.8 - 8 pF variable capacitor
R1, R2, R5, R6	430 Ω , 1/2 W chip resistor

Table 7. 175 MHz test circuit component part list (continued)

Component	Description
R3, R4	270 Ω 1/2 W axial lead resistor
B1	RG-316 50 Ω 11.8" thru ferrite toroid
B2	RG-142 50 Ω 11.8"
T1	4:1, RG-316 25 Ω , 5.9", 2 turns thru ferrite core
T2	1:4, 25 Ω semi-rigid cable, OD .141", 5.9"
L1	$\lambda/4$ inductor, RG-142 50 Ω , 11.8", 3 turns thru ferrite toroid
FB1,FB5	ferrite toroid
FB2, FB6	multi-aperture core
FB3, FB4	surface mount ferrite bead
PCB	Rogers ultralam 2000, Er 2.55, .060"

Figure 11. Circuit layout



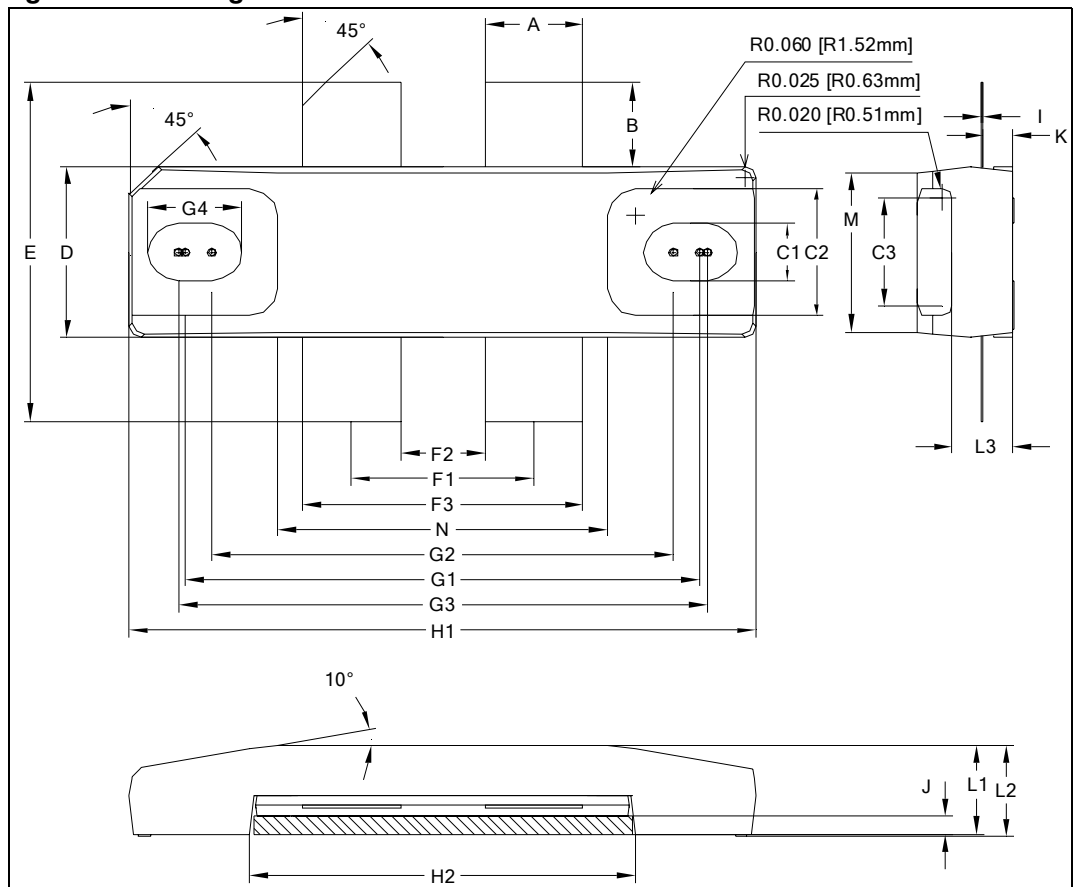
6 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Table 8. STAC244B package dimensions

Dim.	mm.			Inch		
	Min	Typ	Max	Min	Typ	Max
A		5.33			0.210	
B		4.83			0.190	
C1		3.25			0.128	
C2		7.24			0.285	
C3		6.22			0.245	
D		9.78			0.385	
E		19.43			0.765	
F1		9.91			0.390	
F2		4.57			0.180	
F3		15.24			0.600	
G1		27.94			1.100	
G2		25.10			0.988	
G3		28.75			1.132	
G4		5.08			0.200	
H1		34.04			1.340	
H2		20.96			0.825	
I		0.15			0.006	
J		1.02			0.040	
K		1.57			0.062	
L1		5.21			0.205	
L2		5.33			0.210	
L3		3.25			0.128	
M		9.14			0.360	
N		17.91			0.705	
Controlling dimension: inches						

Figure 12. Package dimensions



7 Revision history

Table 9. Document revision history

Date	Revision	Changes
03-Jul-2008	1	First release

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