

Provisional data

Insulated Gate Bi-Polar Transistor

Type T1200TA25A

(Development Type Number: TX033TA25A)

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V _{CES}	Collector – emitter voltage	2500	V
V _{DC link}	Permanent DC voltage for FIT 100 failure rate.	1250	V
V _{GES}	Peak gate – emitter voltage	±20	V

	RATINGS	MAXIMUM LIMITS	UNITS
I _{C(DC)}	Continuous DC collector current, IGBT (Note 2).	1141	A
I _{CRM}	Repetitive peak collector current, t _p =1ms, IGBT.	2400	A
I _{F(DC)}	Continuous DC forward current, Diode (note 2).	909	A
I _{FRM}	Repetitive peak forward current, t _p =1ms, Diode.	2400	A
P _{MAX}	Maximum power dissipation, IGBT (note 3).	5.9	kW
(di/dt) _{cr}	Critical diode di/dt (note 4)	1600	A/μs
T _j	Operating temperature range.	-40 to +125	°C
T _{stg}	Storage temperature range.	-40 to +125	°C

Notes: -

- 1) Unless otherwise indicated T_j = 125°C.
- 2) T_{sink} = 55°C, double side cooled.
- 3) T_{sink} = 25°C, double side cooled.
- 4) Maximum commutation loop inductance 1μH.

Characteristics

IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V _{CE(sat)}	Collector – emitter saturation voltage	-	2.7	3.0	I _C = 1200A, V _{GE} = 15V, T _J = 25°C	V
		-	3.2	3.75	I _C = 1200A, V _{GE} = 15V	V
V _{To}	Threshold voltage	-	-	1.25	Current range: 500A – 1600A	V
r _T	Slope resistance	-	-	2.08		mΩ
V _{GE(TH)}	Gate threshold voltage	6	6.4	9	V _{CE} = V _{GE} , I _C = 200mA	V
I _{CES}	Collector – emitter cut-off current	-	8	35	V _{CE} = V _{CES} , V _{GE} = 0V	mA
I _{GES}	Gate leakage current	-	-	±8	V _{GE} = ±20V	μA
C _{ies}	Input capacitance	-	167	-	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz	nF
t _{d(on)}	Turn-on delay time	-	3.0	-	I _C = 1200A, V _{CE} = 0.5V _{CES} , V _{GE} = ±20V, R _{g(ON)} = 12Ω, R _{g(OFF)} = 8.5Ω, Snubber : 10Ω and 0.5μF in series	μs
t _{r(l)}	Rise time	-	3.0	-		μs
Q _{g(on)}	Turn-on gate charge	-	-	28		μC
E _{on}	Turn-on energy	-	2.5	-		J
t _{d(off)}	Turn-off delay time	-	2.3	-		μs
t _f	Fall time	-	2.4	-		μs
Q _{g(off)}	Turn-off gate charge	-	-	49		μC
E _{off}	Turn-off energy	-	1.4	-		J

Diode Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V _F	Forward voltage	-	2.32	-	I _F = 1200A, T _J = 25°C	V
		-	2.45	3	I _F = 1200A	V
V _{To}	Threshold voltage	-	-	1.5	Current range 500-1600A	V
r _T	Slope resistance	-	-	1.25		mΩ
I _{rm}	Peak reverse recovery current	-	670	-	I _F = 1200A, V _{GE} = ±20V, di/dt=1500A/μs Snubber : 10Ω and 0.5μF in series	A
Q _{rr}	Recovered charge, 50% chord	-	830	-		μC
t _{rr}	Reverse recovery time, 50% chord	-	1.5	-		μs
E _r	Reverse recovery energy	-	0.3	-		J

Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
R _{thJK}	Thermal resistance junction to sink, IGBT	-	-	16.9	Double side cooled	K/kW
		-	-	26.5	Collector side cooled	K/kW
		-	-	49	Emitter side cooled	K/kW
R _{thJK}	Thermal resistance junction to sink, Diode	-	-	29.2	Double side cooled	K/kW
		-	-	45.2	Cathode side cooled	K/kW
		-	-	84	Anode side cooled	K/kW
F	Mounting force	20	-	30		kN
W _t	Weight	-	1.2	-		kg

Notes:-

- 1) Unless otherwise indicated T_J=125°C.

Curves

Figure 1 – Typical collector-emitter saturation voltage characteristics

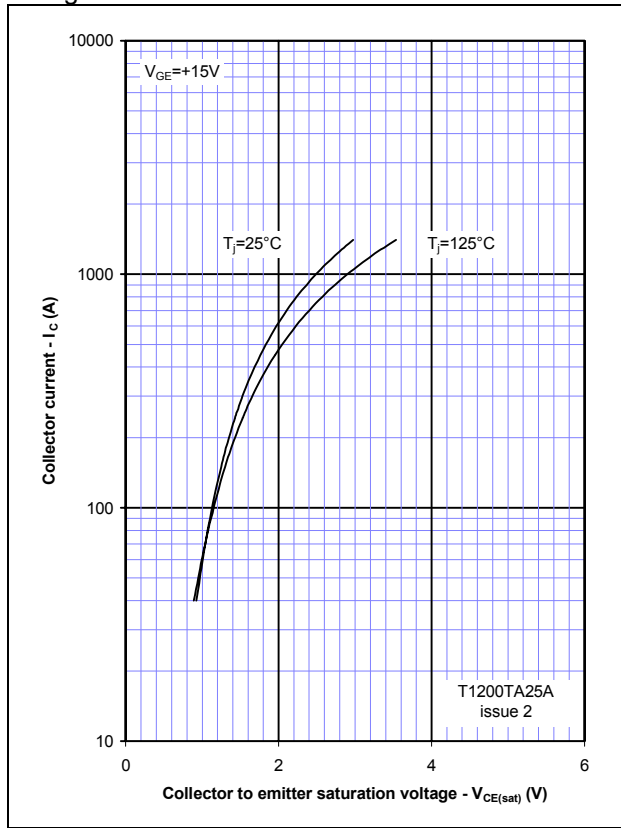


Figure 2 – Typical output characteristic at 25°C

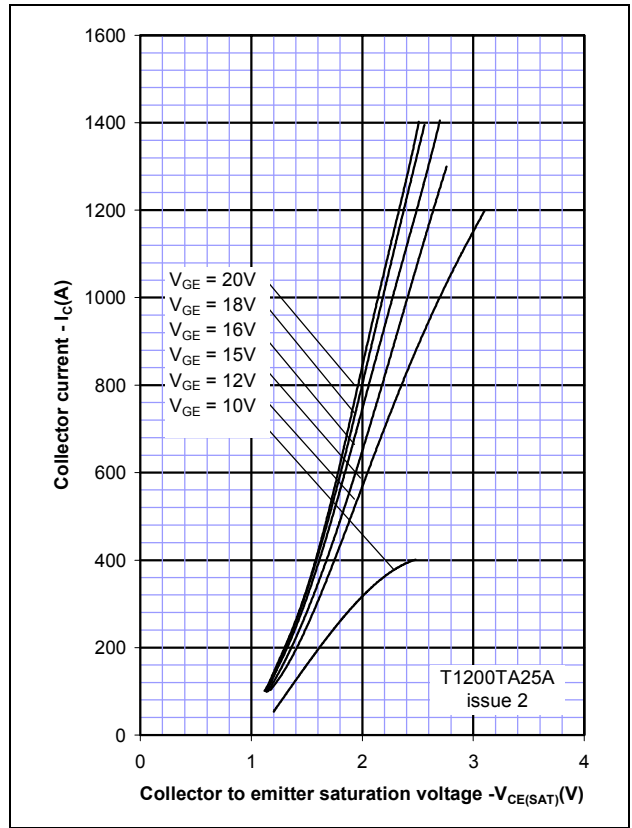


Figure 3 – Typical output characteristic at 125°C

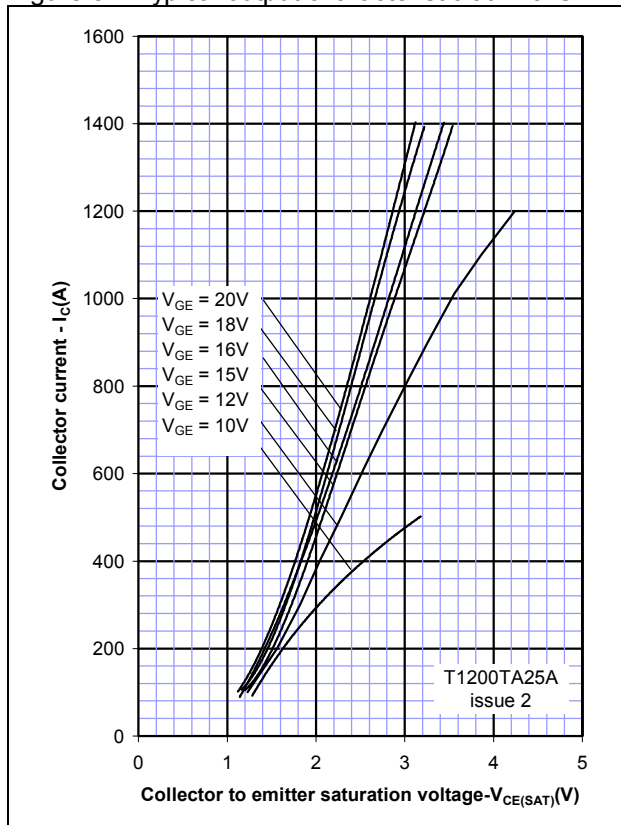


Figure 4 – Typical turn-on gate charge

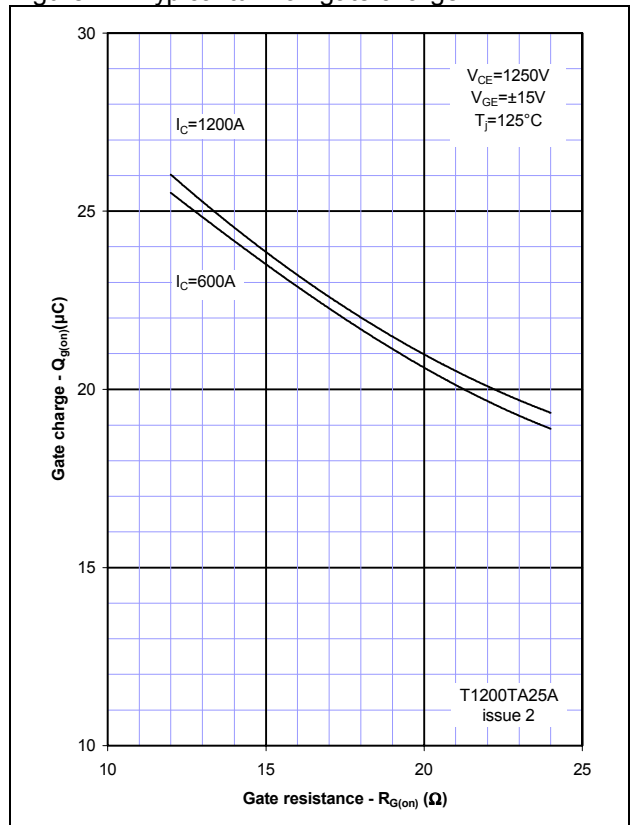


Figure 5 – Typical turn-off gate charge

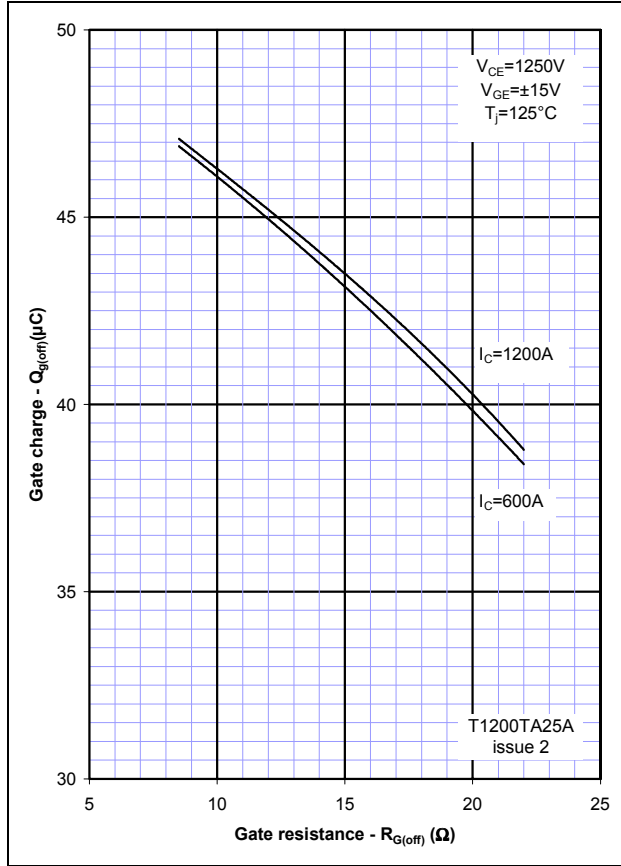


Figure 6 – Typical turn-on delay time vs gate resistance

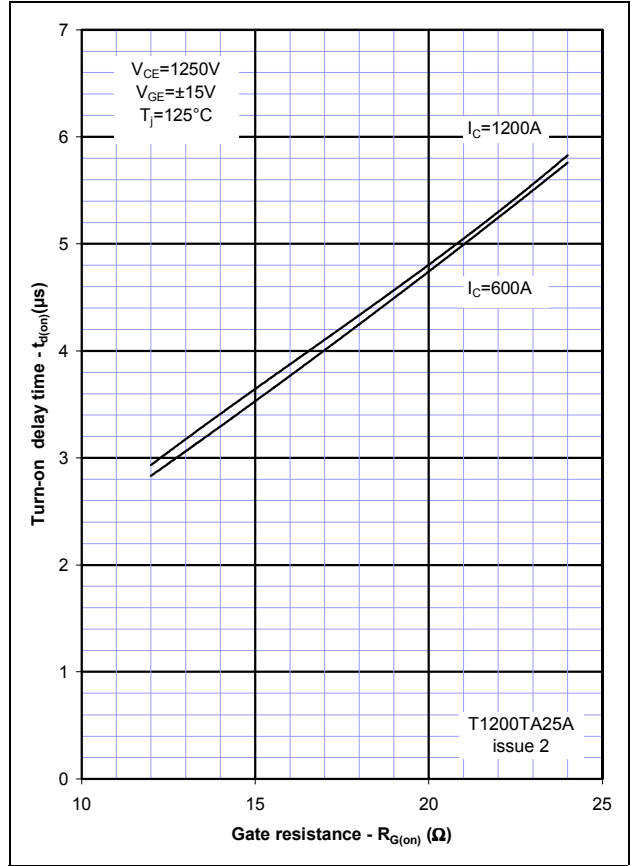


Figure 7 – Typical turn-off delay time vs. gate resistance

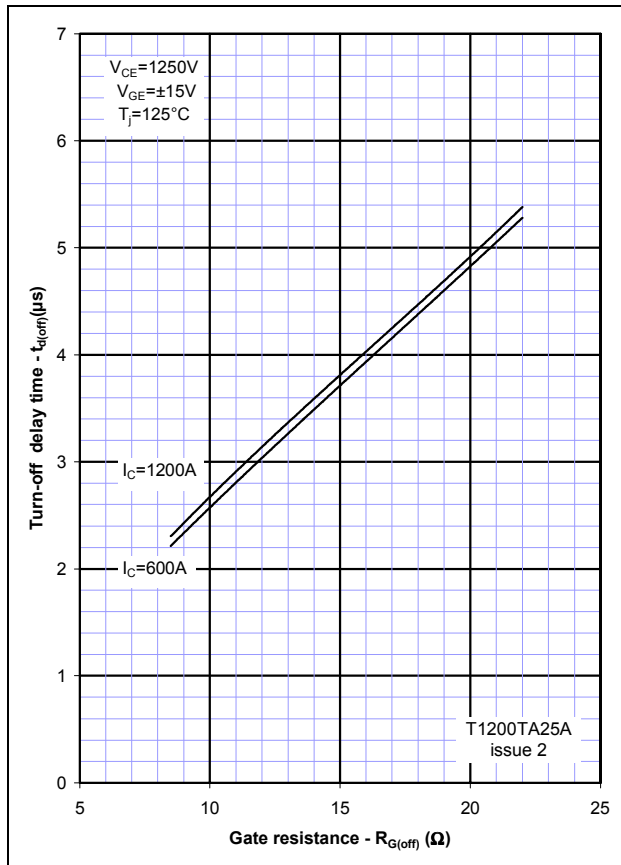


Figure 8 – Typical turn-on energy vs. collector current

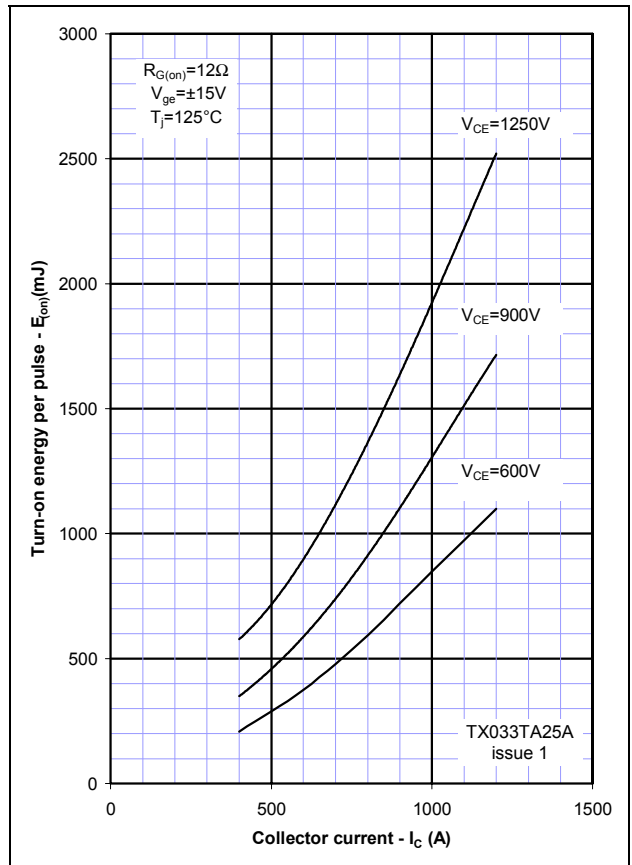


Figure 9 – Typical turn-on energy vs. di/dt

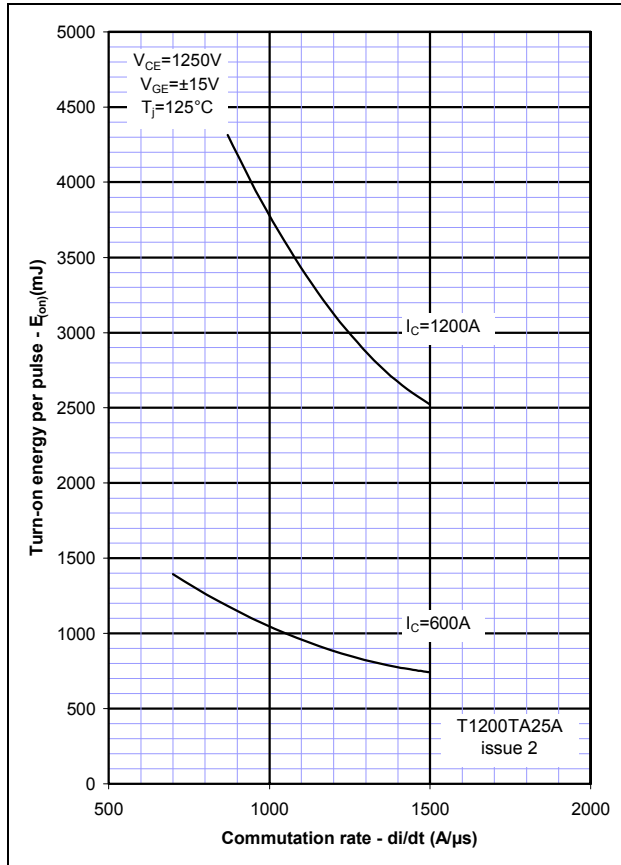


Figure 10 – Typical turn-off energy vs. collector current

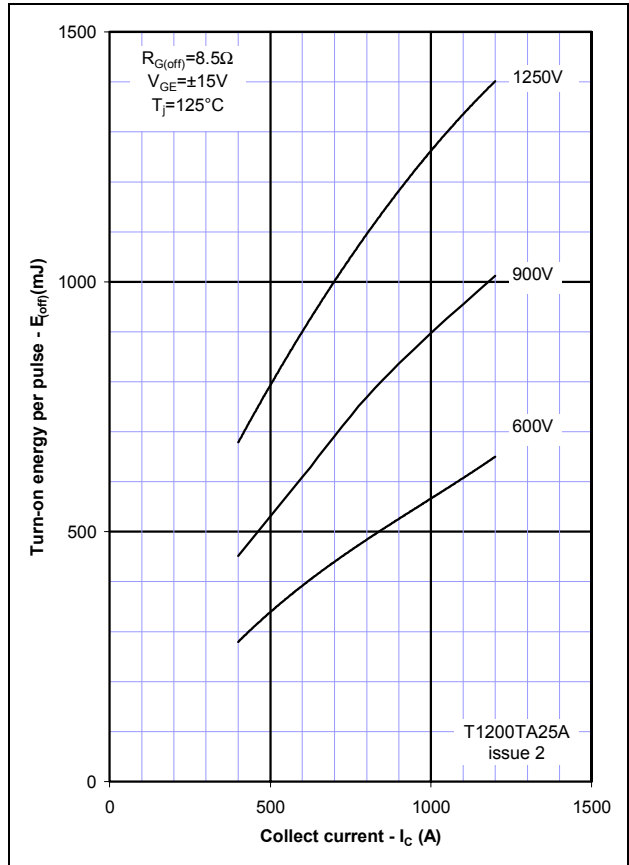


Figure 11 – Turn-off energy vs. voltage

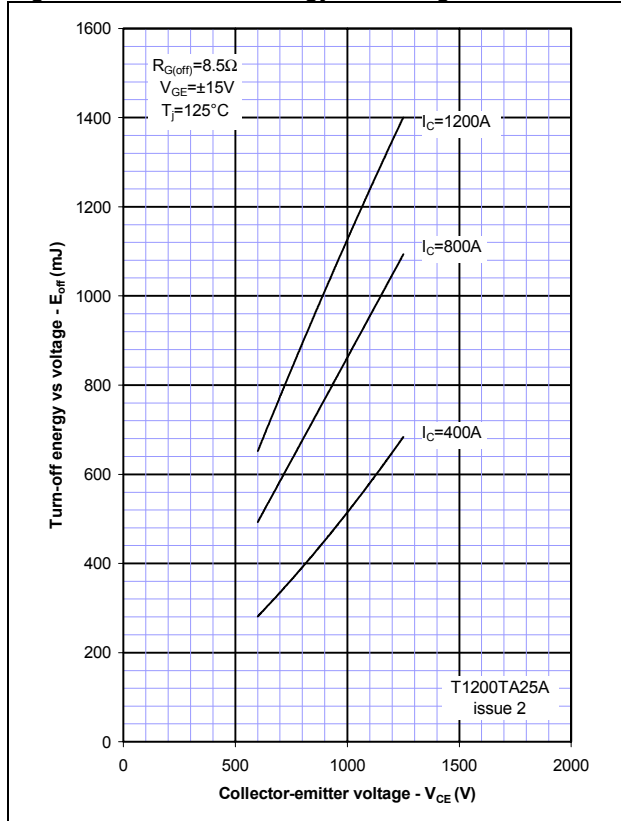


Figure 12 – Safe operating area

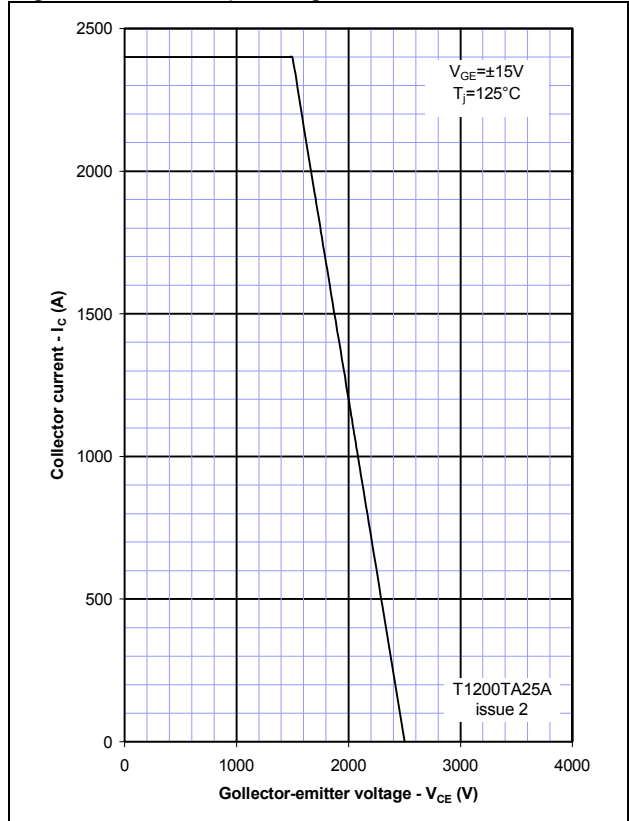


Figure 13 – Typical diode forward characteristic

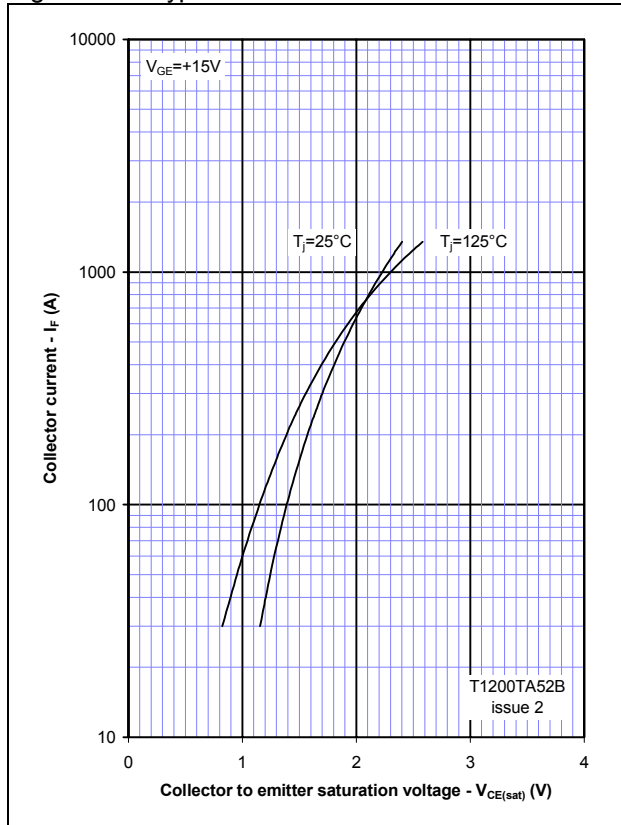


Figure 14 – Typical recovered charge

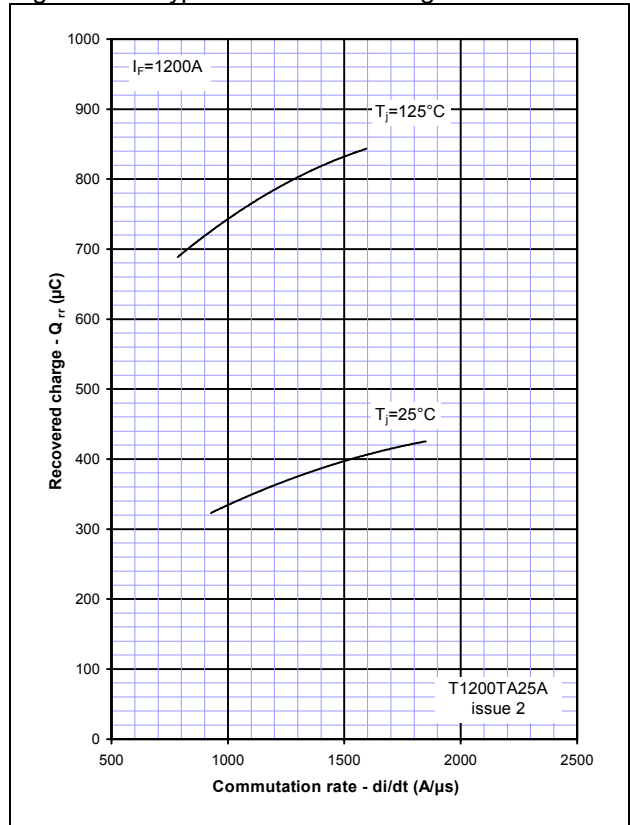


Figure 15 – Typical reverse recovery current

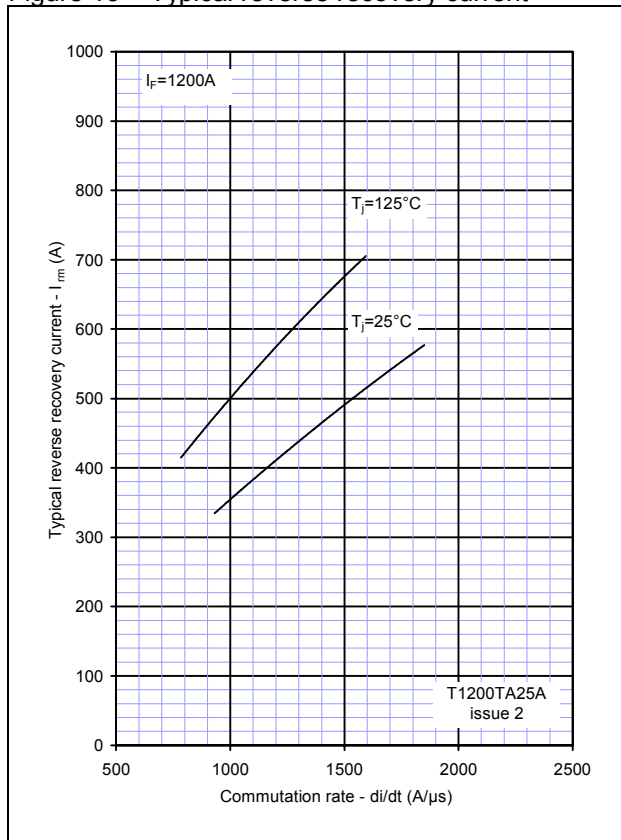


Figure 16 – Typical reverse recovery time

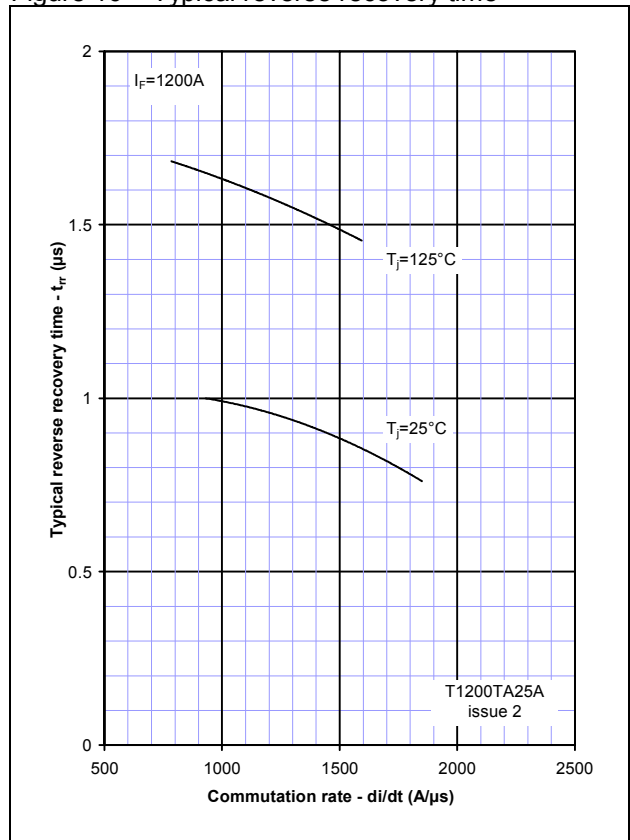


Figure 17– Transient thermal impedance (IGBT)

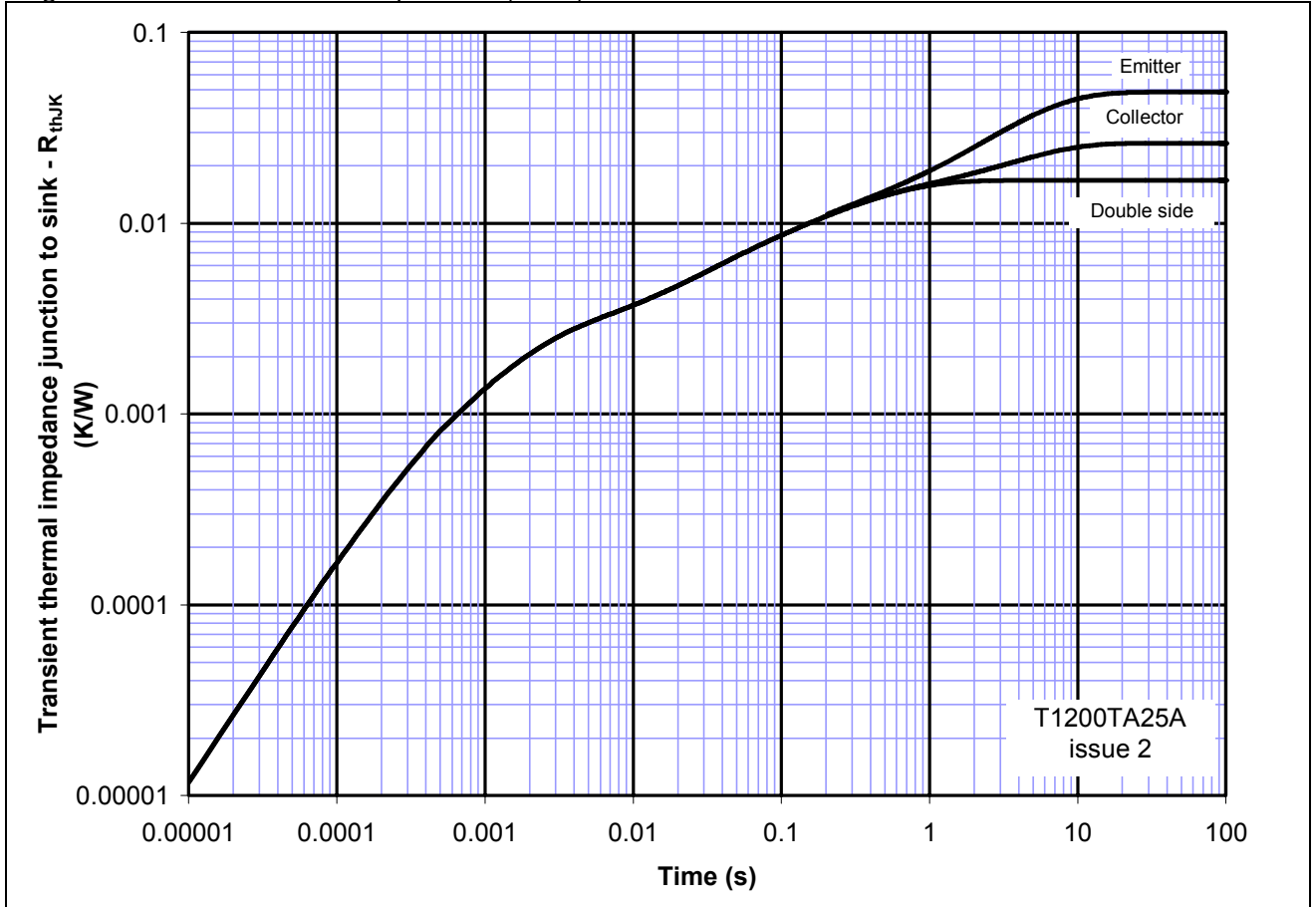
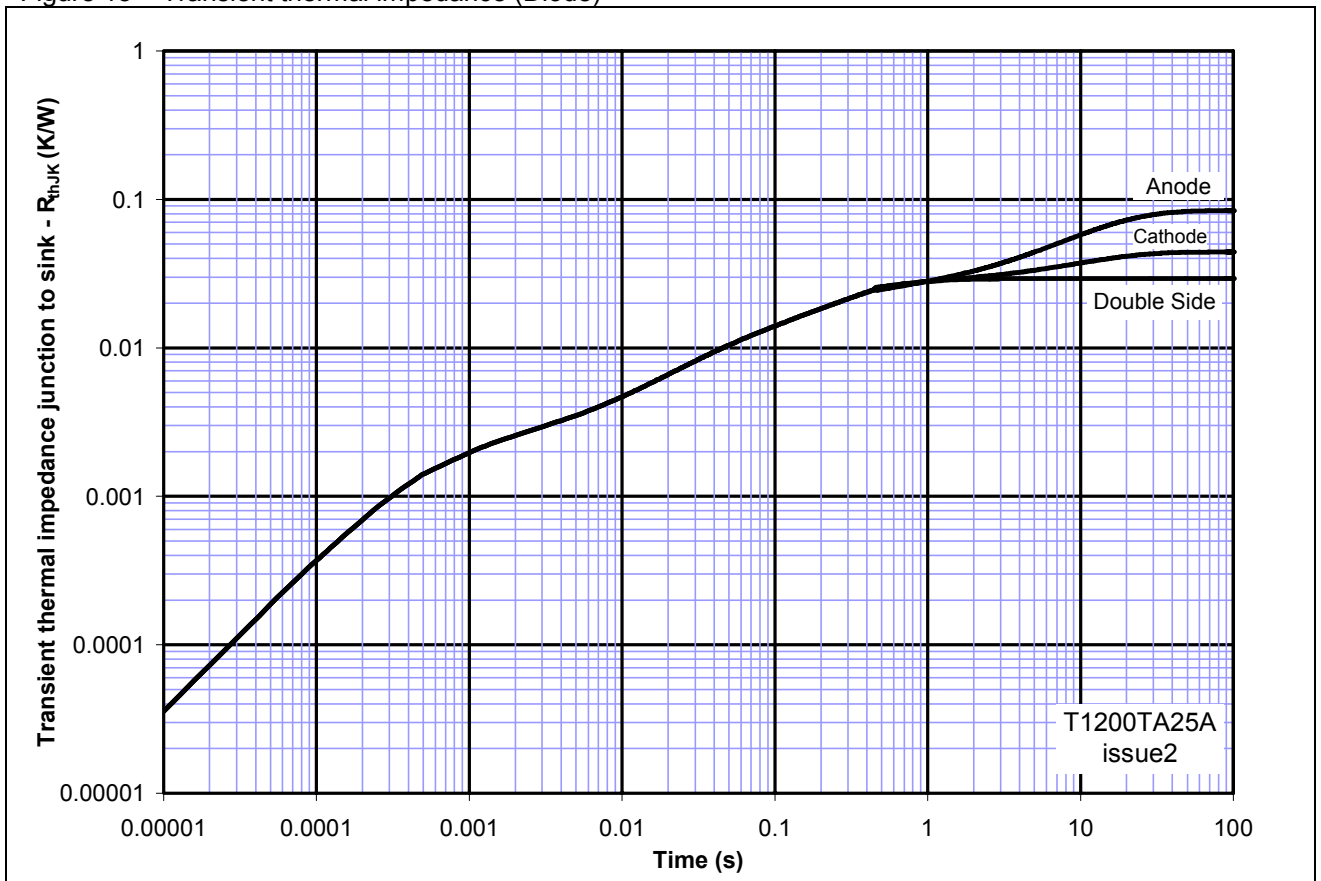
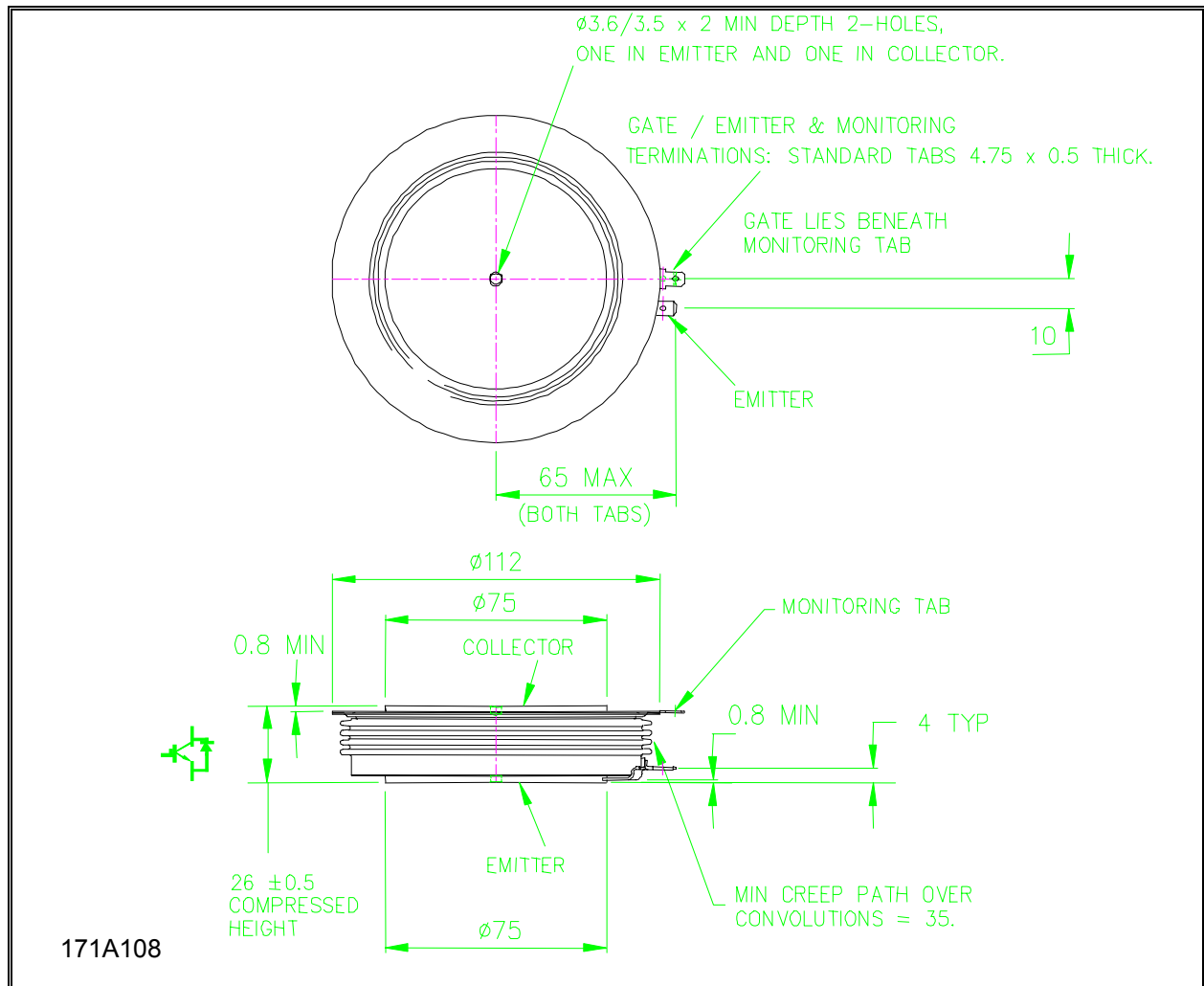


Figure 18 – Transient thermal impedance (Diode)



Outline Drawing & Ordering Information



ORDERING INFORMATION (Please quote 10 digit code as below)

T1200	TA	25	A
Fixed type code	Fixed Outline Code	Voltage Grade 2500V	Fixed format diode

Typical order code: T1200TA25A ($V_{CES} = 2500V$)

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