Soft Recovery Diode Types M0437WC080 to M0437WC140

The data sheet on the subsequent pages of this document is a scanned copy of existing data for this product.

(Rating Report 80NR1 Issue 3)

This data reflects the old part number for this product which is: SM02-14CXC170. This part number must **NOT** be used for ordering purposes – please use the ordering particulars detailed below.

Limitations of this data are as follows: This device is only available for grades 08 to 14 (800V to 1400V V_{RRM})

The following link will direct you to the appropriate outline drawing Outline W1

Where any information on the product matrix page differs from that in the following data, the product matrix must be considered correct

An electronic data sheet for this product is presently in preparation.

For further information on this product, please contact your local ASM or distributor.

Alternatively, please contact Westcode as detailed below.

Ordering Particulars				
M0437	WC	**	0	
Fixed Type Code	Fixed Outline Code	Voltage code V _{RRM} /100 08-14	Fixed Code	
Typical Order Code: M0437WC140, 14mm clamp height, 1400V V _{RRM}				

IXYS Semiconductor GmbH

Edisonstraße 15 D-68623 Lampertheim Tel: +49 6206 503-0 Fax: +49 6206 503-627

E-mail: marcom@ixys.de

IXYS Corporation

3540 Bassett Street Santa Clara CA 95054 USA Tel: +1 (408) 982 0700 Fax: +1 (408) 496 0670 E-mail: sales@ixys.net

An **IXYS** Company

www.westcode.com

www.ixvs.com

Westcode Semiconductors Ltd

Langley Park Way, Langley Park, Chippenham, Wiltshire, SN15 1GE. Tel: +44 (0)1249 444524 Fax: +44 (0)1249 659448 E-mail: WSL.sales@westcode.com

Westcode Semiconductors Inc

3270 Cherry Avenue Long Beach CA 90807 USA Tel: +1 (562) 595 6971 Fax: +1 (562) 595 8182 E-mail: WSI.sales@westcode.com

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In the interest of product improvement, Westcode reserves the right to change specifications at any time without prior notice

Devices with a suffix code (2-letter, 3-letter or letter/digit/letter combination) added to their generic code are not necessarily subject to the conditions and limits contained in this report.

QUALITY EVALUATION LABORATORY

Rating Report: 80NR1 (Issue 3) Date: 18th November, 1991

Origin:

Pages: 22

Diode Type SM02-14CXC170

Written by: MW. Junlop

Checked: MW.) Approved:

This diode consists of a diffused, fast recovery 24 mm diameter silicon slice mounted in a cold weld capsule housing. This Report supersedes Rating report 80NR1: (Issue 2).

Ratings

Voltage Grades : 02-14 V_{RSM} : 300-1500V VRRM : 200-1400V $I_{F(AV)}$: Single phase; 50 Hz, 180° half sinewave; (Converter Ratings): Double Side Cooled ${}^{T}HSE = 55^{\circ}C$, $100^{\circ}C$: 438A, 202A Single Side Cooled $^{T}HS = 100^{\circ}C$: 114A $I_{F(rms)}$ $T_{HS} = 25^{\circ}C$: 880A) Double side cooled T_{HS} = 25⁰C : 727A I_{ESM} : .t = 10ms half sinewave; T_J (initial) = 125°C; $V_{RM} = 0.6V_{RRM(MAX)}$: 4500A I_{FSM} : t = 10ms half sinewave; T_J (initial) = 125°C; V_{RM} : 4950A I^2t : t = 10ms; T_J (initial = 125°C; V_{RM} = 0.6V_{RRM} (MAX) : 1.01 x 10⁵A I^2t : t = 10ms; $T_{.1}$ (initial) = 125°C; $V_{RM} \leq 10V$: 1.22 x 10⁵A⁶ I^2 t : t = 3ms; T_J (initial = 125°C; $V_{RM} \leq 10V$ $: 0.91 \times 10^5 \text{ A}^3$ T_{HS} Operating Ramge : -40 to +125 T_{stq}; Non-operating : -40 to +150

Characteristics

(Maximum values unless stated otherwise)

$V_{D} : T_{.1} = 125^{\circ}C$: 1.02V
$r_s : T_J = 125$ °C	: 0.7mohms
$V_{FM} : I_{FM} = 635A T_{VJ} = 125^{\circ}C$: 1.46V
R _{+b} (J-HS) Double side cooled	: 0.09°C/W
Single side cooled	: 0.18°C/W
I_{RRM} : $T_J = 125 ^{\circ} C_{RM} = V_{RRM(MAX)}$: 20mA
Q_{rr} : $I_{FM} = 550A$; $dI/dt = 40A/uS$ (50% chord value)	:
$V_{RM} = 50V T_{V.I} = 125^{\circ}C$: 75uC
t _{rr} (conditions as above)	: 3uS
Mounting Force	: 330-550kg.f
Outline Drawing	: 10CA241
JEDEC No.	: D0200AA

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CHANGES TO 80NR1 (Issue 2)

Voltage Grade increased from 12 to 14.

Voltage Ratings

Voltage Class	V _{RRM} V	V _{RSM} V
02	200	300
04	400	500
06	600	700
08	800	900
10	1000	1100
12	1200	1300
14	1400	1500

This Report is applicable to higher or lower voltage grades when supply has been agreed by Sales/Production.

2.0 INTRODUCTION

The diode series comprises fast recovery cold-weld capsules with all diffused silicon slices. All these diodes have controlled reverse recovery characteristics with good "S" factors.

3.0 NOTES ON THE RATINGS

(a) Square wave ratings

These ratings are given for leading edge linear rates of rise of forward current of 200 $\,$ and $\,100 A/u S$

(b) Energy per pulse characteristics

These curves enable rapid estimation of device dissipation to be obtained for conditions not covered by the frequency ratings.

Let: Ep be the Energy per pulse for a given current and pulse width, in joules.

Then
$$W_{AV} = Ep \times f$$
.

and
$$T_{SINK} = T_{J(MAX)} - W_{AV} R_{th}$$

4.0 REVERSE RECOVERY LOSS

On account of the number of circuit variables affecting reverse recovery voltage, no allowance for reverse recovery loss has been made in these ratings. The following procedure is recommended for use where it is necessary to include reverse recovery loss.

(a) Determination by Measurement

From waveforms of recovery current obtained from a high frequency shunt (see Note 1) and reverse voltage present during recovery, an instantaneous reverse recovery loss waveform must be constructed. Let the area under this waveform be A joules per pulse. A new heat sink temperature can then be evaluated from:

$$T_{SINK}$$
 (new) = T_{SINK} (original) - A $(\frac{r_{t}.10^{6}}{t} + R_{th} \times f)$
where r_{t} = $1.64 \times 10^{-4} \sqrt{t}$

t = duration of reverse recovery loss per pulse in microseconds

A = Area under reverse loss waveform per pulse in joules (W.S.)

f = rated frequency at the original heat sink temperature

The total dissipation is now given by

$$W(TOT) = W(original) + Axf$$

NOTE 1

REVERSE RECOVERY LOSS BY MEASUREMENT

This device has a low reverse recovered charge and peak reverse recovery current. When measuring the charge care must be taken to ensure that:

- (a) a.c. coupled devices such as current transformers are not affected by prior passage of high amplitude forward current.
- (b) The measuring oscilloscope has adequate dynamic range typically 100 screen heights - to cope with the initial forward current without overload.

(c) Measurement of reverse recovery voltage waveform should be carried out with an appropriate snubber of 0.1uF and 10 ohms in series connected across diode anode to cathode.

(b) <u>Design Method</u>

In circumstances where it is not possible to measure voltage and current conditions, or for design purposes, the additional losses may be estimated from curves on page 12.

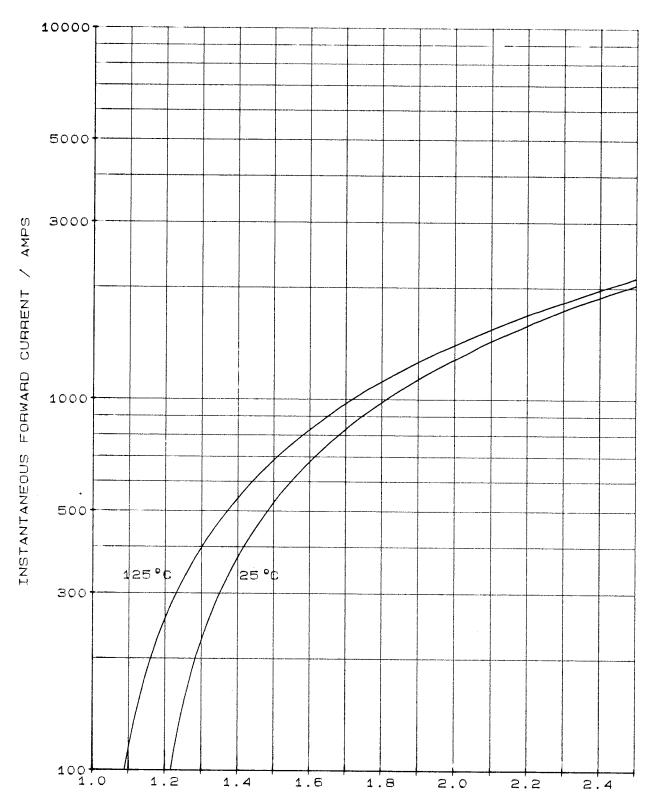
Let E be the value of energy per reverse cycle in joules (curves on page 12)

Let f be the operating frequency in ${\sf Hz}$

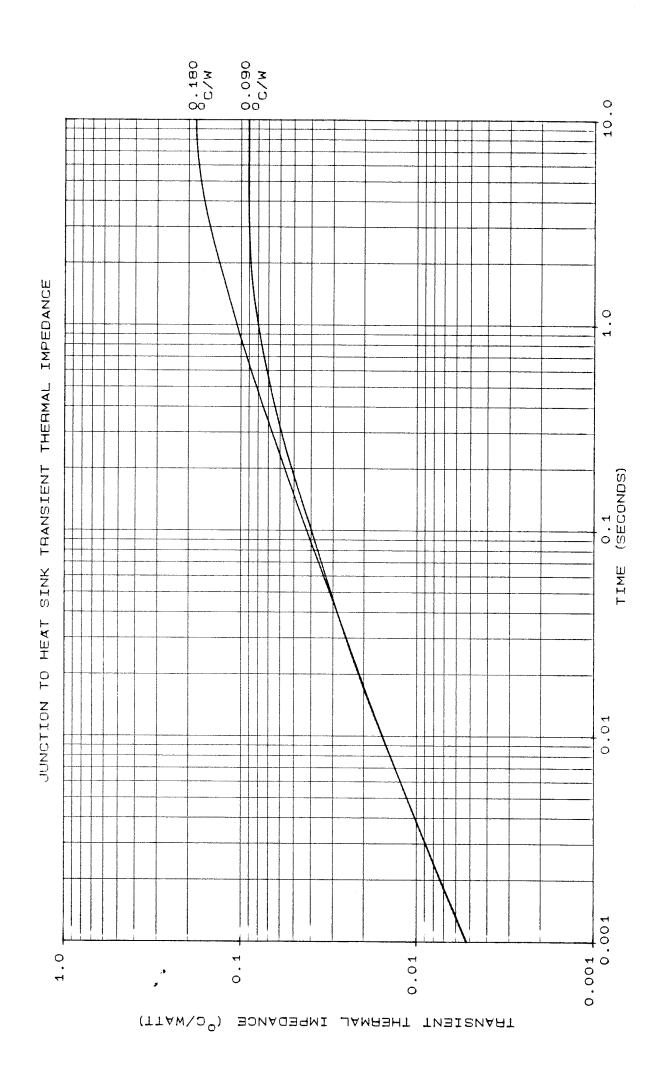
Then
$$T_{SINK}$$
 (new) = T_{SINK} (original) - E x R_{th} x f

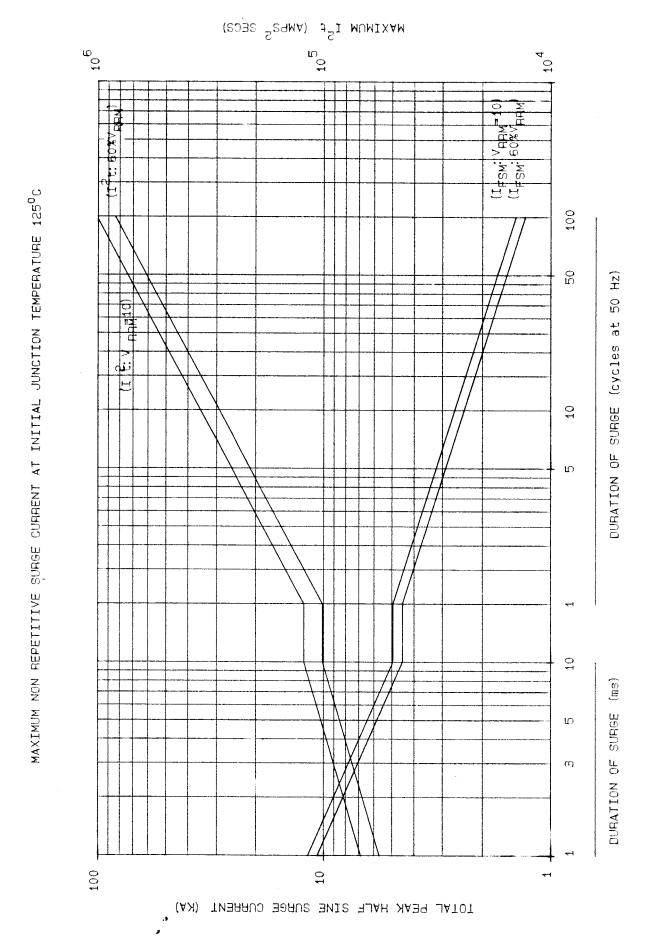
Where $\mathsf{T}_{\mathsf{SINK}}$ new is the required maximum heat sink temperature and $\mathsf{T}_{\mathsf{SINK}}$ original is the heat sink temperature given with the frequency ratings.

FORWARD CHARACTERISTIC OF LIMIT DEVICE



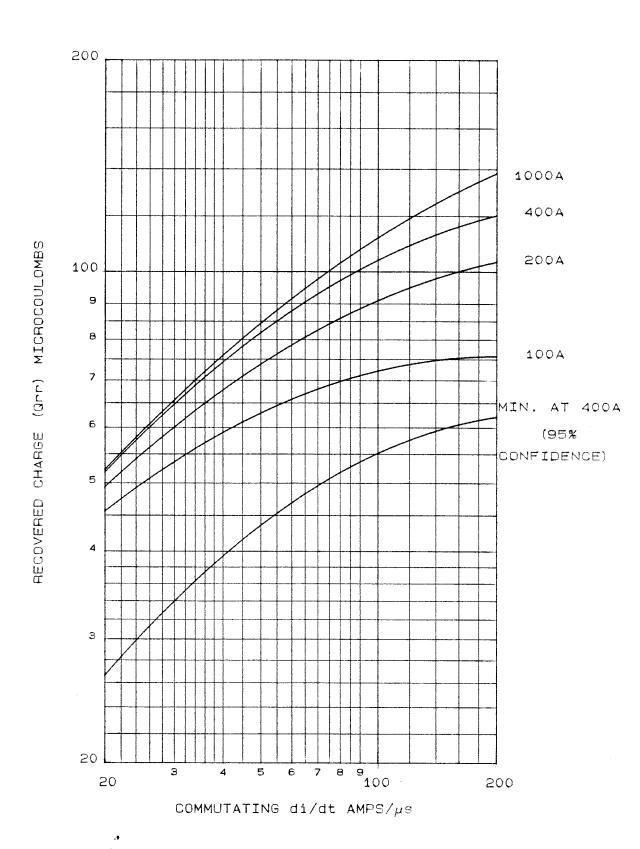
MAXIMUM, INSTANTANEOUS FORWARD VOLTAGE / VOLTS

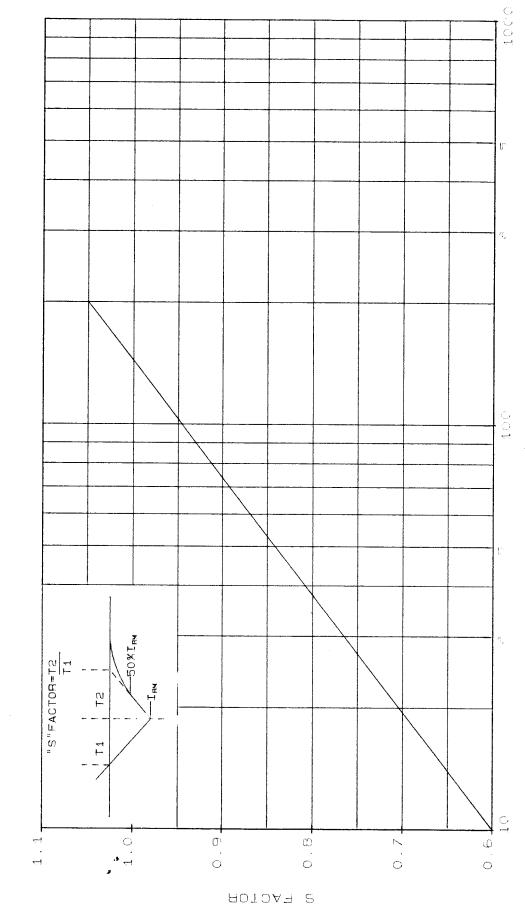




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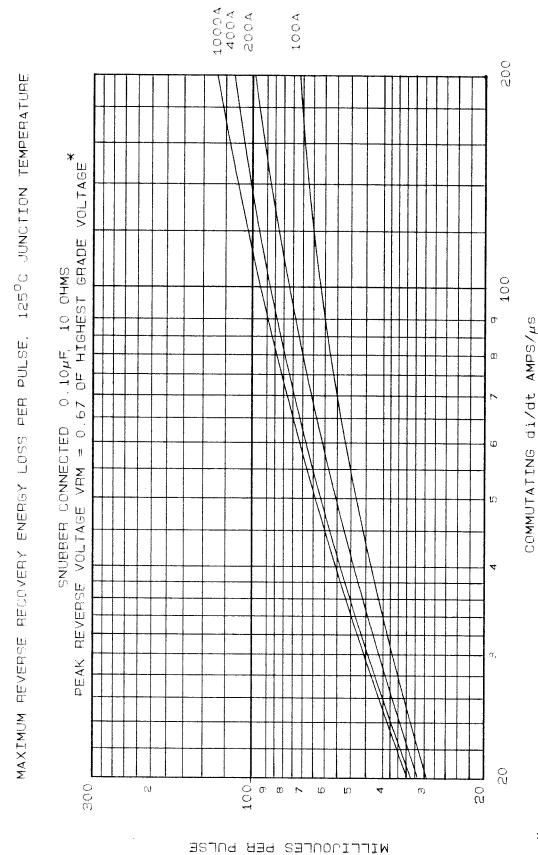
MAXIMUM RECOVERED CHARGE AT 125°C JUNCTION TEMPERATURE





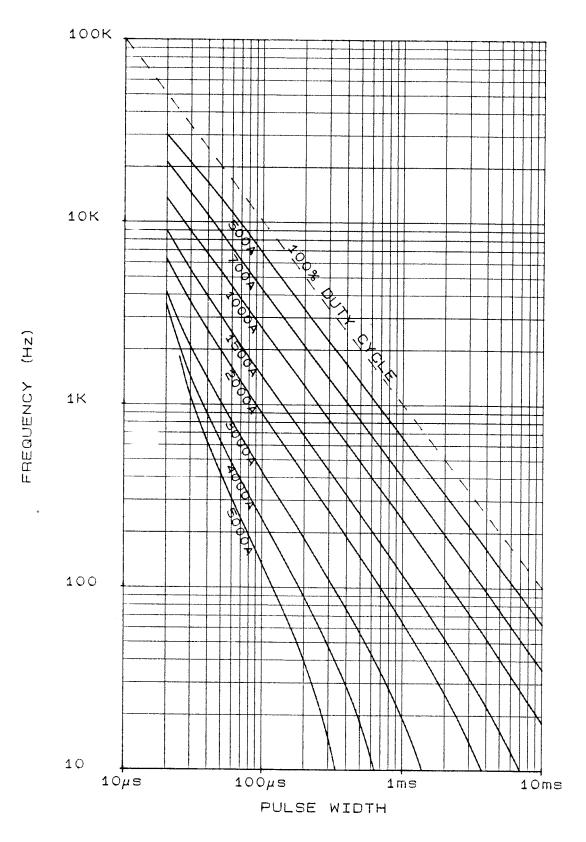
MINIMUM S FACTOR AT 1259C JUNCTION TEMPERATURE

RECOVERED CHARGE $\mu \mathrm{C}$

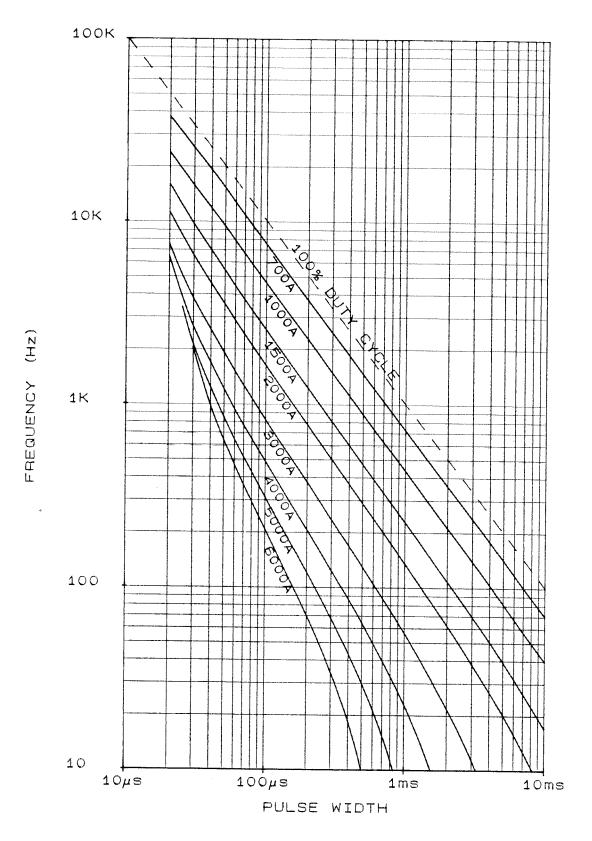


* Note: Fnersy per pulse should be adjusted pro rata with applied peak recovery voltage

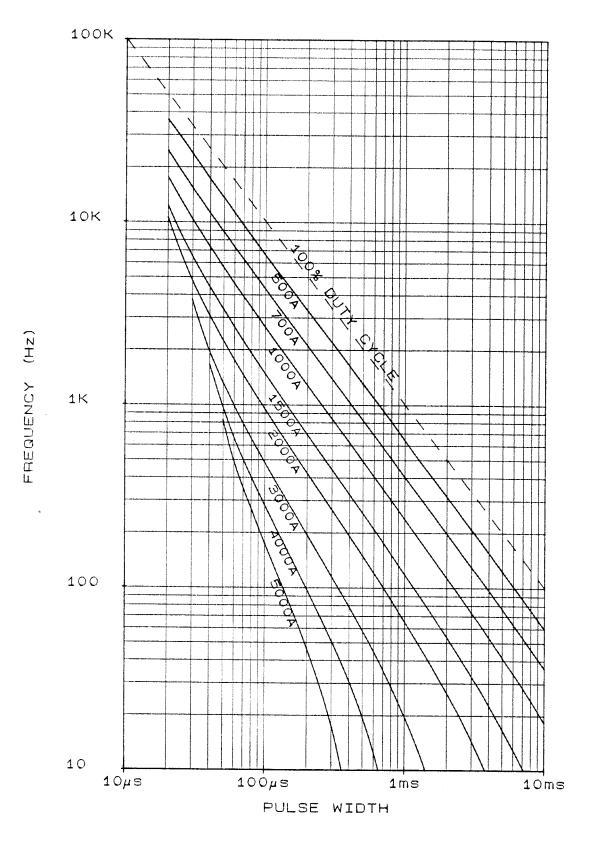
T SINK 85° C. $200A/\mu$ s



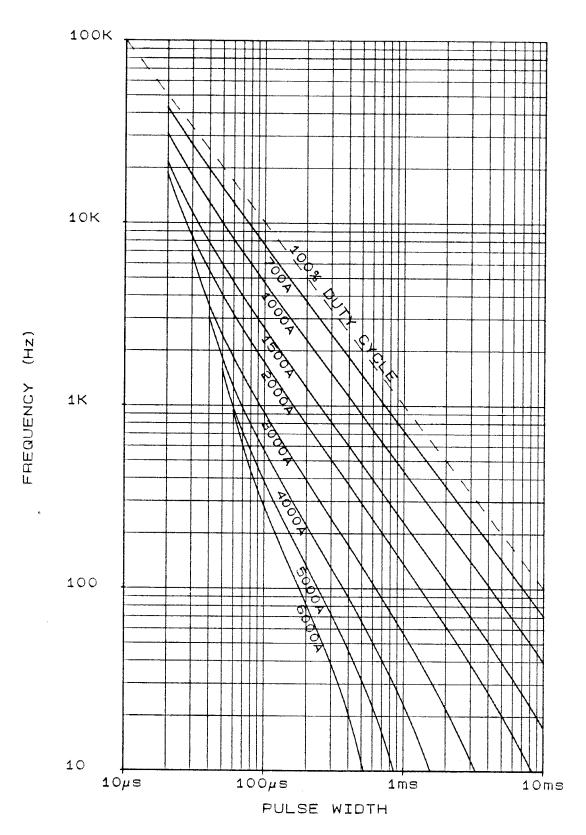
T SINK 55°C. 200A/µs



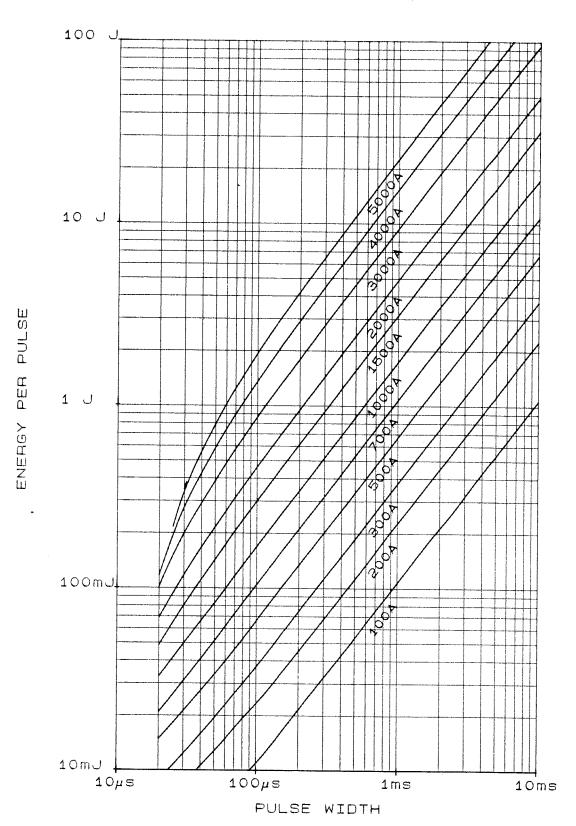
T SINK 85°C. 100A/µs



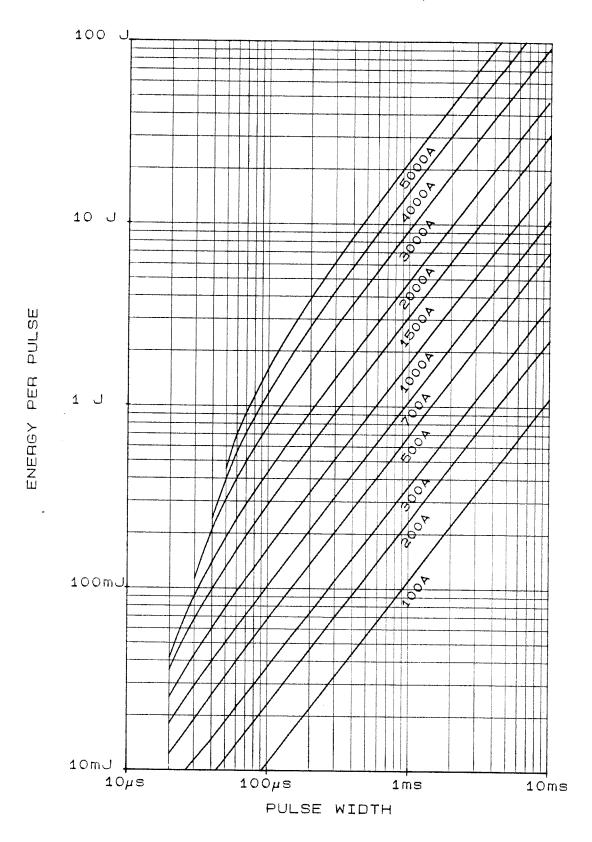
T SINK 55° C. $100A/\mu s$



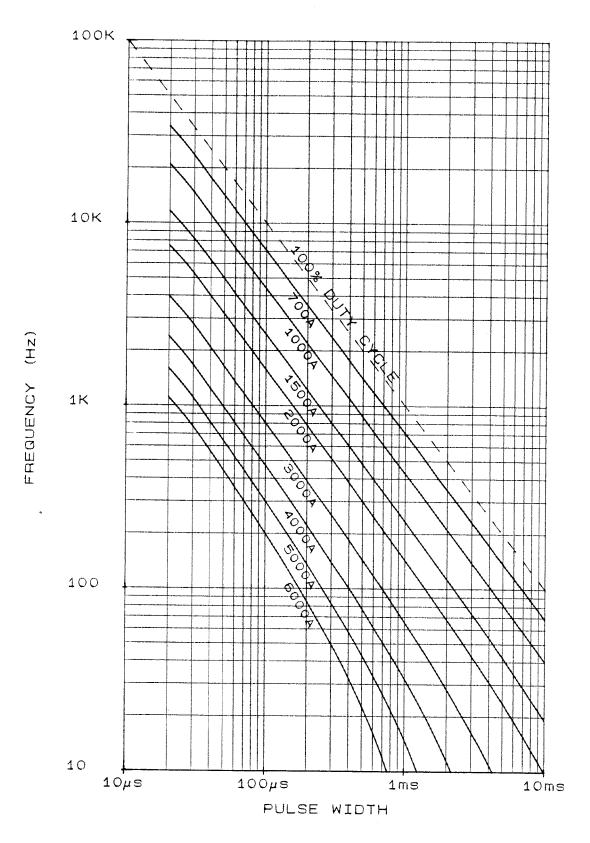
Tj 125⁰C. 200Α/μs

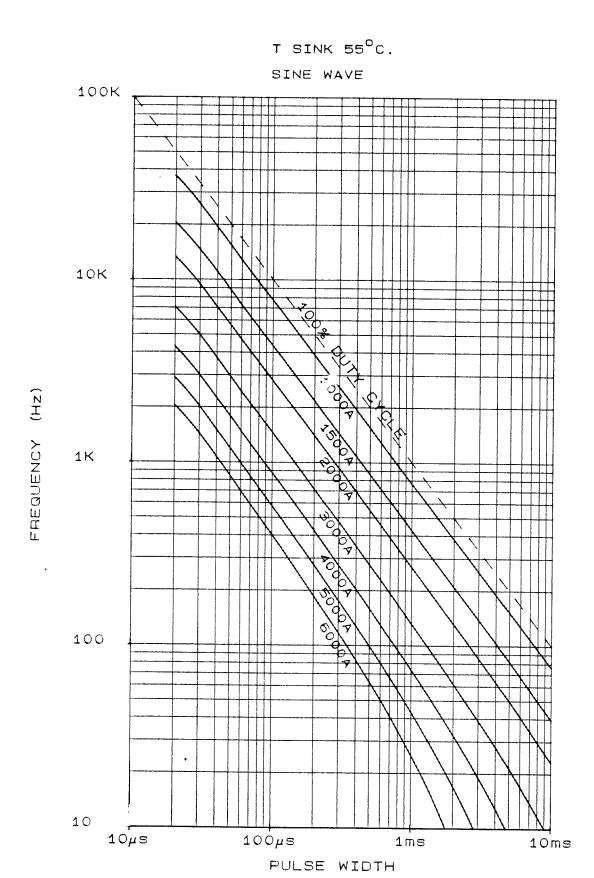


Tj 125° C. $100A/\mu s$

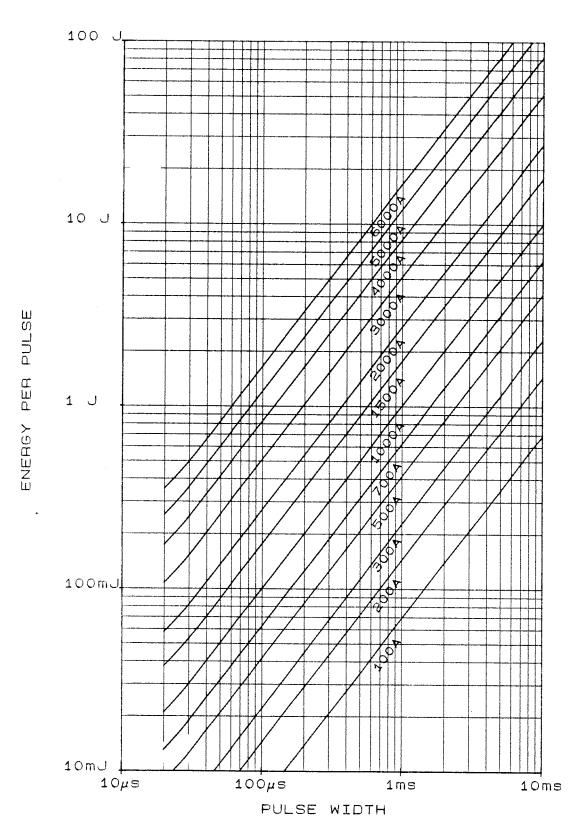


T SINK 85°C. SINE WAVE





Tj 125°C. SINE WAVE



INTERNATIONAL OUTLINE No. DO-200AA G.A. DWG No. 159B100H100-H110.

WEIGHT. 70 GRAMS

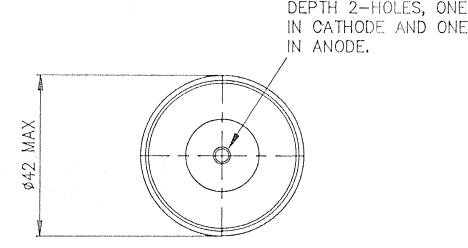
THE INFORMATION CONTAINED IN THIS DWG IS PROTECTED BY GOVERIGHT. THE INFORMATION MAY NOT BE DISCLOSED EXCEPT WITH THE WRITTEN PERMISSION OF WESTCODE

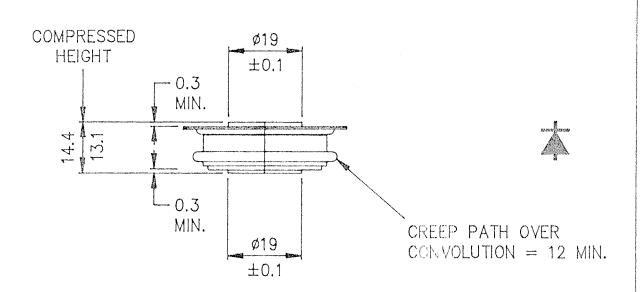
FINISH. NICKEL PLATE

DEVICE MOUNTING: CLAMPING FORCE TO BE APPLIED ON GENTRE LINE OF LOCATION HOLES AND BE EVENLY DISTRIBUTED OVER AREA OF CONTACT. FLAT TOL. ON SURFACES TO WHICH DEVICE IS CLAMPED TO BE 0.04 WIDE. CLAMPING FORCE = 330-550kgf.

TYPE NUMBER	ACAD_REF: 100A24
CXC300 CXC320 CXC380 CXC400 CXC470	CXC134 CXC144 CXC170 CXC174

 $\phi 3.6/3.5 \times 1.8$ MIN.





10-09-90 REDRAWN ON CAD HDN THIRD ANGLE PROJECTION. DWG. COMPLIES WITH BS 308. DIMNS. IN MILLIMETRES. DWG No. 100A241 ORAWN HON

WESTCODE SEMICONDUCTORS P.O. BOX 57, CHIPPENHAM, WILTSHIRE, SN15 1JL, ENGLAND. TELEX 44751 WESCDE G. TELEFAX 0248 6 58448.