



Description

Features

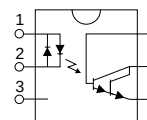
- ## Approvals

- ## Applications

- ## Ordering Information

Pin Configuration

LDA110 Pinout



Absolute Maximum Ratings (@ 25° C)

Parameter	Min	Typ	Max	Units
Input Power Dissipation	-	-	150 ¹	mW
Input Control Current	-	-	100	mA
Peak (10ms)	-	-	1	A
Reverse Input Voltage	-	-	5	V
Phototransistor	-	-	150 ²	mW
Power Dissipation				
Total Package Dissipation	-	-	800 ³	mW
Isolation Voltage				
Input to Output	3750	-	-	V _{RMS}
Operational Temperature	-40	-	+85	°C
Storage Temperature	-40	-	+125	°C
Soldering Temperature				
DIP Package	-	-	+260	°C
Surface Mount Package (10 Seconds Max.)	-	-	+220	°C

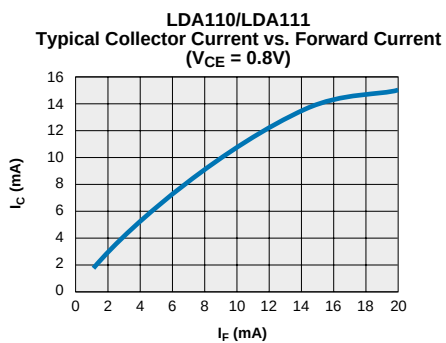
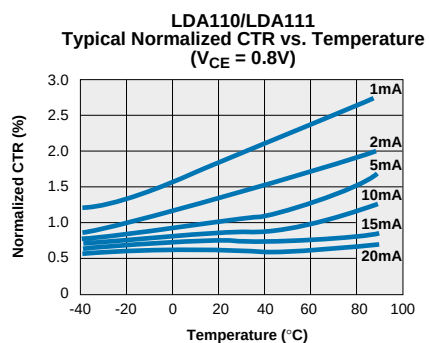
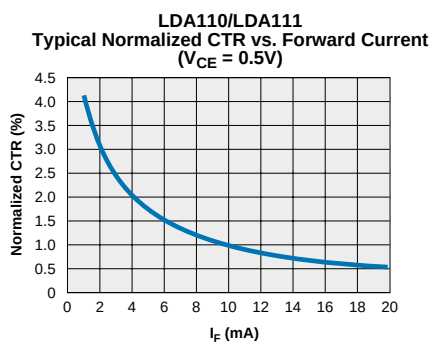
¹ Derate Linearly 1.33 mW/°C² Derate Linearly 2.0 mW/°C³ Derate Linearly 6.67 mW/°C

Absolute Maximum Ratings are stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this data sheet is not implied. Exposure of the device to the absolute maximum ratings for an extended period may degrade the device and effect its reliability.

Electrical Characteristics

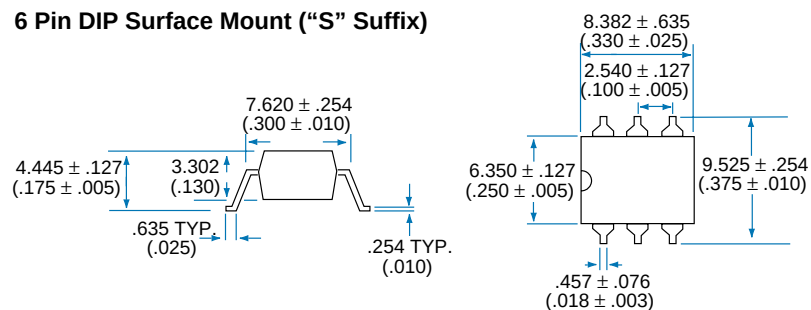
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNITS
Output Characteristics @ 25°C						
Phototransistor Blocking Voltage	$I_C=10\mu A$	BV_{CEO}	20	50	-	V
Phototransistor Output Current	$V_{CE}=5V, I_F=0mA$	I_{CEO}	-	50	500	nA
Saturation Voltage	$I_C=2mA, I_F=16mA$	V_{SAT}	-	-	-	V
	$I_C=.15mA, I_F=.05mA$		-	0.5	0.5	V
Current Transfer Ratio	$I_F=6mA, V_{CE}=0.5V$	CTR	300	1000	-	%
Output Capacitance	50V, f=1 MHz	C_{OUT}	-	3	-	pF
Capacitance						
Input to Output	-	-	-	-	-	-pF
Input Characteristics @ 25°C						
Input Control Current	$I_C=2mA, V_{CE}=0.5V$	I_F	2	1	100	mA
Input Voltage Drop	$I_F=5mA$	V_F	0.9	1.2	1.4	V
Input Reverse Voltage (LDA101, LDA111)	-	V_R	-	-	5	V
Input Reverse Current (LDA101, LDA111)	$V_R=5V$	I_R	-	-	10	nA
Common Characteristics @ 25°C						
Input to Output Isolation	-	$V_{I/O}$	3750	-	-	V _{RMS}

Performance Data

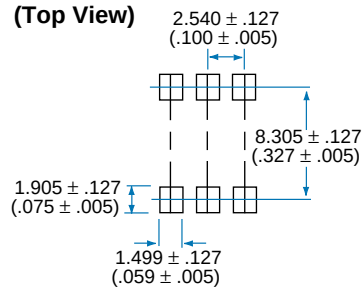


Mechanical Dimensions

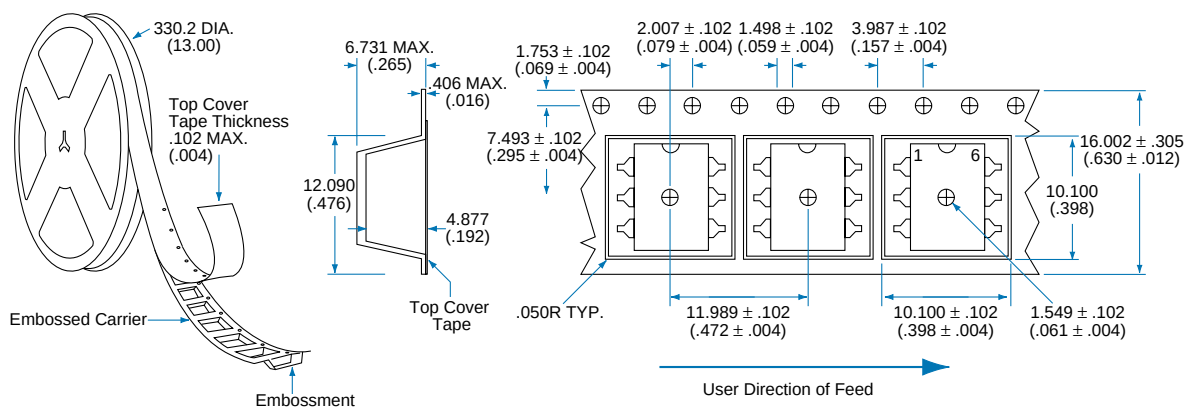
6 Pin DIP Surface Mount ("S" Suffix)



PC Board Pattern (Top View)



Tape and Reel Packaging for 6 Pin Surface Mount Package



Dimensions
mm
(inches)



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Specification: DS-LDA110-R3.0
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6/25/02