

**PART NUMBERING GUIDE**

**Environmental/Mechanical Specifications on page F5**

**SV G - 25 C 27 3 A B T - 40.000MHz**

5X7X1.7mm Max.  
G=4 Pad, H=6Pad

Frequency Stability  
100 = +/-100ppm, 50 = +/-50ppm,  
25 = +/-25ppm, 15 = +/-15ppm, 10 = +/- 10ppm

Frequency Pullability  
A = +/-10ppm, B = +/-30ppm, C = +/-50ppm  
D = +/-100ppm, E = +/- 150ppm

Operating Temperature Range  
Blank= 0°C to 70°C, 48 = -40°C to 85°C

Tristate Option  
Blank = No Connect, T = Tristate

Linearity  
A = 20%, B = 15%, C = 10%, D = 5%

Duty Cycle  
Blank = 40-60%, A= 45-55%

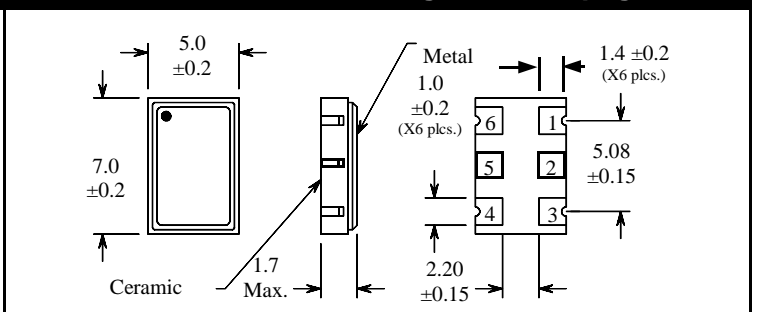
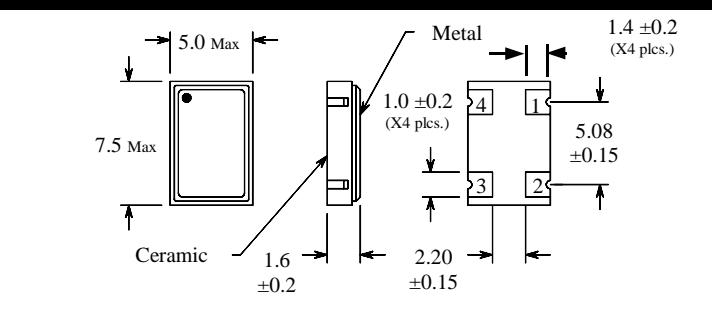
Input Voltage  
Blank = 5.0V, 3=3.3V

**ELECTRICAL SPECIFICATIONS**

<b>Frequency Range</b>	2.000MHz to 60.000MHz	
<b>Operating Temperature Range</b>	0°C to 70°C / -40°C to 85°C	
<b>Storage Temperature Range</b>	-55°C to 100°C	
<b>Supply Voltage</b>	5.0Vdc ±5% or 3.3Vdc ±5%	
<b>Aging (at 25°C)</b>	±3ppm / year Maximum	
<b>Load Drive Capability</b>	15pF HCMOS Load	
<b>Start Up Time</b>	10mSeconds Maximum	
<b>Linearity</b>	±20%, ±15%, ±10%, ±5% Maximum	
<b>Input Current</b>	2.000MHz to 20.000MHz 20.001MHz to 40.000MHz 40.001MHz to 60.000MHz	10mA Maximum 10mA Maximum (3.3V) 20mA Maximum 15mA Maximum (3.3V) 30mA Maximum 20mA Maximum (3.3V)
<b>Pin 2 Tri-State Input Voltage</b> or <b>Pin 5 Tri-State Input Voltage</b>	No Connection VIH: >2.0Vdc VIL: <0.8Vdc	Enables Output Enables Output Disables Output: High Impedance
<b>Pin 1 Control Voltage / Frequency Deviation</b>	2.5Vdc ±2.0Vdc 2.5Vdc ±2.5Vdc 1.65Vdc ±1.65Vdc (or ±1.5Vdc)	±10, ±, 30, ±50, ±100ppm Minimum ±10, ±, 30, ±50, ±100, ±150ppm Minimum ±10, ±, 30, ±50, ±100, ±150ppm Minimum
<b>One Sigma Clock Jitter</b>	<60.000MHz	±10pSeconds Maximum
<b>Absolute Clock Jitter</b>	<60.000MHz	±100pSeconds Maximum
<b>Frequency Tolerance / Stability</b>	Inclusive of Operating Temperature Range, Supply Voltage and Load	±50ppm, ±30ppm, ±20ppm (-10°C to 70°C max.), ±15ppm (-10°C to 70°C max.), ±10ppm (-10°C to 60° max.)
<b>Output Voltage Logic High (Voh)</b>	w/HCMOS Load	90% of Vdd Minimum
<b>Output Voltage Logic Low (Vol)</b>	w/HCMOS Load	10% of Vdd Maximum
<b>Rise Time / Fall Time</b>	0.4Vdc to 2.4Vdc w/TTL Load; 20% to 80% of Waveform w/HCMOS Load	5nSeconds Maximum
<b>Duty Cycle</b>	@ 1.4Vdc w/TTL Load; @50% w/HCMOS Load @ 1.4Vdc w/TTL Load or w/HCMOS Load	50 ±10% (Standard) 50±5% (Optional)
<b>Frequency Deviation Over Control Voltage</b>	A=±50ppm Min. / B=±100ppm Min. / C=±150ppm Min. / D=±200ppm Min. / E=±250ppm Min. / F=±300ppm Min. / G=±350ppm Min.	

**MECHANICAL DIMENSIONS**

**Marking Guide on page F3-F4**



Pin 1: Control Voltage (Vc)      Pin 2: Output  
Pin 4: Case Ground              Pin 3: Supply Voltage

Pin 1: Control Voltage (Vc)      Pin 2: Tri-State      Pin 3: Ground  
Pin 4: Output                      Pin 5: N.C.              Pin 6: Supply Voltage