

PART NUMBERING GUIDE

Environmental/Mechanical Specifications on page F5

OC-3.3- 100 48 A T - 30.000MHz

Package _____ OC-3.3 = 5X7X1.6mm / 3.3Vdc / HCMOS-TTL _____ **Pin One Connection**
T = Tri State Enable High

Inclusive Stability _____ **Output Symmetry**
Blank = 40/60%, A = 45/55%

Operating Temperature Range
Blank = 0°C to 70°C, 27 = -20°C to 70°C, 48 = -40°C to 85°C

100= +/-100ppm, 50= +/-50ppm, 30= +/-30ppm, 25= +/-25ppm,
20= +/-20ppm, 15= +/-15ppm, 10= ±10ppm (25,20,15,10= 0°C-70°C Only)

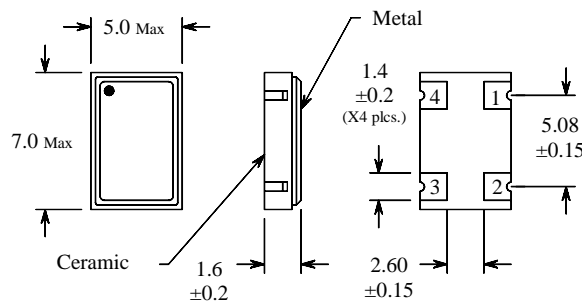
ELECTRICAL SPECIFICATIONS

Frequency Range	1.544MHz to 185.000MHz	
Operating Temperature Range	0°C to 70°C / -20°C to 70°C / -40°C to 85°C	
Storage Temperature Range	-55°C to 125°C	
Supply Voltage	3.3Vdc ±10%	
Input Current	1.544MHz to 36.000MHz 36,001MHz to 70.000MHz 70.001MHz to 185.000MHz	10mA Maximum 18mA Maximum 40mA Maximum
Frequency Tolerance / Stability	Inclusive of Operating Temperature Range, Supply Voltage and Load	±100ppm, ±50ppm, ±30ppm, ±25ppm, ±20ppm, ±15ppm or ±10ppm (25, 20, 15, 10 = 0°C to 70°C)
Output Voltage Logic High (Voh)	w/HCMOS or TTL Load	90% of Vdd Min. / Ioh=-8mA
Output Voltage Logic Low (Vol)	w/HCMOS or TTL Load	10% of Vdd Max. / Iol=8mA
Rise / Fall Time	10% to 90% of Waveform w/HCMOS Load; 0.4Vdc to 2.4V w/TTL Load / 5nSec Max.	
Duty Cycle	@ 1.4Vdc w/TTL Load; @ 50% w/HCMOS Load @ 1.4Vdc w/TTL Load or w/HCMOS Load	50 ±10% (Standard) 50±5% (Optional)
Load Drive Capability	<= 70.000MHz >70.000MHz <=70.000MHz (Optional)	10LSTTL Load or 15pF HCMOS Load 15pF HCMOS Load 10TTL Load or 50pF HCMOS Load
Pin 1 Tristate Input Voltage	No Connection VIH VIL	Enables Output +0.7Vdc Minimum to Enable Output +0.3Vdc Maximum to Disable Output
Aging (@ 25°C)	±5ppm / year Maximum	
Start Up Time	10mSeconds Maximum	
Absolute Clock Jitter	±100pSeconds Maximum	
One Sigma Clock Jitter	±25pSeconds Maximum	

MECHANICAL DIMENSIONS

Marking Guide on page F3-F4

All Dimensions in mm.



Pin 1: Tri-State
Pin 2: Case Ground

Pin 3: Output
Pin 4: Supply Voltage