			Phase Control Thyristor
V_{DRM}	=	4200 V	
$I_{T(AV)M}$	=	2075 A	
$I_{T(RMS)}$	=	3260 A	**5STP 18H4200**
I_{TSM}	=	32×10^3 A	
V_{T0}	=	0.96 V	
r_T	=	0.285 mW	

- **Patented free-floating silicon technology**
- **Low on-state and switching losses**
- **Designed for traction, energy and industrial applications**
- **Optimum power handling capability**
- **Interdigitated amplifying gate**

Blocking
Maximum rated values [1]

Parameter	Symbol	Conditions	5STP 18H4200	Unit
Max repetitive peak forward and reverse blocking voltage	V_{DRM}, V_{RRM}	$f = 50$ Hz, $t_p = 10$ ms, $T_{vj} = 5…125°C$, Note 1	4200	V
Critical rate of rise of commutating voltage	dv/dt_{crit}	Exp. to 2810 V, $T_{vj} = 125°C$	1000	V/µs
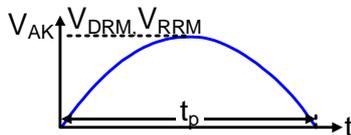
Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Forward leakage current	I_{DRM}	V_{DRM}, $T_{vj} = 125°C$			300	mA
Reverse leakage current	I_{RRM}	V_{RRM}, $T_{vj} = 125°C$			300	mA

Note 1: Voltage de-rating factor of 0.11% per °C is applicable for Tvj below +5 °C

Mechanical data
Maximum rated values [1]

Parameter	Symbol	Conditions	min	typ	max	Unit
Mounting force	F_M		45	50	60	kN
Acceleration	a	Device unclamped			50	m/s^2
Acceleration	a	Device clamped			100	m/s^2

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Weight	m				0.9	kg
Housing thickness	H	$F_M = 50$ kN, $T_a = 25$ °C	26		26.6	mm
Surface creepage distance	D_S		36			mm
Air strike distance	D_a		15			mm

1) Maximum rated values indicate limits beyond which damage to the device may occur

ABB Switzerland Ltd, Semiconductors reserves the right to change specifications without notice.

On-state

Maximum rated values [1]

Parameter	Symbol	Conditions	min	typ	max	Unit
Average on-state current	$I_{T(AV)M}$	Half sine wave, T_c = 70 °C			2075	A
RMS on-state current	$I_{T(RMS)}$				3260	A
Peak non-repetitive surge current	I_{TSM}	t_p = 10 ms, T_{vj} = 125 °C, sine wave after surge: $V_D = V_R$= 0 V			32×10^3	A
Limiting load integral	I^2t				5.12×10^6	A^2s
Peak non-repetitive surge current	I_{TSM}	t_p = 8.3 ms, T_{vj} = 125 °C, sine wave after surge: $V_D = V_R$= 0 V			35×10^3	A
Limiting load integral	I^2t				5×10^6	A^2s

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
On-state voltage	V_T	I_T = 2000 A, T_{vj} = 125 °C			1.53	V
Threshold voltage	$V_{(T0)}$	I_T = 1000 A - 3000 A, T_{vj}= 125 °C			0.96	V
Slope resistance	r_T				0.285	mΩ
Holding current	I_H	T_{vj} = 25 °C			80	mA
		T_{vj} = 125 °C			60	mA
Latching current	I_L	T_{vj} = 25 °C			600	mA
		T_{vj} = 125 °C			200	mA

Switching

Maximum rated values [1]

Parameter	Symbol	Conditions		min	typ	max	Unit
Critical rate of rise of on-state current	di/dt_{crit}	T_{vj} = 125 °C, I_{TRM} = 3000 A,	Cont. f = 50 Hz			200	A/µs
Critical rate of rise of on-state current	di/dt_{crit}	$V_D \leq 2810$ V, I_{FG} = 2 A, t_r = 0.5 µs	Cont. f = 1Hz			1000	A/µs
Circuit-commutated turn-off time	t_q	T_{vj} = 125°C, I_{TRM} = 2000 A, V_R = 200 V, di_T/dt = -1.5 A/µs, $V_D \leq 0.67 \cdot V_{DRM}$, dv_D/dt = 20 V/µs		600			µs

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Reverse recovery charge	Q_{rr}	T_{vj} = 125°C, I_{TRM} = 2000 A, V_R = 200 V, di_T/dt = -1.5 A/µs	1800		2800	µAs
Reverse recovery current	I_{RM}		30		70	A
Gate turn-on delay time	t_{gd}	T_{vj} = 25 °C, V_D = 0.4·V_{RM}, I_{FG} = 2 A, t_r = 0.5 µs			3	µs

Triggering

Maximum rated values [1]

Parameter	Symbol	Conditions	min	typ	max	Unit
Peak forward gate voltage	V_{FGM}				12	V
Peak forward gate current	I_{FGM}				10	A
Peak reverse gate voltage	V_{RGM}				10	V
Average gate power loss	$P_{G(AV)}$			see Fig. 9		W

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Gate-trigger voltage	V_{GT}	T_{vj} = 25 °C			2.6	V
Gate-trigger current	I_{GT}	T_{vj} = 25 °C			400	mA
Gate non-trigger voltage	V_{GD}	V_D = 0.4 x V_{DRM}, T_{vjmax} = 125 °C	0.3			V
Gate non-trigger current	I_{GD}	V_D = 0.4 x V_{DRM}, T_{vjmax} = 125°C	10			mA

Thermal

Maximum rated values [1]

Parameter	Symbol	Conditions	min	typ	max	Unit
Operating junction temperature range	T_{vj}				125	°C
Storage temperature range	T_{stg}		-40		140	°C

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Thermal resistance junction to case	$R_{th(j-c)}$	Double-side cooled F_m = 45...60 kN			10	K/kW
	$R_{th(j-c)A}$	Anode-side cooled F_m = 45...60 kN			20	K/kW
	$R_{th(j-c)C}$	Cathode-side cooled F_m = 45...60 kN			20	K/kW
Thermal resistance case to heatsink	$R_{th(c-h)}$	Double-side cooled F_m = 45...60 kN			2	K/kW
	$R_{th(c-h)}$	Single-side cooled F_m = 45...60 kN			4	K/kW

Analytical function for transient thermal impedance:

$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_i (1 - e^{-t/t_i})$$

i	1	2	3	4
R_i(K/kW)	6.520	1.550	1.670	0.490
τ_i(s)	0.4562	0.0792	0.0088	0.0037
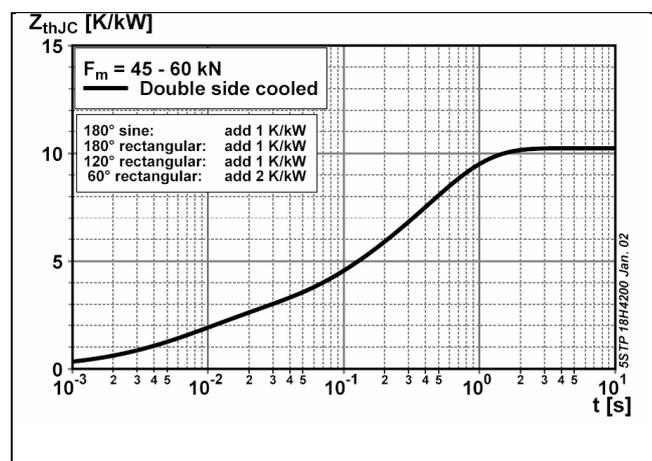
Fig. 1 Transient thermal impedance (junction-to-case) vs. time

---

ABB Switzerland Ltd, Semiconductors reserves the right to change specifications without notice.
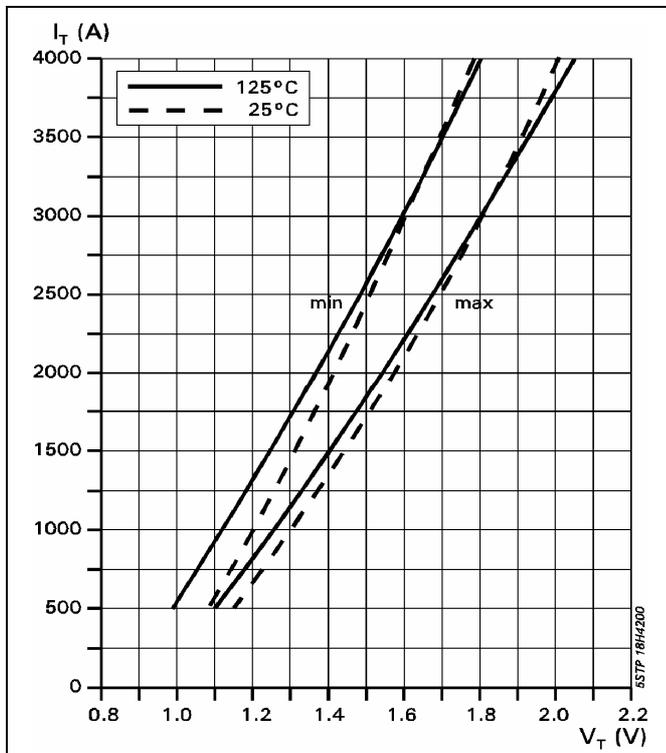
Fig. 2 On-state voltage characteristics
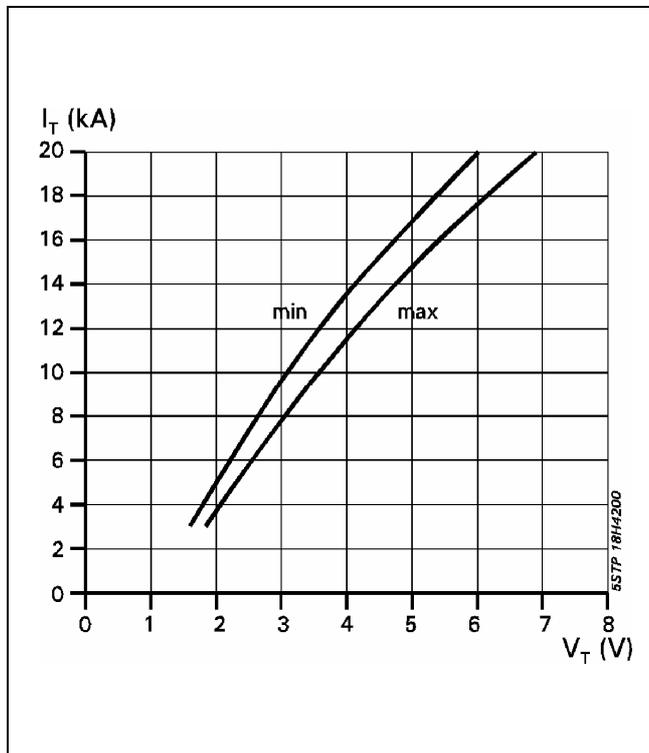
**Fig. 3** On-state characteristics,
$T_j = 125°C$, 10ms half sine
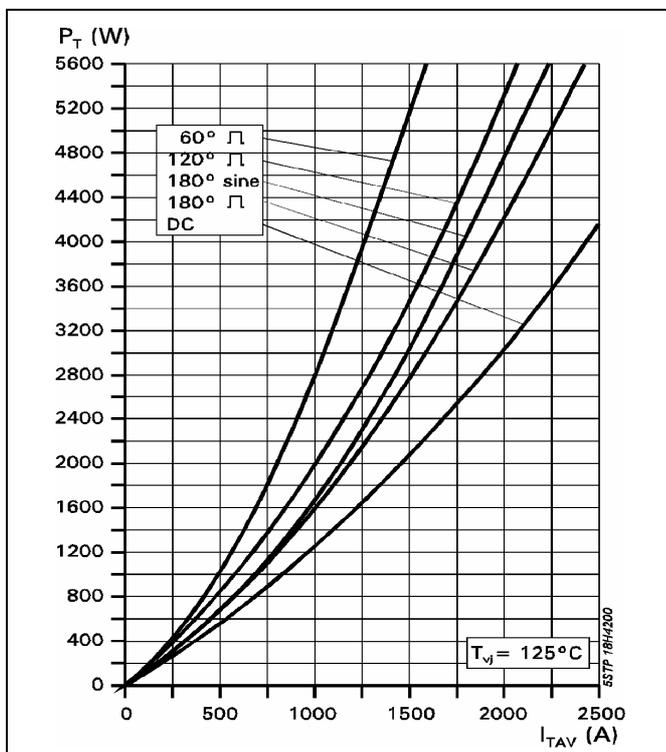
Fig. 4 On-state power dissipation vs. mean on-state
current, turn-on losses excluded
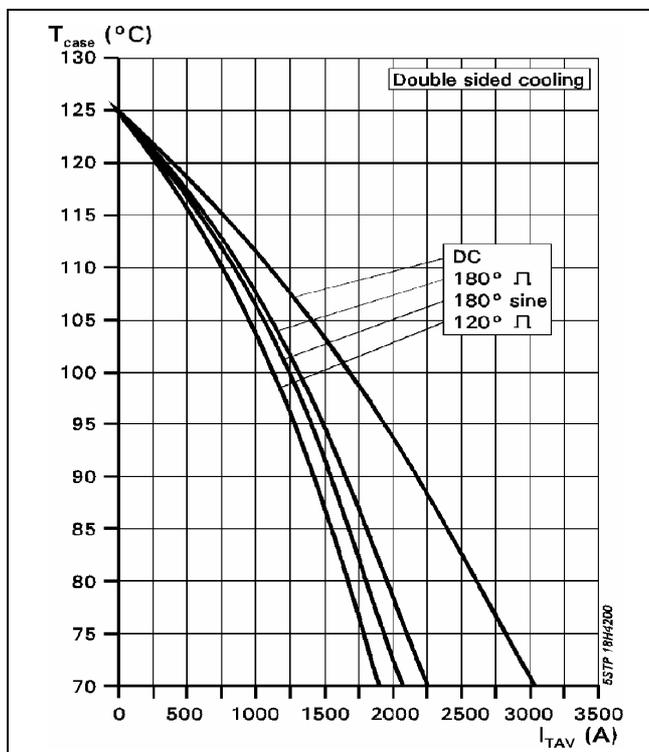
Fig. 5 Max. permissible case temperature vs. mean
on-state current, switching losses ignored

ABB Switzerland Ltd, Semiconductors reserves the right to change specifications without notice.
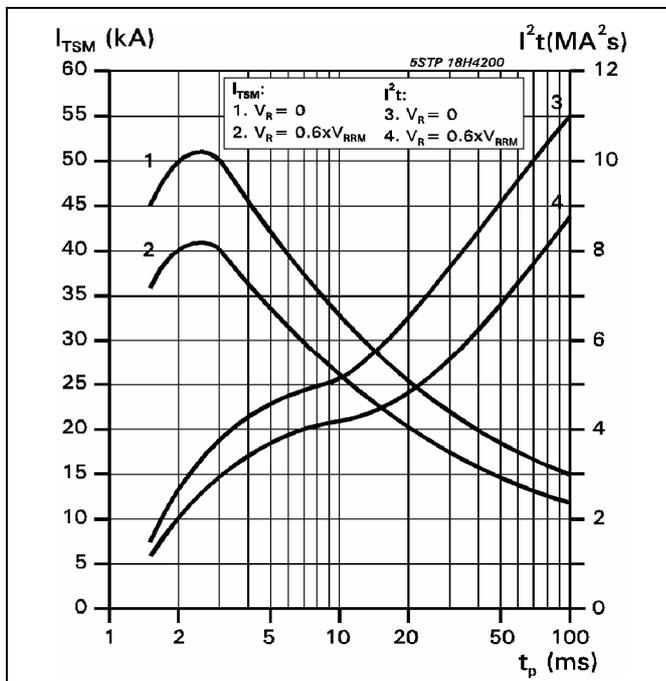
Fig. 6 Surge on-state current vs. pulse length, half-sine wave
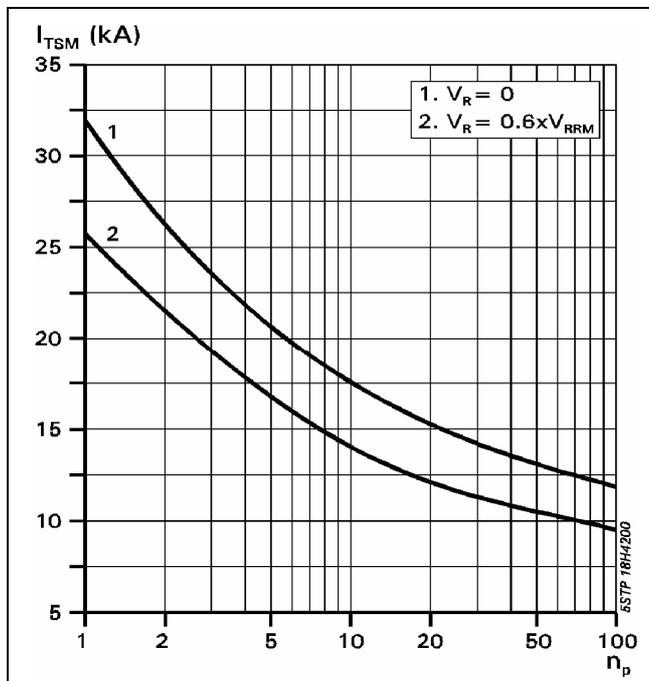
Fig. 7 Surge on-state current vs. number of pulses, half-sine wave, 10 ms, 50Hz
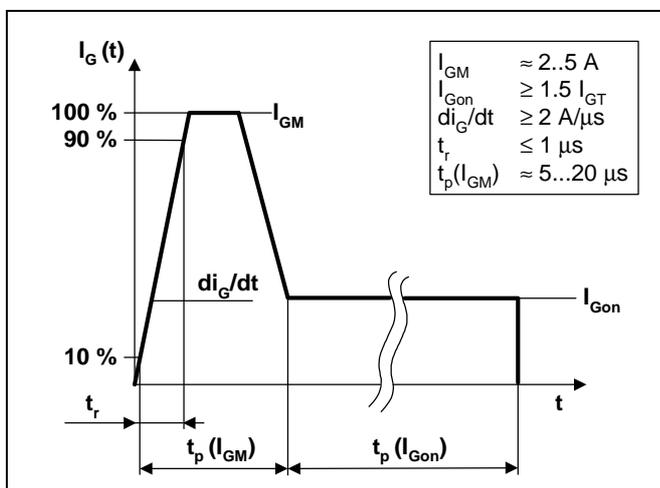
Fig. 8 Recommended gate current waveform
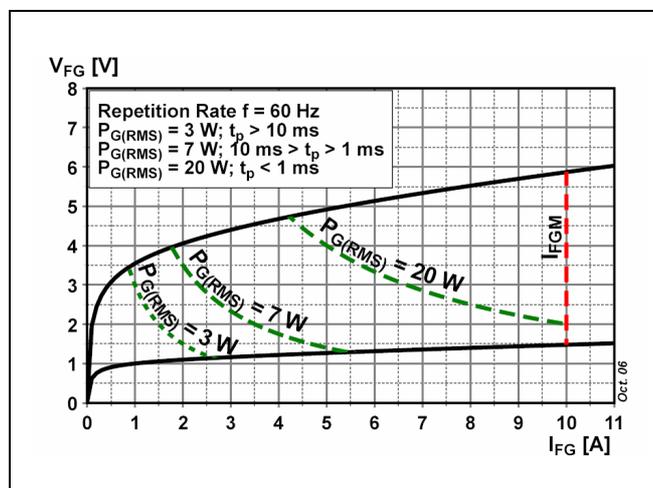
Fig. 9 Max. peak gate power loss
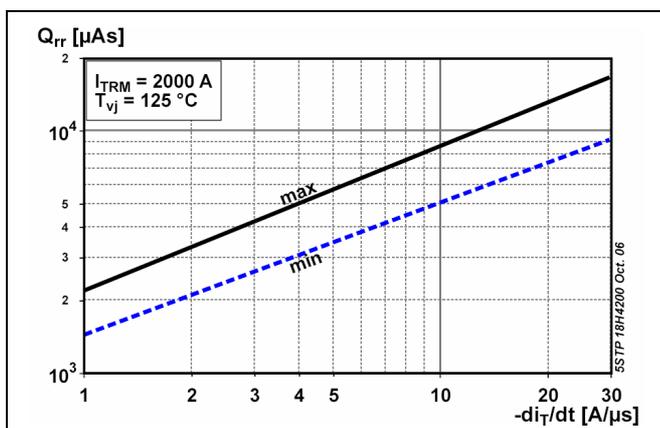
Fig. 10 Reverse recovery charge vs. decay rate of on-state current
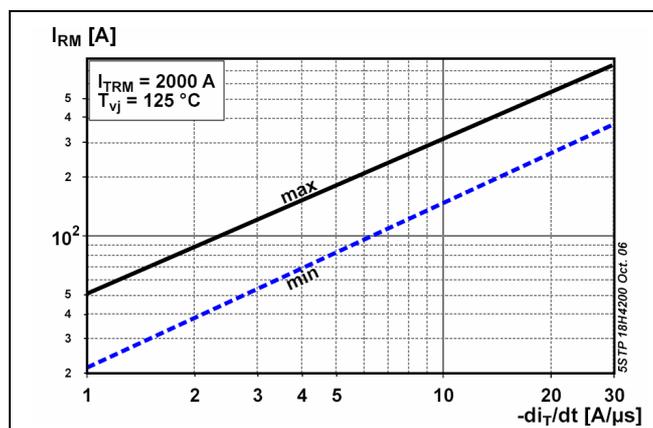
**Fig. 11** Peak reverse recovery current vs. decay rate of on-state current

ABB Switzerland Ltd, Semiconductors reserves the right to change specifications without notice.

Turn-on and Turn-off losses
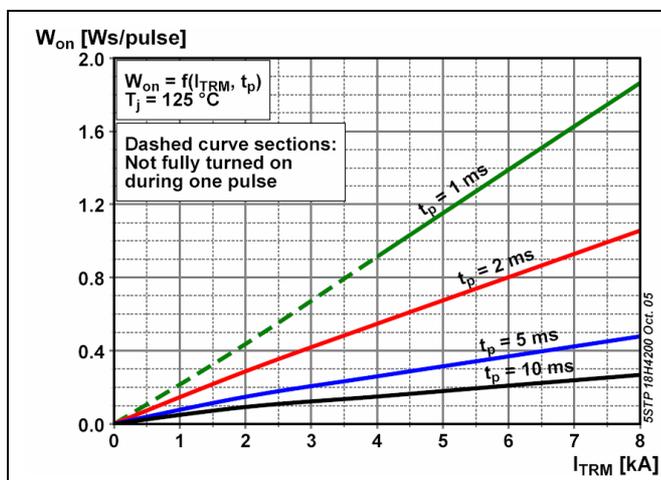
Fig. 12 Turn-on energy, half sinusoidal waves
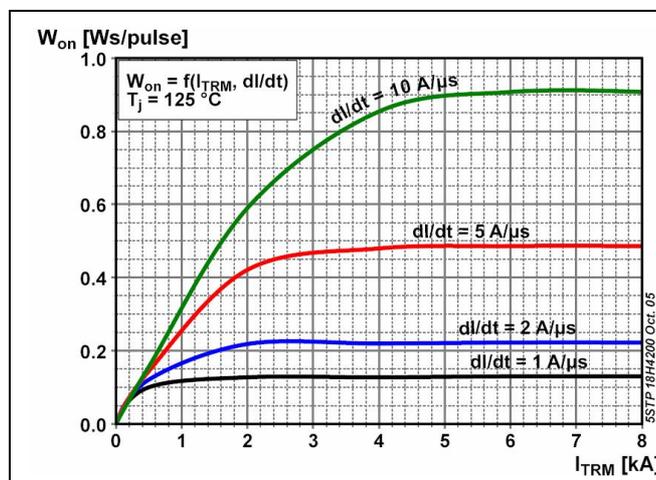
**Fig. 13** Turn-on energy, rectangular waves
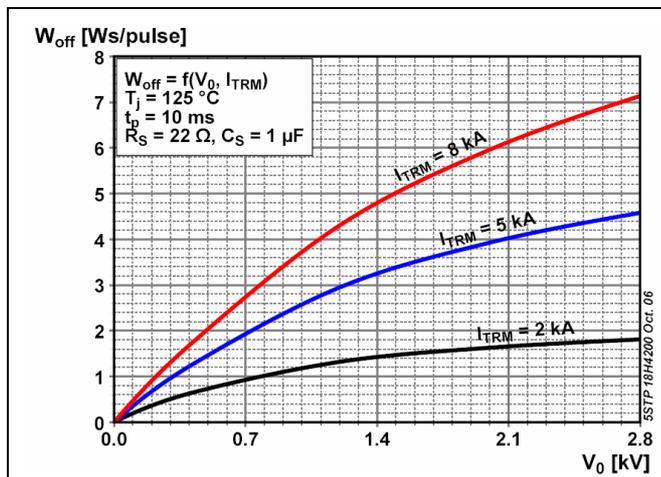
**Fig. 14** Turn-off energy, half sinusoidal waves
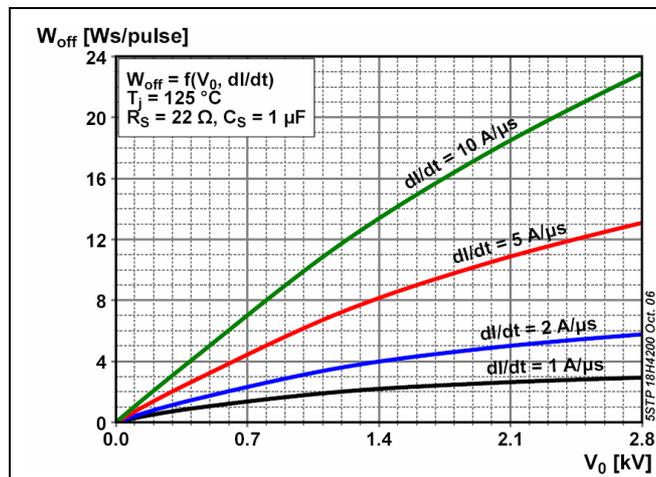
**Fig. 15** Turn-off energy, rectangular waves
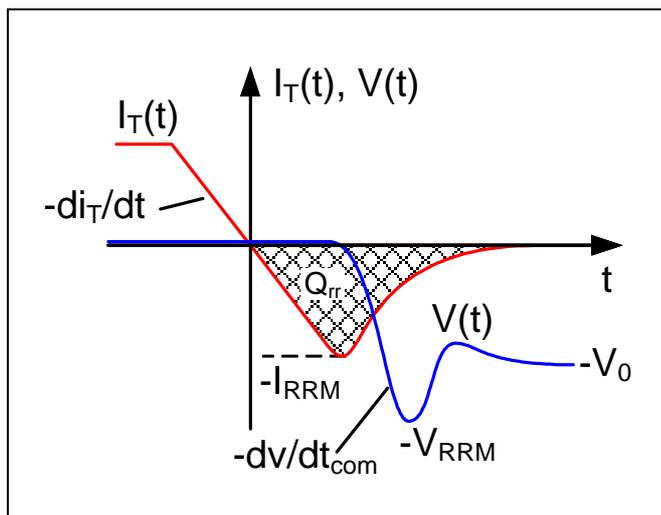
Fig. 16 Current and voltage waveforms at turn-off

Total power loss for repetitive waveforms:

$$P_{TOT} = P_T + W_{on} \cdot f + W_{off} \cdot f$$

where

$$P_T = \frac{1}{T} \int_0^T I_T \cdot V_T(I_T) \; dt$$

Fig. 17 Relationships for power loss
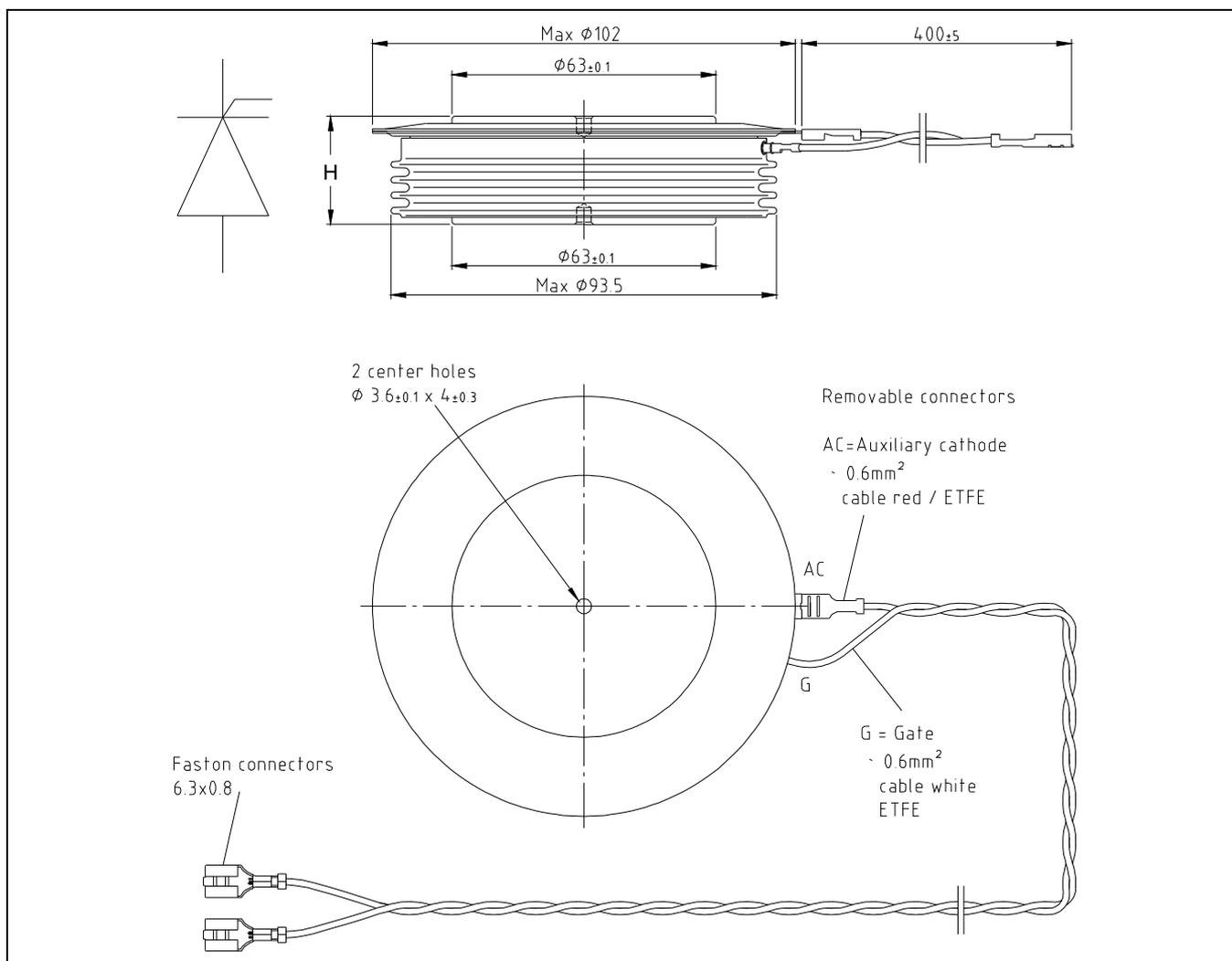
Fig. 18 Device Outline Drawing

Related documents:

5SYA 2020	Design of RC-Snubber for Phase Control Applications
5SYA 2049	Voltage definitions for phase control thyristors and diodes
5SYA 2051	Voltage ratings of high power semiconductors
5SYA 2034	Gate-Drive Recommendations for PCT's
5SYA 2036	Recommendations regarding mechanical clamping of Press Pack High Power Semiconductors
5SZK 9104	Specification of environmental class for pressure contact diodes, PCTs and GTO, STORAGE available on request, please contact factory
5SZK 9105	Specification of environmental class for pressure contact diodes, PCTs and GTO, TRANSPORTATION available on request, please contact factory

Please refer to http://www.abb.com/semiconductors for current version of documents.