

DATA SHEET

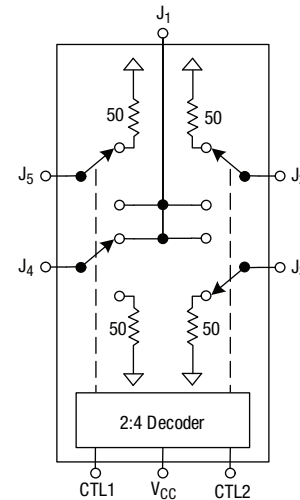
AS204-80, AS204-80LF: GaAs IC SP4T Nonreflective Switch With Driver 300 kHz–3.5 GHz

Features

- Integrated driver 5 V supply voltage
- High isolation (45 dB @ 0.9 GHz)
- Low insertion loss (0.5 dB @ 0.9 GHz)
- SSOP-16 plastic package
- Nonreflective all ports
- ESD rated at class 1A HBM
- Available lead (Pb)-free and RoHS-compliant MSL-1 @ 260 °C per JEDEC J-STD-020

Description

The AS204-80 is a high-isolation SP4T FET IC nonreflective switch with driver. The insertion loss is 0.5 dB and isolation is 45 dB at 0.9 GHz. The switch is ideal for cellular base station switch matrices.

Simplified Block Diagram

NEW

Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.


Electrical Specifications at 25 °C

$V_{CC} = 5\text{ V}$, $Z_0 = 50\ \Omega$, unless otherwise noted

Parameter ⁽¹⁾	Frequency	Min.	Typ.	Max.	Unit
Insertion loss ⁽²⁾	300 kHz–1.0 GHz		0.4	0.6	dB
	300 kHz–2.0 GHz		0.6	0.8	dB
	300 kHz–2.5 GHz		0.7	0.9	dB
	300 kHz–3.5 GHz		0.9	1.2	dB
Isolation	300 kHz–1.0 GHz	40	45		dB
	300 kHz–2.0 GHz	30	38		dB
	300 kHz–2.5 GHz	28	32		dB
	300 kHz–3.5 GHz	22	25		dB
VSWR ⁽³⁾ on state	300 kHz–3.5 GHz		1.3:1		
VSWR ⁽³⁾ off state	0.5 GHz–3.5 GHz		1.5:1		

1. All measurements made in a 50 Ω system, unless otherwise specified.

2. Insertion loss changes by 0.003 dB/°C.

3. Input/Output.

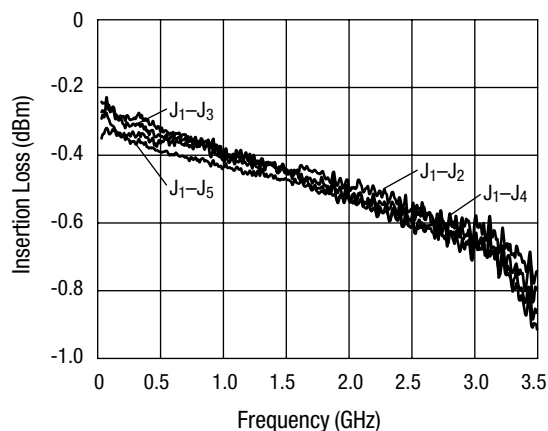
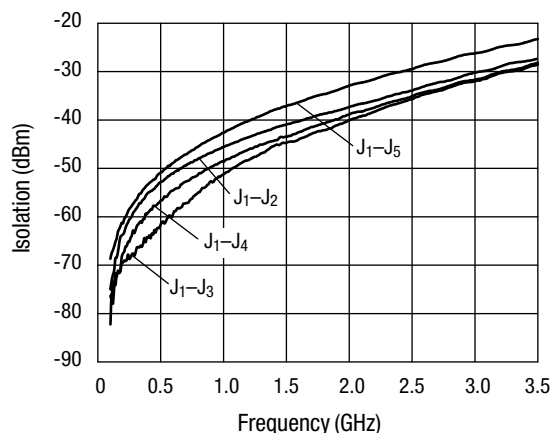
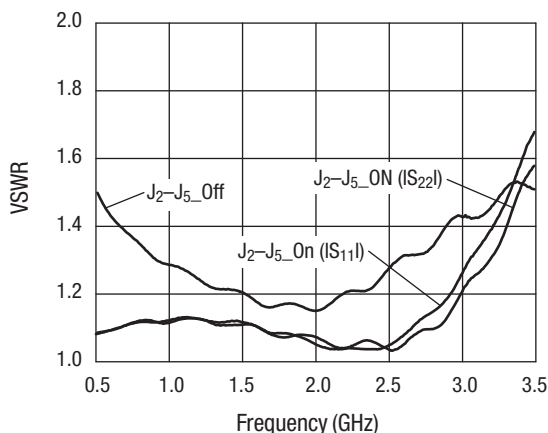
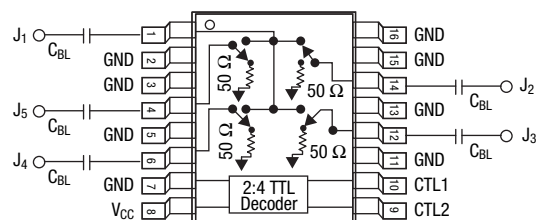


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Operating Characteristics at 25 °C **$V_{CC} = 5\text{ V}$, $Z_0 = 50\ \Omega$, unless otherwise noted**

Parameter	Condition	Frequency	Min.	Typ.	Max.	Unit
Switching characteristics						
Rise, fall	10/90% or 90/10% RF			75		ns
On, off	50% CTL to 90/10% RF			125		ns
Video feedthru	$T_{RISE} = 1\text{ ns}$, BW = 500 MHz			50		mV
Input power for 1 dB compression		0.5–2 GHz		26		dBm
Intermodulation intercept point (IP3)	For two-tone input power 13 dBm	0.5–2 GHz		40		dBm
		0.05 GHz		29		dBm
Thermal resistance				30		°C/W
Control voltages ⁽¹⁾	CTL1, 2 low		0		0.5	V
	CTL1, 2 high		2.4		5.0	V
Supply voltage, V_{CC} ⁽¹⁾			4.8		5.2	
Supply current	$V_{CC} = 5\text{ V}$			500		μA

1. V_{CC} must be powered on by a minimum of 10 ns prior to V_{CTL} .**Typical Performance Data** **$V_{CC} = 5\text{ V}$, $Z_0 = 50\ \Omega$, unless otherwise noted****Insertion Loss vs. Frequency****Isolation vs. Frequency****VSWR vs. Frequency****Pin Out**

DC blocking capacitors (C_{BL}) required for positive voltage operation.
 $C_{BL} = 47\text{ pF}$ for operation frequency >500 MHz.

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