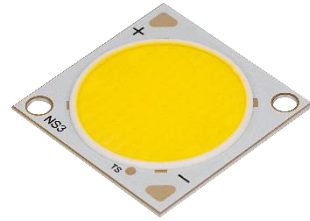


Enable High Flux and Cost Efficient System

Acrich Chip on Board – MJT COB series

SAW02298A



Product Brief

Description

- The MJT series are LED arrays which provide High Flux and High Efficacy.
- It is especially designed for easy assembly of Lighting fixtures by eliminating reflow soldering process.
- It's thermal management is excellent than other power LED solutions with wider Metal area.
- The MJT series are ideal light sources for General Lighting applications including Replacement Lamps, Industrial & Commercial Lightings and other high Lumen required applications.

Features and Benefits

- Efficacy up to 169lm/W @5000K
- Size 28mm * 28mm
- MacAdam 3-step binning
- Uniformed Shadow
- Excellent Thermal management
- RoHS compliant

Key Applications

- Out door area – Bay lighting, Street lighting, Tunnel lighting
- Architectural – Spot lighting
- Industrial

Table 1. Product Selection Table

Part Number	CCT			CRI
	Color	Min.	Max.	Min
SAW02298A	Cool White	4700K	7000K	70

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Performance Characteristics

Table.2 Electro Optical Characteristics, $T_j=85^{\circ}\text{C}$

Part Number	CCT (K) ^[1]	Typical Luminous Flux ^[2] Φ_v ^[3] (lm)	Typical Forward Voltage (V_F) ^[4]	CRI ^[5] , R_a	Viewing Angle (degrees) $2\theta_{1/2}$
	Typ.	1.1A	1.1A	Min.	Typ.
SAW02298A	6500	9,311	51.2	70	118
	5700	9,402	51.2	70	118
	5000	9,512	51.2	70	118

Notes :

- (1) Correlated Color Temperature is derived from the CIE 1931 Chromaticity diagram. Color coordinate : ± 0.005 , CCT $\pm 5\%$ tolerance.
- (2) Seoul Semiconductor maintains a tolerance of $\pm 7\%$ on flux and power measurements.
- (3) Φ_v is the total luminous flux output as measured with an integrating sphere.
- (4) Tolerance is $\pm 3\%$ on forward voltage measurements.
- (5) Tolerance is ± 2 on CRI measurements.

* For reference only.

Performance Characteristics

Table.3 Absolute Maximum Ratings

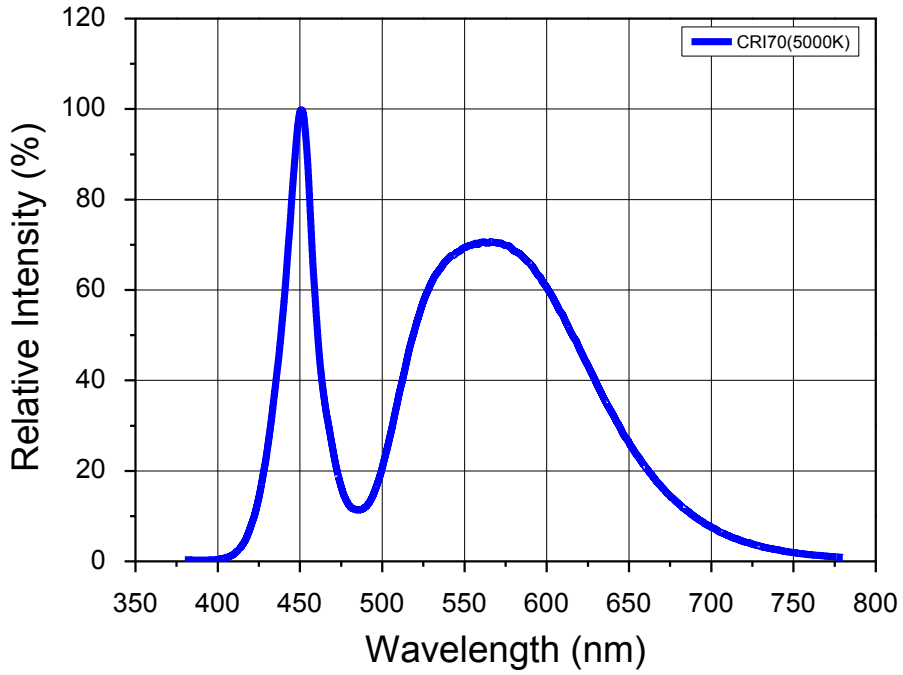
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Forward Current	I_F	-	1.1	2.75	A
Power Dissipation	P_d	-	56.3	153.8	W
Junction Temperature	T_j	-	-	150	°C
Operating Temperature	T_{opr}	- 40	-	100	°C
Surface Temperature	T_S	- 40	-	120	°C
Storage Temperature	T_{stg}	- 40	-	105	°C
Thermal resistance (J to S) ^[1]	$R\theta_{J-S}$	-	0.23	-	K/W
ESD Sensitivity(HBM)		Class 3A JESD22-A114-E			

Notes :

- (1) Thermal resistance : $R\theta_{J-S}$ At thermal resistance, J to S means junction to COB's substrate bottom.
- (2) LED's properties might be different from suggested values like above and below tables if operation condition will be exceeded our parameter range. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
- (3) Thermal resistance can be increased substantially depending on the heat sink design/operating condition, and the maximum possible driving current will decrease accordingly.
- (4) All measurements were made under the standardized environment of Seoul Semiconductor.

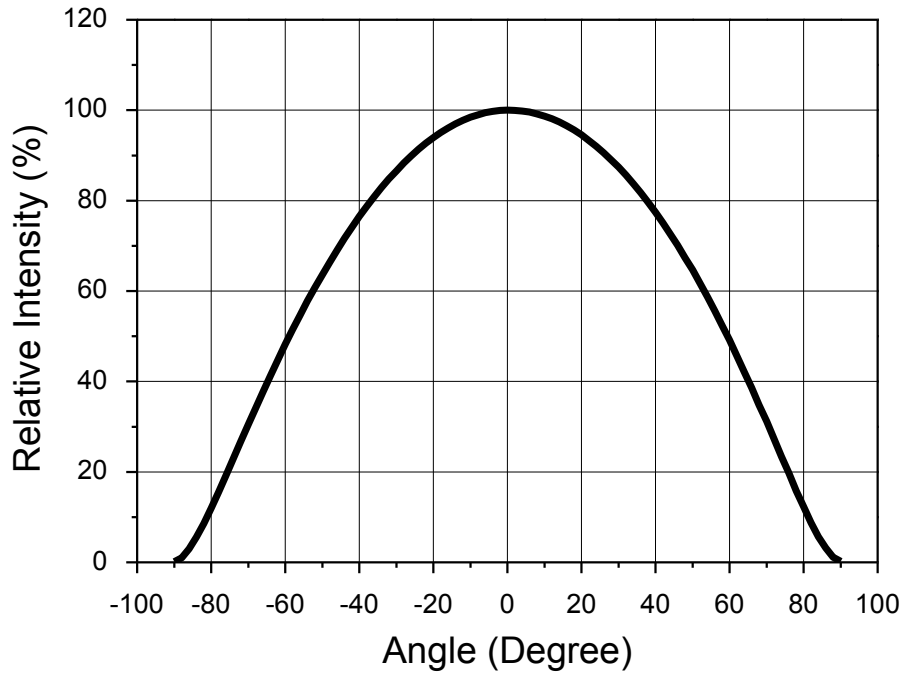
Performance Characteristics

Fig 1. Color Spectrum, CRI70



Performance Characteristics

Fig 2. Radiant Pattern



Performance Characteristics

Fig 3. Forward Voltage vs. Forward Current, $T_j=85^{\circ}\text{C}$

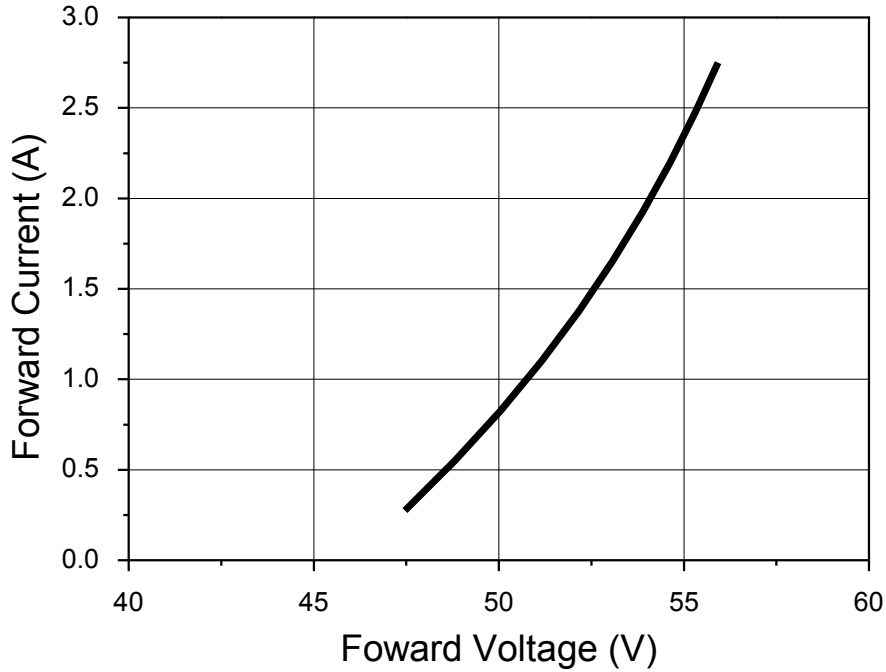
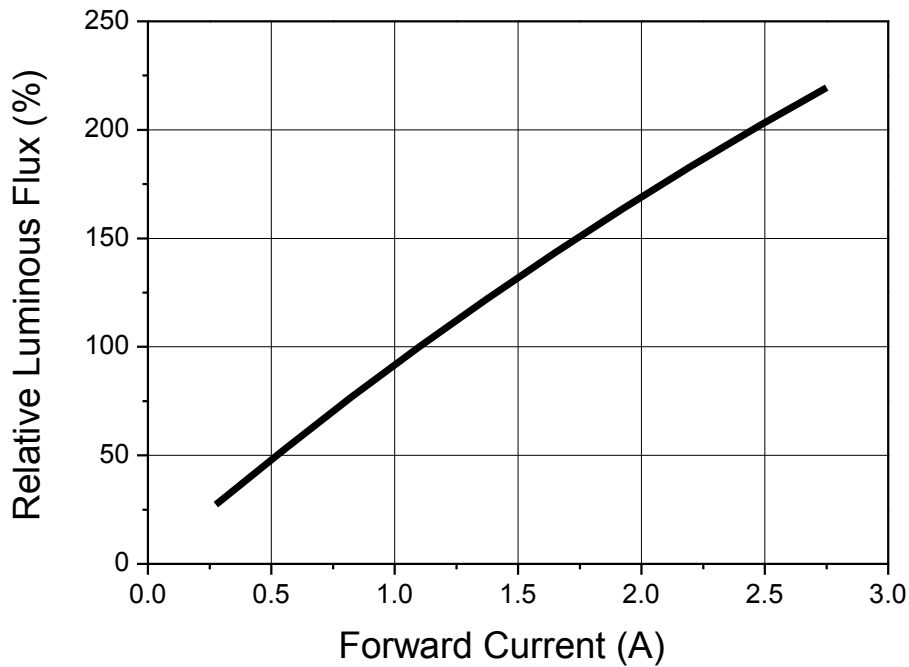


Fig 4. Forward Current vs. Relative Luminous Flux, $T_j=85^{\circ}\text{C}$



Performance Characteristics

Fig 5. Junction Temperature vs. Relative Luminous Flux, $I_F=1.1A$

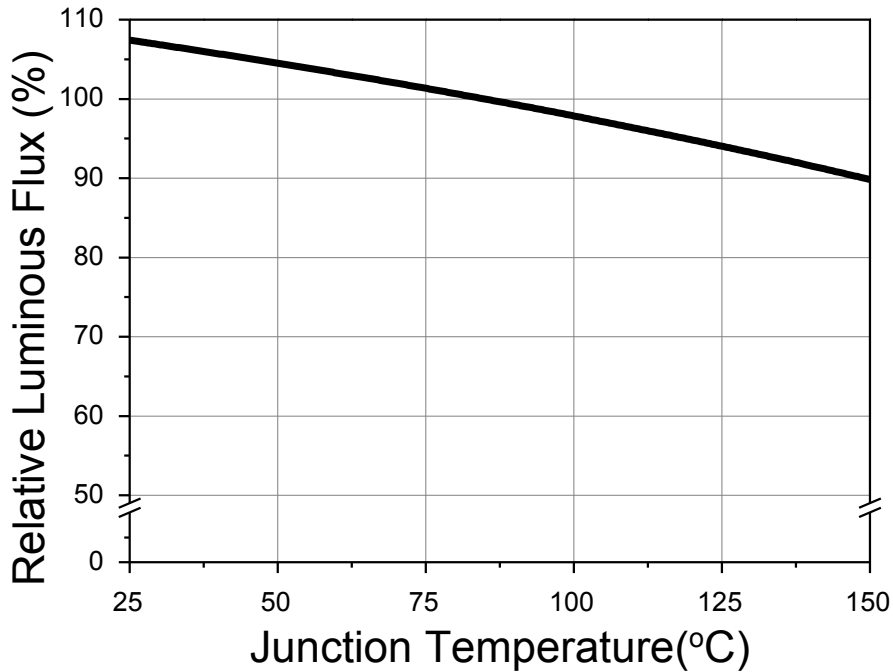
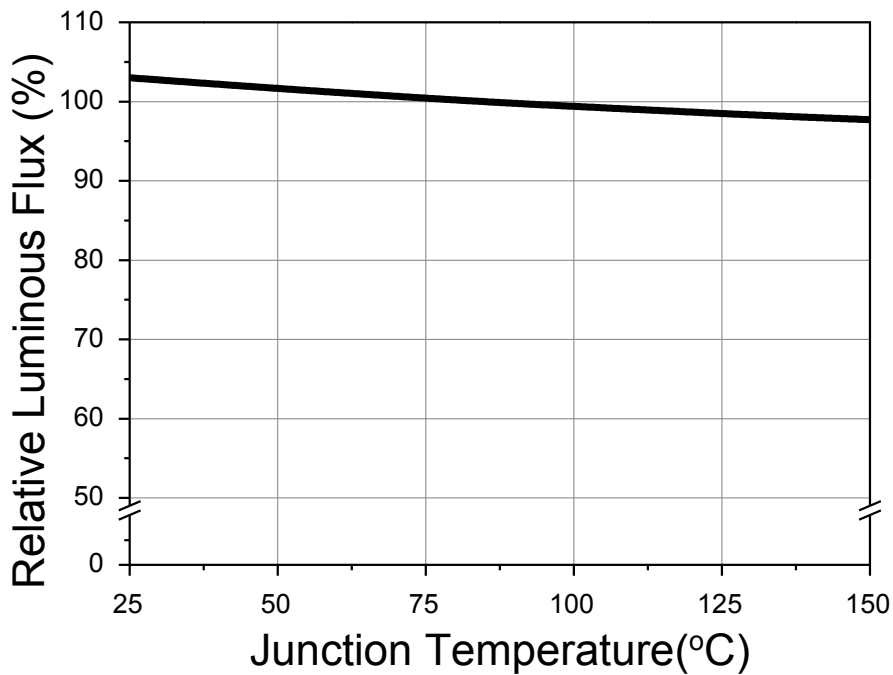
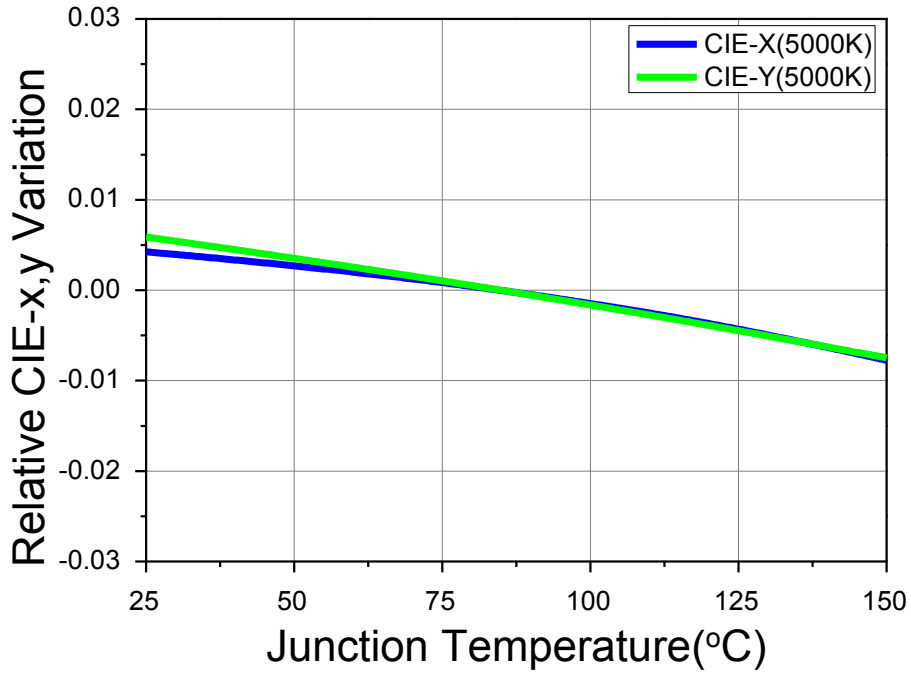


Fig 6. Junction Temperature vs. Forward Voltage, $I_F=1.1A$



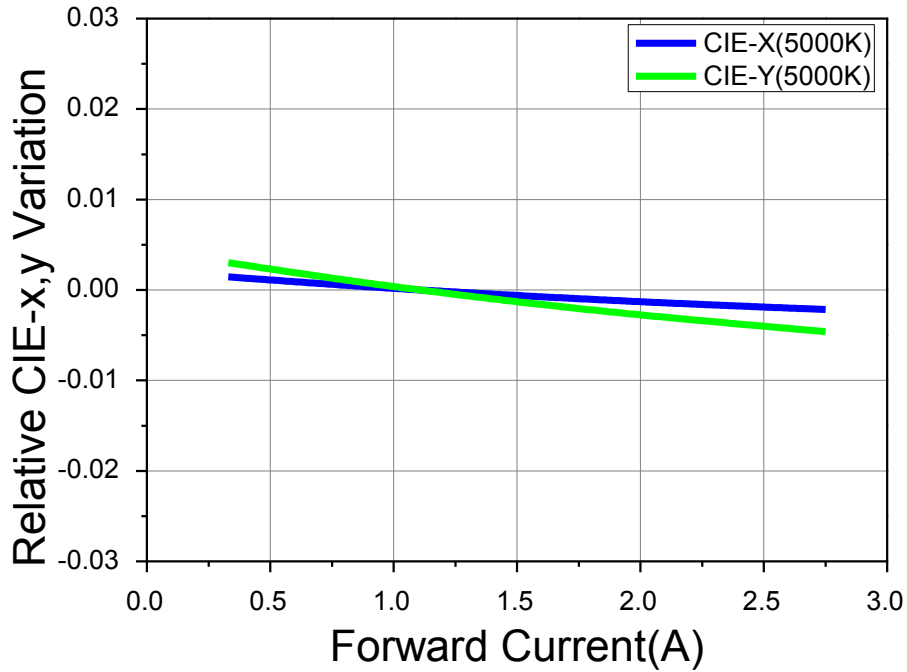
Performance Characteristics

Fig 7. Junction Temperature vs. CIE x,y Shift, $I_f=1.1A$ (CRI70, 5000K)



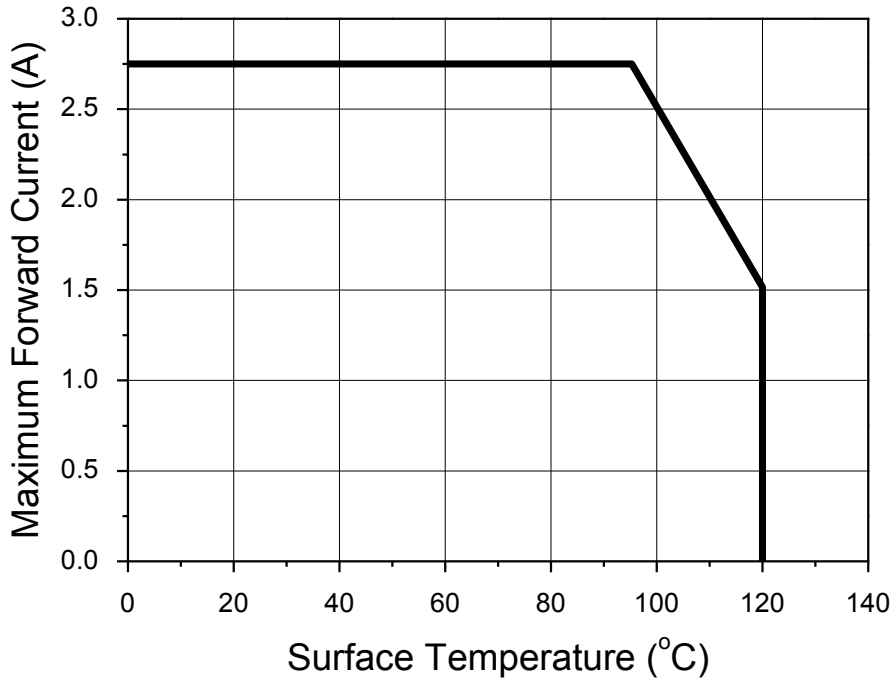
Performance Characteristics

Fig 8. Forward current vs. CIE x,y Shift, $T_j=85^\circ\text{C}$ (CRI70, 5000K)



Performance Characteristics

Fig 9. Surface Temperature vs. Maximum Forward Current, $T_j(\text{max.})=150^\circ\text{C}$

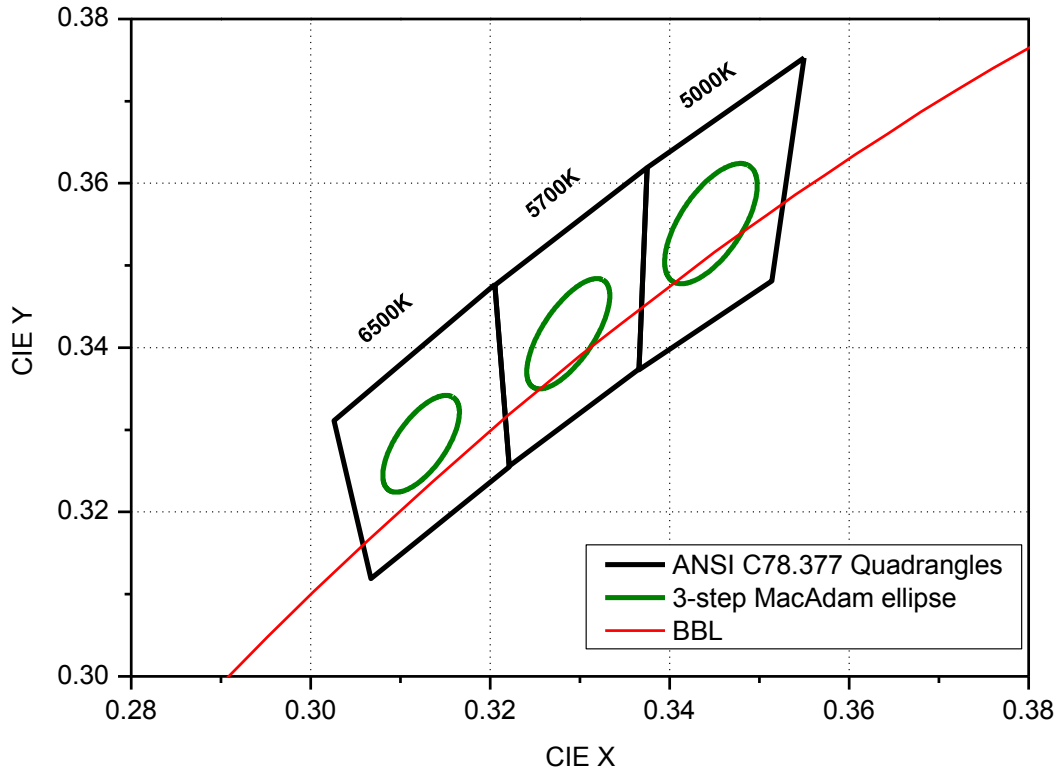


Color Bin Structure

Table 4. Bin Code description, $T_j=85^{\circ}\text{C}$, $I_F=1.1\text{A}$

Part Number	Luminous Flux (lm)			Color Chromaticity		Typical Forward Voltage (V)			CRI	
	Bin Code	Min.	Typ.	Bin Code	Typ. CCT	Bin Code	Min.	Max.	Bin Code	Min
SAW02298A	G0	8,566	9,311	AE3	6500K	K	48.6	53.7	0	70
	G0	8,650	9,402	BE3	5700K	K	48.6	53.7	0	70
	G0	8,751	9,512	CE3	5000K	K	48.6	53.7	0	70

Color Bin Structure

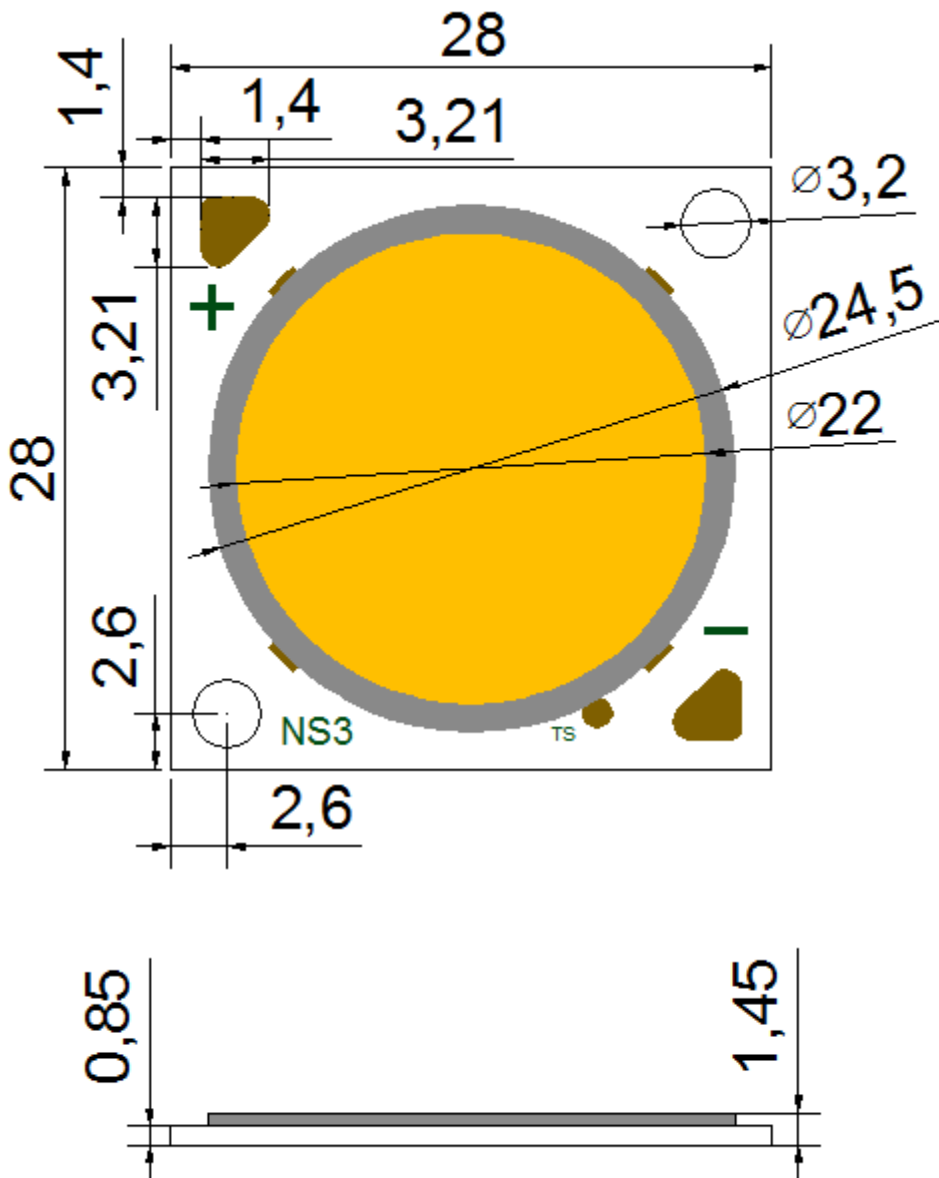
CIE Chromaticity Diagram, $T_j=85^\circ\text{C}$

Table 5. 3-step MacAdam ellipse color bin definitions

Color Region	CCT (K)	Center Point		Major Axis (a)	Minor Axis (b)	Rotation Angle (θ)
		CIE x	CIE y			
3-step MacAdam ellipse	6500	0.3123	0.3283	0.00669	0.00285	58.38
	5700	0.3287	0.3425	0.00760	0.00296	59.46
	5000	0.3446	0.3551	0.00822	0.00354	59.62

Notes :

- (1) The chromaticity center refers to ANSI C78.377:2015.
- (2) (a), (b), and (θ) indicate the major axis length, the minor axis length, and the rotation angle from the X axis of the ellipse bin, respectively.

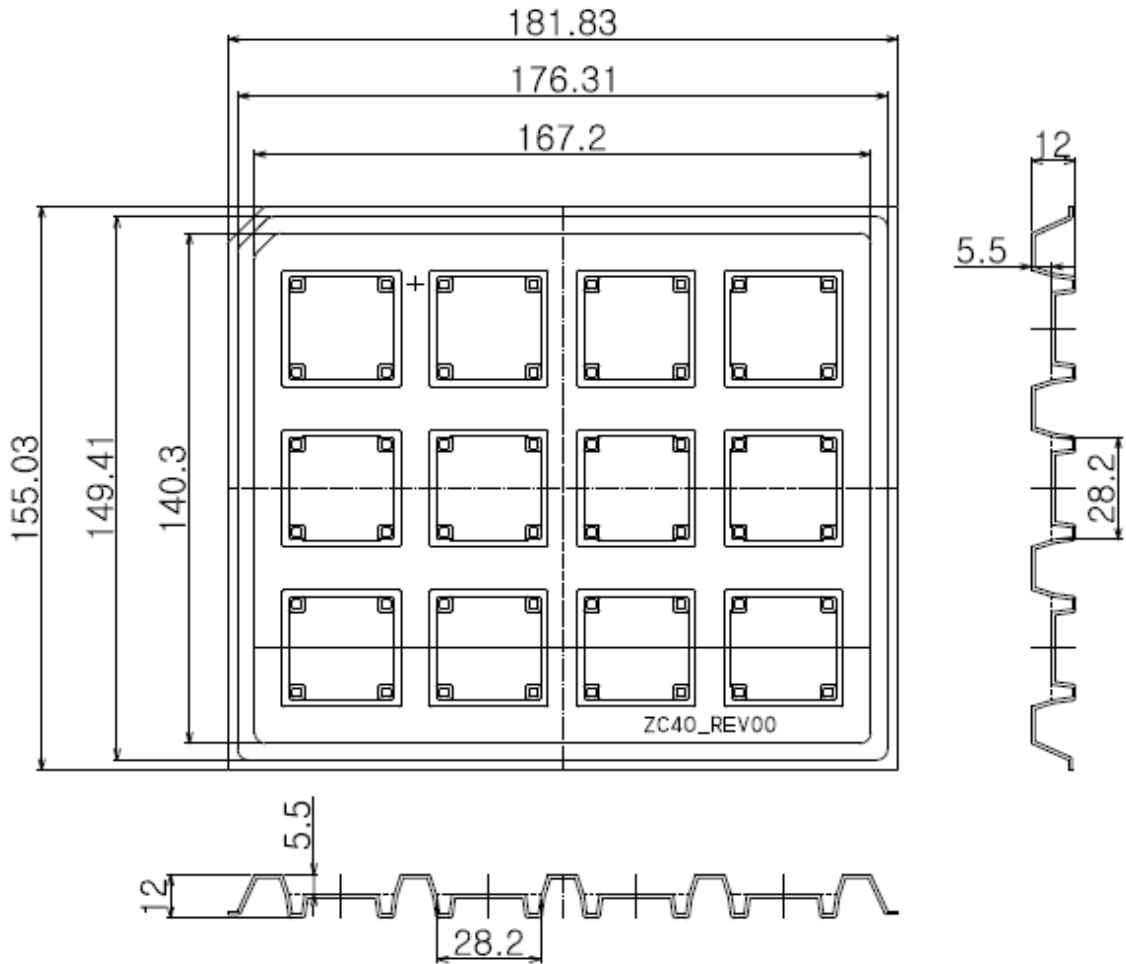
Mechanical Dimensions



Notes :

1. All dimensions are in millimeters.
2. Scale : none
3. Undefined tolerance is ± 0.2 mm

Packaging Specification

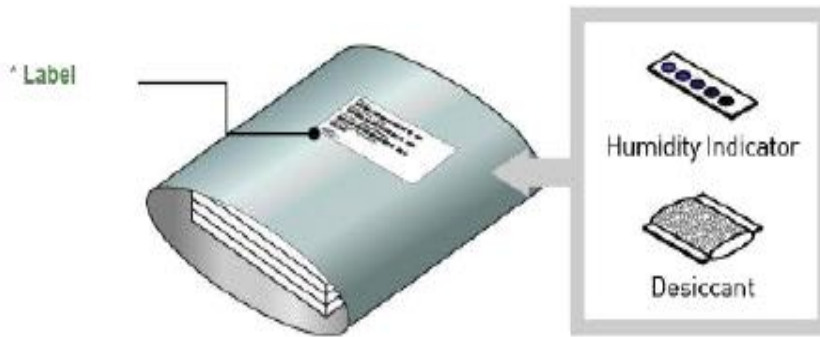


Notes :

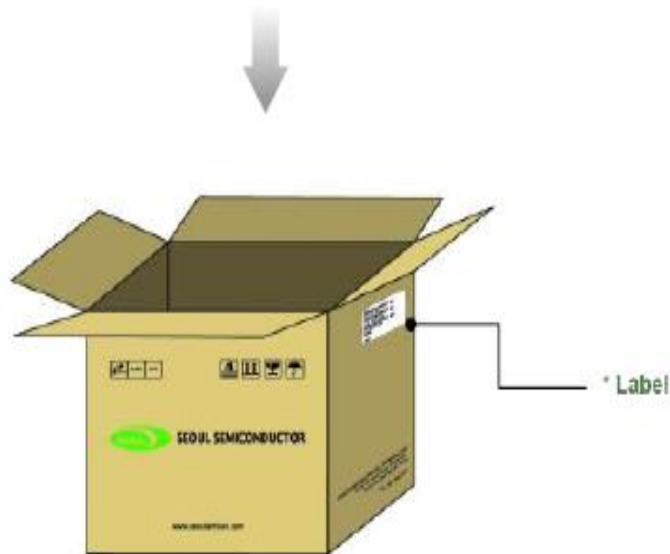
- (1) Quantity : 12pcs/Tray
- (2) All dimensions are in millimeters (tolerance : ± 0.3)
- (3) Scale none

Packaging Specification

Aluminum Bag




Outer Box





Notes :


- (1) Heat Sealed after packing (Use Zipper Bag)
- (2) Quantity : 1Tray(12pcs) /Bag
- (3) Smallest packing quantity : 3Bags(36pcs) / small box

Product Nomenclature

RANK : Z₁Z₁Z₂Z₂Z₃Z₄


QUANTITY : 12


LOT NUMBER : Y₁Y₂Y₃Y₄Y₅Y₆Y₇Y₈Y₉Y₁₀ – Y₁₁Y₁₂Y₁₃Y₁₄Y₁₅Y₁₆Y₁₇


SSC PART NUMBER : X₁X₂X₃X₄X₅X₆X₇X₈X₉





Table 6. Part Numbering System : X₁X₂X₃X₄X₅X₆X₇X₈X₉

Part Number Code	Description	Part Number	Value
X ₁	Company	S	
X ₂	Package series	A	
X ₃ X ₄	Color Specification	W0	CRI 70
X ₅ X ₆	LES size	22	
X ₇	Chip Array	9	Series
X ₈	Chip Array	8	Parallel
X ₉	Revision number	A	

Table 7. Lot Numbering System : Y₁Y₂Y₃Y₄Y₅Y₆Y₇Y₈Y₉Y₁₀ – Y₁₁Y₁₂Y₁₃Y₁₄Y₁₅Y₁₆Y₁₇

Lot Number Code	Description
Y ₁ Y ₂ Y ₃ Y ₄ Y ₅	Date of box packing
Y ₆ Y ₇ Y ₈ Y ₉ Y ₁₀	Date of label order
Y ₁₁ Y ₁₂ Y ₁₃ Y ₁₄ Y ₁₅ Y ₁₆ Y ₁₇	Item code

Handling of Silicone Resin for LEDs

- (1) During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.



- (2) In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.



- (3) Silicone differs from materials conventionally used for the manufacturing of LEDs. These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust. As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of wire.
- (4) Seoul Semiconductor suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.
- (5) Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this product with acid or sulfur material in sealed space.
- (6) Avoid leaving fingerprints on silicone resin parts.

Precaution for Use

(1) Storage

To avoid the moisture penetration, we recommend storing LEDs in a dry box with a desiccant . The recommended storage temperature range is 5°C to 30°C and a maximum humidity of RH50%.

(2) Use Precaution after Opening the Packaging

Use SMT techniques properly when you solder the LED as separation of the lens may affect the light output efficiency.

Pay attention to the following:

a. Recommend conditions after opening the package

- Sealing / Temperature : 5 ~ 40°C Humidity : less than RH30%

b. If the package has been opened more than 4 week(MSL_2a) or the color of the desiccant changes, components should be dried for 10-12hr at 60±5°C

(3) Radioactive exposure is not considered for the products listed here in.

(4) Gallium arsenide is used in some of the products listed in this publication. These products are dangerous if they are burned or shredded in the process of disposal. It is also dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.

(5) This device should not be used in any type of fluid such as water, oil, organic solvent and etc. When washing is required, IPA (Isopropyl Alcohol) should be used.

(6) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.

(7) LEDs must be stored in a clean environment. We recommend LEDs store in nitrogen-filled container.

(8) The appearance and specifications of the product may be modified for improvement without notice.

(9) Long time exposure of sunlight or occasional UV exposure will cause lens discoloration.

(10) Attaching LEDs, do not use adhesive that outgas organic vapor.

(11) The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.

(12) Please do not touch any of the circuit board, components or terminals with bare hands or metal while circuit is electrically active.

Precaution for Use

(13) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.

(14) LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.

a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to LEDs may cause the product to demonstrate unusual characteristics such as:

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event.

One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

Environmental controls:

- Humidity control (ESD gets worse in a dry environment)

Precaution for Use

b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device.

The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package
(If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package
(shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires.
- This damage usually appears due to the thermal stress produced during the EOS event.

c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:

- A surge protection circuit
- An appropriately rated over voltage protection device
- A current limiting device