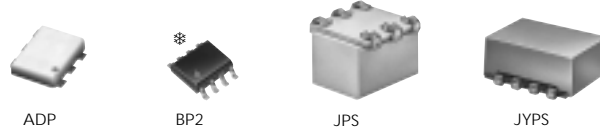


POWER SPLITTERS/COMBINERS

50 & 75Ω

2 WAY-0° 100 kHz to 3 GHz



MODEL NO.	FREQ. RANGE MHz f_L - f_U	ISOLATION dB			INSERTION LOSS, dB Above 3dB				PHASE UNBALANCE Degrees			AMPLITUDE UNBALANCE dB			CASE STYLE Note B	CONNECTION	PCB Lay-out PL- Qty. (10-49)	PRICE \$					
		L Typ. Min.	M ^o Typ. Min.	U Typ. Min.	L Typ. Max.	M ^o Typ. Max.	U Typ. Max.	L Max.	M ^o Max.	U Max.	L Max.	M ^o Max.	U Max.										
◆ ADP-2-1*	0.5-400	25	20	25	20	25	20	0.2	0.4	0.3	0.6	0.5	1.0	1.0	2.0	3.0	0.1	0.2	0.3	CD636	mp	116	7.95
◆ ADP-2-1W*	1-650	30	20	30	20	24	20	0.2	0.8	0.25	0.8	0.5	1.0	2.0	2.0	3.0	0.15	0.2	0.3	CD636	hv	035	6.95
◆ ADP-2-4*	10-1000	25	20	23	16	19	14	0.3	0.5	0.4	0.9	0.8	1.5	1.0	3.0	5.0	0.15	0.2	0.4	CD636	mp	116	11.95
◆ ADP-2-9*	200-900			27	20					0.4	0.8				2.0			0.3		CD636	mp	116	9.95
◆ ADP-2-10*	5-1000	25	15	23	15	20	15	0.3	0.9	0.4	0.9	0.6	1.2	2.0	2.0	3.0	0.2	0.2	0.3	CD636	hv	035	12.95
◆ ADP-2-10-75*	50-1000	26	20	—	—	22	18	0.6	1.0	—	—	0.8	1.4	2.0	—	3.0	0.15	—	0.3	CD542	mp	105	12.95
◆ ADP-2-10W-75*	5-1000	24	14	23	18	24	18	0.2	0.6	0.3	0.9	0.5	1.1	1.0	3.0	5.0	0.1	0.2	0.3	CD636	mp	105	12.95
◆ ADP-2-20*	20-2000	18	15	18	15	18	15	0.5	0.8	0.7	1.0	0.8	1.5	2.0	3.0	5.0	0.2	0.3	0.7	CD542	hv	035	16.95
◆ ADP-2-20-75*	5-2000	16	12	16	13	28	15	0.4	0.9	0.5	1.2	0.6	1.4	1.0	4.0	5.0	0.15	0.3	0.6	CD542	hv	141	17.95
◆ BP2C	810-960			25	18					0.6	0.9				3.0			0.2		XX211	jm	053	1.29
◆ BP2G	1420-1660			28	20 ^o					0.6	1.0				3.0			0.2		XX211	jm	053	0.99
◆ BP2P	1710-1990			30	18					0.7	1.0				3.0			0.2		XX211	jm	053	1.24
																							Qty. (1-9)
JPS-2-1	1-500	34	20	30	20	27	20	0.2	0.8	0.25	0.7	0.4	0.9	1.0	2.0	3.0	0.1	0.2	0.3	BH292	hv	035	9.95
JPS-2-1-75	5-500	25	18	35	20	20	18	0.15	0.5	0.15	0.7	0.25	0.7	1.0	2.0	3.0	0.1	0.2	0.4	BH292	me	054	9.95
JPS-2-1N	350-550			30	20					0.25	0.5				3.0			0.3		BH292	hv	035	8.95
JPS-2-1W	3-750	36	20	28	17	19	16	0.5	0.8	0.4	1.0	0.9	1.4	1.0	2.0	4.0	0.2	0.3	0.4	BH292	hv	035	8.95
JPS-2-4	100-1000			22	16					0.5	1.4				5.0			0.4		BH292	hv	035	9.95
■ JPS-2-4-75	20-1000	27	20	29	20	27	16	0.35	0.7	0.4	0.8	0.45	1.0	2.0	2.0	3.0	0.2	0.2	0.3	BH292	me	054	10.95
JPS-2-900	400-900			24	18					0.5	1.2				3.0			0.4		BH292	hv	035	9.95
■ JYPS-2-4-75	5-1000	24	17	25	20	30	18	0.4	0.8	0.4	1.0	0.8	1.5	3.0	4.0	5.0	0.2	0.3	0.4	BJ293	jf	069	16.95

L = low range [f_L to $10 f_L$]

M = mid range [$10 f_L$ to $f_U/2$]

U = upper range [$f_U/2$ to f_U]

NOTES:

- * Smaller size package available. See outline drawing CA531, TP model series.
- ◆ Aqueous washable. For non-aqueous requirements, LRPS units available in case style QQQ130.
- ⊕ When only specification for M range given, specification applies to entire frequency range.
- ◇ 18 dB min. at frequencies 1500 - 1660 MHz.
- Denotes 75 Ohm model
- * Protected under U.S. Patent 6133525
- A. Environmental specifications and re-flow soldering information available in General Information Section.
- B. Units are non-hermetic unless otherwise noted. For details on case dimensions & finishes see "Case Styles & Outline Drawings".
- C. Prices and specifications subject to change without notice.
- 1. Absolute maximum power, voltage and current ratings:
 - 1a. Matched power rating,
 - Models BP2C, BP2G, BP2P 1.5 Watts
 - Models JYPS-2-4-75, ADP-2-10, ADP-2-10-75, 0.5 Watt
 - ADP-2-20-75, ADP-2-1, ADP-2-9, TCP 0.5 Watt
 - ADP-2-1W 2 Watts
 - All other models 1 Watt
 - 1b. Internal load dissipation,
 - Models BP2C, BP2G, BP2P 0.375 Watt
 - Models ADP-2-10-75 0.250 Watt
 - All other models 0.125 Watt

Surface Mount □



LRPS-J



RPS



SCP



SYPS



TCP

MODEL NO.	FREQ. RANGE MHz f_L - f_U	ISOLATION dB			INSERTION LOSS, dB Above 3dB						PHASE UNBALANCE Degrees			AMPLITUDE UNBALANCE dB			CASE STYLE Note B	CONNECTION	PCB Lay-out PL-	PRICE \$ Qty. (10-49)			
		L Typ. Min.	M ^o Typ. Min.	U Typ. Min.	L Typ. Max.	M ^o Typ. Max.	U Typ. Max.	L Max.	M ^o Max.	U Max.	L Max.	M ^o Max.	U Max.	L Max.	M ^o Max.	U Max.							
◆ LRPS-2-1J	5-500	50	25	33	24	30	23	0.25	0.5	0.3	0.6	0.5	1.2	1.0	2.0	3.0	0.15	0.2	0.3	QQQ569	am	058	8.95
◆ LRPS-2-1-75J	2-500	35	18	35	25	27	20	0.30	0.8	0.35	0.6	0.5	1.0	1.0	2.0	3.0	0.15	0.2	0.3	QQQ569	am	109	8.95
◆ LRPS-2-1W-75J	10-650	28	22	29	24	30	20	0.5	1.0	0.6	0.75	0.6	1.2	1.0	2.0	3.0	0.15	0.2	0.3	QQQ569	am	109	9.95
◆ LRPS-2-4J	10-1000	25	20	23	16	19	14	0.3	0.5	0.4	0.9	0.8	1.5	1.0	3.0	5.0	0.15	0.2	0.4	QQQ569	am	058	19.95
◆ LRPS-2-11J	20-2000	19	15	21	15	30	15	0.6	0.8	0.7	1.0	0.8	1.5	2.0	3.0	5.0	0.2	0.3	0.7	QQQ569	gn	057	24.95
◆ LRPS-2-25J	1700-2500			20	16					0.8	1.3				10.0				QQQ569	gn	057	21.95	
◆ LRPS-2-980J	800-980			30	18					0.5	1.0				3.0				QQQ569	am	058	8.95	
NEW RPS-2-30	10-3000	19	12	22	15	15	9	0.6	1.0	0.9	1.5	1.2	2.5	2.0	4.0	8.0	0.3	0.6	1.2	TT240	gn	110	24.95
SCP-2-1	0.1-400	25	15	30	20	25	20	0.3	1.2	0.2	0.6	0.4	1.1	2.0	2.0	3.0	0.15	0.2	0.3	YY101	aq	060	10.45
SCP-2-1A	1-550	25	20	25	20	25	20	0.3	0.6	0.3	0.6	0.7	1.3	2.0	2.0	3.0	0.15	0.2	0.4	YY101	aq	060	10.45
SYPS-2-1	2-500	40	20	32	20	30	20	0.2	0.6	0.3	0.75	0.6	1.0	2.0	3.0	4.0	0.2	0.3	0.5	TTT167	hk	079	12.95 Qty. (25)
◆ TCP-2-10	5-1000	25	17	25	16	21	16	0.3	0.9	0.5	0.9	0.5	1.4	4.0	4.0	6.0	0.6	0.6	0.3	DB714	mt	001	1.49
◆ TCP-2-10-75	5-1000	24	14	29	19	30	16	0.3	1.4	0.3	0.9	0.6	1.3	6.0	4.0	3.0	1.2	0.6	0.5	DB714	mt	002	1.99
◆ TCP-2-25	200-2500			18	10														0.8	DB714	nb	008	1.99

L = low range [f_L to $10 f_L$]

M = mid range [$10 f_L$ to $f_U/2$]

U = upper range [$f_U/2$ to f_U]

pin connections see case style outline drawings for pin locations

PORT	am	aq	gn	hk	hv	jf	jm	me	mp ⁽¹⁾	mt ⁽²⁾	nb ⁽³⁾
SUM PORT	6	1	6	3	1	1	2	1	1	6	6,5,2
PORT 1	4	5	4	1	3	3	8	3	3	3	3
PORT 2	3	6	3	2	4	6	5	4	4	4	4
GND EXT.	1	2,3,4,7,8	1,2,5	4,5,6	6	7,8	1,3,4,6,7	2,5,6	6	1	1
CASE GND	—	—	—	—	—	—	—	—	—	—	—
NOT USED	2,5	—	—	—	2,5	2,4,5	—	—	—	—	—
ISOLATE	—	—	—	—	—	—	—	—	2,5	—	—
SHORT	—	—	—	—	—	—	—	—	—	2,5	—
DEMO BOARD	TB-94 (50Ω) TB-221 (75Ω)	TB-50	TB-155(RPS) TB-100(LRPS)	TB-12	TB-48 (50Ω) TB-243 (75Ω)	TB-105	TB-37	TB-169	TB-09 (75Ω) TB-208 (50Ω)	TB-124 (75Ω) TB-232 (50Ω)	TB-86

⁽¹⁾ pins 2&5 must be connected together on PC board.

⁽²⁾ pins 2&5 must be connected together on PC board and grounded via 1.5pF capacitors.

A resistor must be placed between pins 3&4.

⁽³⁾ A 475 ohm resistor must be placed between pins 3&4.



The Design Engineers Search Engine
Provides Actual Data Instantly
At: <http://www.minicircuits.com>

In Stock... Immediate Delivery
For Custom Versions Of Standard Models
Consult Our Applications Dept.

