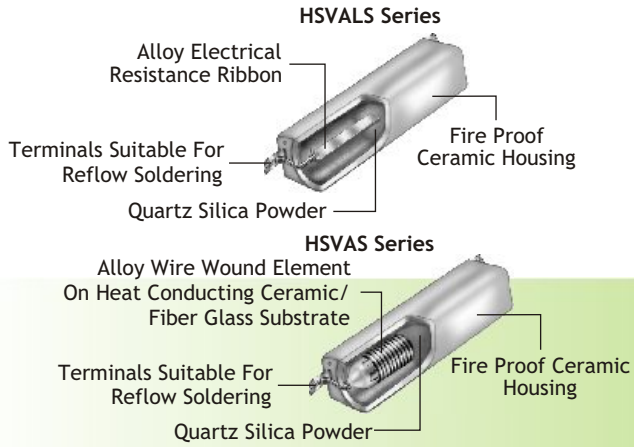


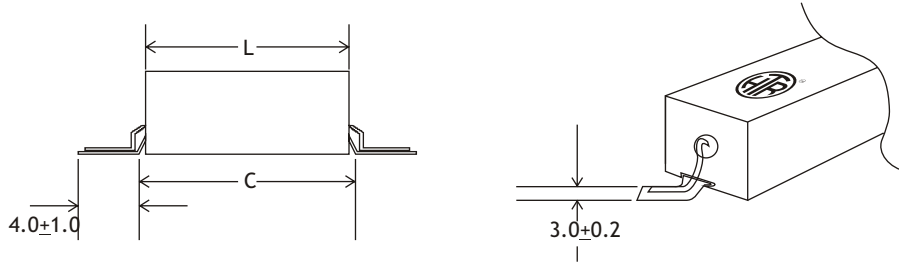


HSVAS/ HSVALS SERIES

Surface Mount
Wire Wound/Current Sense Resistors



PHYSICAL CONFIGURATION

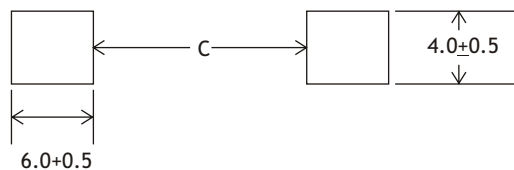


HTR SERIES	HTR TYPE	POWER RATING AT 70°C	DIMENSIONS (mm)		RESISTANCE RANGE		TYPICAL WT. PC. (gms)
			L (±1.5)	C (±1.5)	min	max	
HSVAS	SV4AS	4W	20.0	20.5	R10	11K	2.5
HSVAS	SV5AS	5W	25.0	25.5	R10	16K	3.0
HSVALS	SV4ALS	4W	20.0	20.5	R003	R051	2.5
HSVALS	SV5ALS	5W	25.0	25.5	R004	R068	3.0

SV4ALS & SV5ALS :- Resistance value must be checked using 4½ digit micro-ohm meter with four wire system and insulated clips and the designer of the pad layout might prefer to split the pad for four wire checking.

MOUNTING / ASSEMBLY DATA

For the guidance of the Design Engineer, our applications laboratory has given the recommended pad size and geometry which is shown below :-





ELECTRICAL AND ENVIRONMENTAL CHARACTERISTICS / DATA

Test	Performance Requirements
Resistance tolerance	± 10% [K]; ± 5% [J]; ±3% [H]; ±2% [G]; ±1% [F]
Rated ambient temperature	at 70 °C full power dissipation derated linearly to zero at 275 °C
Insulation resistance	> 1000 Mega Ohms
Short time overload	5 times rated wattage for 5 sec.
Di-electric withstanding voltage	500 Volts r.m.s.
Ambient operating temperature range	-40 °C to + 155 °C
Temperature co-efficient of resistance	< + 250ppm / °C (Average). For very low resistance values, TCR will increase.

TYPICAL APPLICATIONS

These devices have been introduced to answer the increased demand for power resistors which can be surface mounted.

The HSVAS Series caters to those who require a normal wire wound with surface mounting and the HSVALS series caters to those who require a shunt / current sense device which is surface mounted.

Note: Due to recent technological advances, the ceramic cases used may be steatite ceramic or cordierite ceramic or high alumina ceramic depending on the nature of the application. Hence the ceramic cases may be off-white or variations of brown and variations of grey; colours which are inherent to these ceramic materials.

ORDERING INFORMATION

