

## KBU10005 thru KBU1004

## Single Phase Silicon Bridge Rectifier

 $V_{RRM} = 50 \text{ V} - 400 \text{ V}$  $I_{O} = 10 \text{ A}$ 

#### **Features**

- Plastic material used carries Underwriters Laboratory Flammability Classification 94V-0
- · Ideal for printed circuit boards
- · High forward surge current capability
- High temperature soldering guaranteed: 250°C/ 10 seconds, 0.375 (9.5mm) lead length, 5 lbs. (2.3 kg) tension
- $\bullet$  Types from 50 V up to 400 V  $V_{RRM}$
- Not ESD Sensitive









#### **Mechanical Data**

Case: Molded plastic body

Terminals: Plated leads, solderable per MIL-STD-750,

Method 2026

Mounting position: Any

Mounting torque: 5 inch-lbs max Weight: 0.268 ounces, 7.6 grams

### Maximum ratings at Tc = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions	KBU10005	KBU1001	KBU1002	KBU1004	Unit
Repetitive peak reverse volt	age V <sub>RRM</sub>		50	100	200	400	V
RMS reverse voltage	$V_{RMS}$		35	70	140	280	V
DC blocking voltage	$V_{DC}$		50	100	200	400	V
Operating temperature	Tj		-55 to 150	-55 to 150	-55 to 150	-55 to 150	°C
Storage temperature	$T_{stg}$		-55 to 150	-55 to 150	-55 to 150	-55 to 150	°C

### Electrical characteristics at Tc = 25 °C, unless otherwise specified

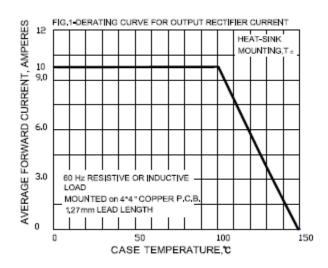
Single phase, half sine wave, 60 Hz, resistive or inductive load

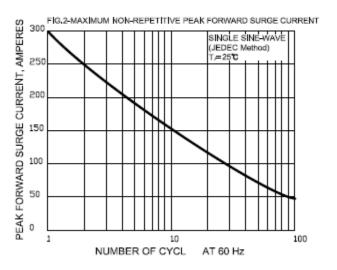
For capacitive load derate current by 20%

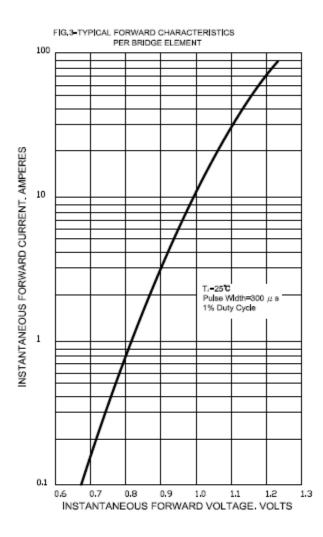
Parameter	Symbol	Conditions	KBU10005	KBU1001	KBU1002	KBU1004	Unit
Maximum average forward rectified current	Io	T <sub>c</sub> = 100 °C	10	10	10	10	Α
Peak forward surge current	I <sub>FSM</sub>	$t_p = 8.3 \text{ ms}, \text{ half sine}$	300	300	300	300	Α
Maximum instantaneous forward voltage drop per leg	V <sub>F</sub>	I <sub>F</sub> = 10 A	1.05	1.05	1.05	1.05	V
Maximum DC reverse current at rated DC blocking voltage	I <sub>R</sub>	T <sub>a</sub> = 25 °C T <sub>a</sub> = 100 °C	10 500	10 500	10 500	10 500	μΑ

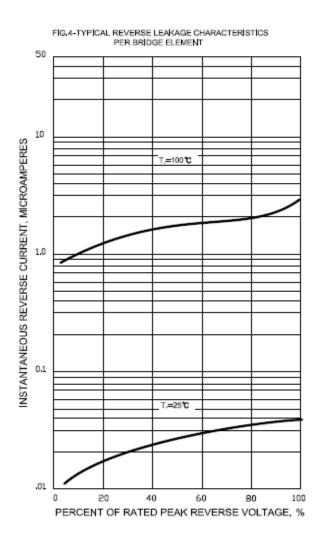


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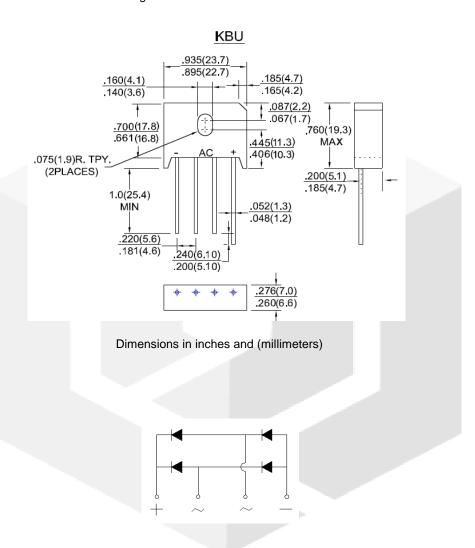






### Package dimensions and terminal configuration

Product is marked with part number and terminal configuration.



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GeneSiC Semiconductor: KBU10005